

DS14C238

Single Supply TIA/EIA-232 4 x 4 Driver/Receiver

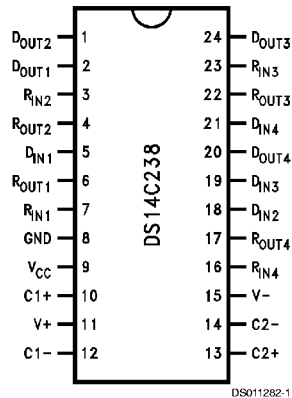
General Description

The DS14C238 is a four driver, four receiver device which conforms to the TIA/EIA-232-E standard and CCITT V.28 recommendations. This device eliminates $\pm 12V$ supplies by employing an internal DC-DC converter to generate the necessary output levels from a single +5V supply. Driver slew rate control and receiver noise filtering have also been internalized to eliminate the need for external slew rate control and noise filtering capacitors.

Features

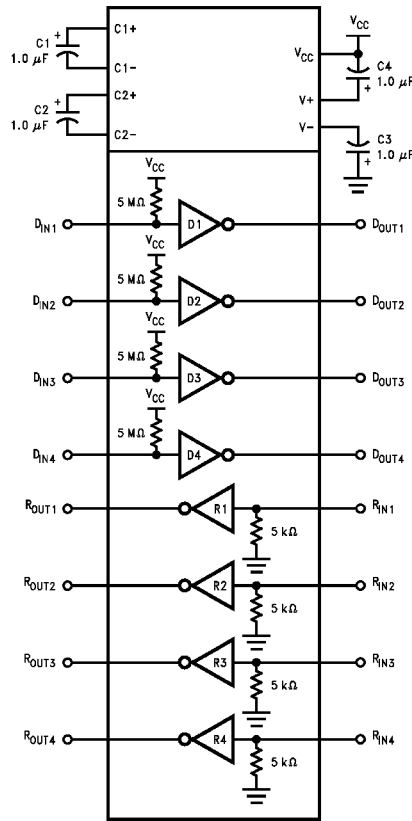
- Conforms to TIA/EIA-232-E and CCITT V.28
- Internal DC-DC converter
- Operates with single +5V supply
- Low power requirement— I_{CC} 10 mA max
- Internal driver slew rate control
- Receiver noise filtering
- Operates above 120 kbits/sec
- Direct replacement for MAX238

Connection Diagram



Order Number DS14C238WM
See NS Package Number M24B

Functional Diagram



Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage (V_{CC})	−0.3V to +6V
V+ Pin	(V_{CC} −0.3V) to +15V
V− Pin	+0.3V to −15V
Driver Input Voltage	−0.3V to (V_{CC} +0.3V)
Driver Output Voltage	(V+ +0.3V) to (V− −0.3V)
Receiver Input Voltage	±30V
Receiver Output Voltage	−0.3V to (V_{CC} +0.3V)
Junction Temperature	+150°C
Maximum Package Power Dissipation @ +25°C (Note 6)	
WM Package	1400 mW
Storage Temp. Range	−65°C to +150°C

Lead Temp.

(Soldering, 4 Seconds)

+260°C

Short Circuit Duration
(D_{OUT})

Continuous

ESD Rating

(HBM, 1.5 k Ω , 100 pF)

≥ 2.0 kV

Recommended Operating Conditions

	Min	Max	Units
Supply Voltage, V_{CC}	4.5	5.5	V
Operating Free Air Temp. (T_A)			°C
DS14C238	0	+70	

Electrical Characteristics (Note 2)

Over recommended operating conditions, unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Units	
DEVICE CHARACTERISTICS							
V+	Positive Power Supply	R _L = 3 kΩ, C1–C4 = 1.0 μF, D _{IN} = 0.8V		9.0		V	
V–	Negative Power Supply	R _L = 3 kΩ, C1–C4 = 1.0 μF, D _{IN} = 2.0V		–8.0		V	
I _{CC}	Supply Current (V _{CC})	No Load		7.0	10	mA	
DRIVER CHARACTERISTICS							
V _{IH}	High Level Input Voltage			2.0	V _{CC}	V	
V _{IL}	Low Level Input Voltage			GND	0.8	V	
I _{IH}	High Level Input Current	V _{IN} ≥ 2.0V		–10	+10	μA	
I _{IL}	Low Level Input Current	V _{IN} ≤ 0.8V		–10	+10	μA	
V _{OH}	High Level Output Voltage	R _L = 3 kΩ		5.0	7.4	V	
V _{OL}	Low Level Output Voltage				–6.3	–5.0	V
I _{OS+}	Output High Short Circuit Current	V _O = 0V, V _{IN} = 0.8V	(Note 3)	–30	–15	–5.0	mA
I _{OS–}	Output Low Short Circuit Current	V _O = 0V, V _{IN} = 2.0V		5.0	12	30	mA
R _O	Output Resistance	–2V ≤ V _O ≤ +2V, V _{CC} = GND = 0V		300		Ω	
RECEIVER CHARACTERISTICS							
V _{TH}	Input High Threshold Voltage				1.9	2.4	V
V _{TL}	Input Low Threshold Voltage			0.8	1.5		V
V _{HY}	Hysteresis			0.2	0.4	1.0	V
R _{IN}	Input Resistance			3.0	4.5	7.0	kΩ
I _{IN}	Input Current	V _{IN} = +15V		2.14	3.8	5.0	mA
		V _{IN} = +3V		0.43	0.6	+1.0	mA
		V _{IN} = –3V		–1.0	–0.6	–0.43	mA
		V _{IN} = –15V		–5.0	–3.8	–2.14	mA
V _{OH}	High Level Output Voltage	V _{IN} = –3V, I _O = –3.2 mA		3.5	4.5		V
		V _{IN} = –3V, I _O = –20 μA		4.0	4.9		V
V _{OL}	Low Level Output Voltage	V _{IN} = +3V, I _O = +2.0 mA			0.25	0.4	V

Switching Characteristics (Note 4)

Over recommended operating conditions, unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Units
DRIVER CHARACTERISTICS						
t_{PLH}	Propagation Delay LOW to HIGH	$R_L = 3\text{ k}\Omega$		0.7	4.0	μs
t_{PHL}	Propagation Delay HIGH to LOW	$C_L = 50\text{ pF}$		0.6	4.0	μs
t_{sk}	Skew $ t_{PLH} - t_{PHL} $	(Figures 1, 2)		0.1	1.0	μs
SR1	Output Slew Rate	$R_L = 3\text{ k}\Omega$ to $7\text{ k}\Omega$, $C_L = 50\text{ pF}$	4.0	15	30	$\text{V}/\mu\text{s}$
SR2	Output Slew Rate	$R_L = 3\text{ k}\Omega$, $C_L = 2500\text{ pF}$	3.0	5.0		$\text{V}/\mu\text{s}$
RECEIVER CHARACTERISTICS						
t_{PLH}	Propagation Delay LOW to HIGH	Input Pulse Width $> 10\text{ }\mu\text{s}$		2.0	6.5	μs
t_{PHL}	Propagation Delay HIGH to LOW	$C_L = 50\text{ pF}$		2.8	6.5	μs
t_{sk}	Skew $ t_{PLH} - t_{PHL} $	(Figures 3, 4)		0.8	2.0	μs
t_{NW}	Noise Pulse Width Rejected	(Figures 3, 4)		2.5	1.0	μs

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the devices should be operated at these limits. The tables of "Electrical Characteristics" specify conditions for device operation.

Note 2: Current into device pins is defined as positive. Current out of device pins is defined as negative. All voltages are referenced to ground unless otherwise specified.

Note 3: I_{OS+} and I_{OS-} values are for one output at a time. If more than one output is shorted simultaneously, the device power dissipation may be exceeded.

Note 4: Receiver AC input waveform for test purposes: $t_r = t_f = 200\text{ ns}$, $V_{IH} = 3\text{V}$, $V_{IL} = -3\text{V}$, $f = 64\text{ kHz}$ (128 kbits/sec). Driver AC input waveform for test purposes: $t_r = t_f \leq 10\text{ ns}$, $V_{IH} = 3\text{V}$, $V_{IL} = 0\text{V}$, $f = 64\text{ kHz}$ (128 kbits/sec).

Note 5: All typicals are given for $V_{CC} = 5.0\text{V}$ and $T_A = +25^\circ\text{C}$.

Note 6: Ratings apply to ambient temperature at $+25^\circ\text{C}$. Above this temperature derate: WM package $13.5\text{ mW}/^\circ\text{C}$.

Parameter Measurement Information

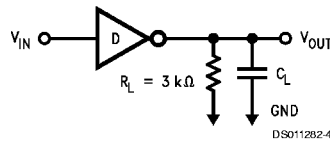


FIGURE 1. Driver Load Circuit

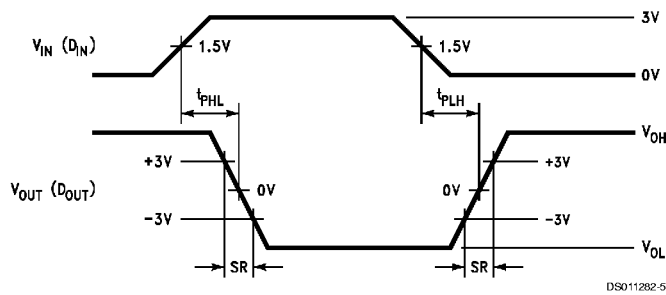


FIGURE 2. Driver Switching Waveform

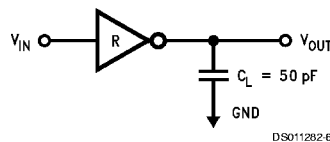
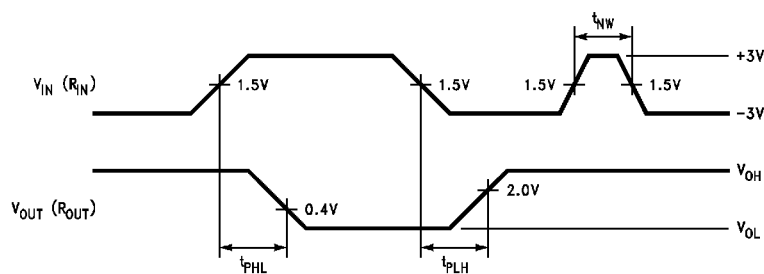


FIGURE 3. Receiver Load Circuit

Parameter Measurement Information (Continued)



DS011282-7

FIGURE 4. Receiver Propagation Delays and Noise Rejection

Pin Descriptions

V_{CC} (pin 9)— Power supply pin for the device, +5V (±10%).

V₊ (pin 11)— Positive supply for TIA/EIA-232-E drivers. Recommended external capacitor: C4 = 1.0 μF (6.3V). This supply is not intended to be loaded externally.

V₋ (pin 15)— Negative supply for TIA/EIA-232-E drivers. Recommended external capacitor: C3 = 1.0 μF (16V). This supply is not intended to be loaded externally.

C1+, C1- (pins 10 and 12)— External capacitor connection pins. Recommended capacitor – 1.0 μF (6.3V).

C2+, C2- (pins 13 and 14)— External Capacitor connection pins. Recommended capacitor – 1.0 μF (16V).

D_{IN} 1–4 (pins 5, 18, 19, and 21)— Driver input pins are TTL/CMOS compatible. Inputs of unused drivers may be left

open, an internal pull-up resistor (500 kΩ minimum, typically 5 MΩ) pulls input to V_{CC}. Output will be LOW for open inputs.

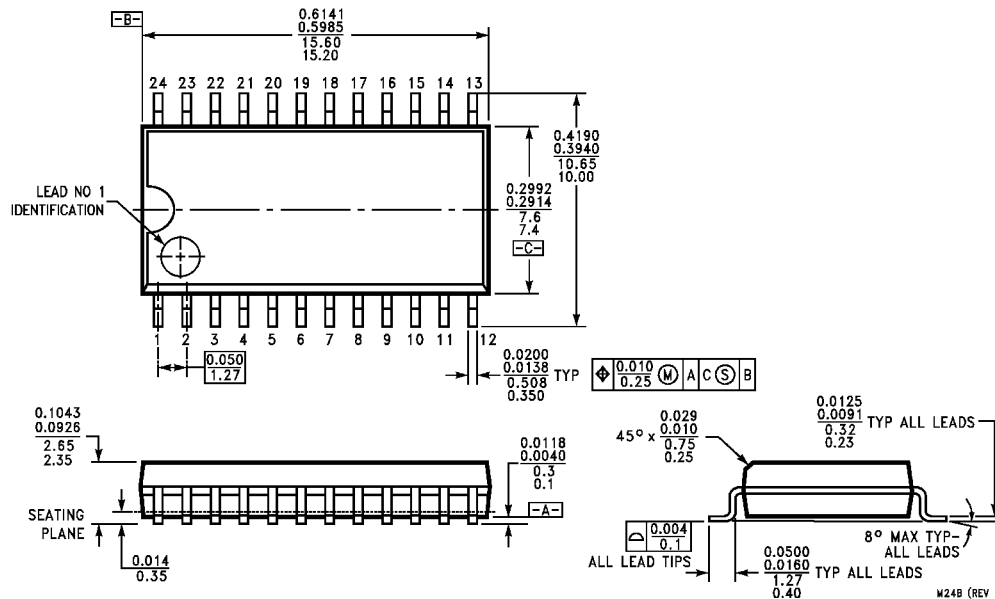
D_{OUT} 1–4 (pins 2, 1, 24, and 20)— Driver output pins conform to TIA/EIA-232-E levels.

R_{IN} 1–4 (pins 3, 7, 23, and 16)— Receiver input pins accept TIA/EIA-232-E input voltages (±15V). Receivers feature a noise filter and guaranteed hysteresis of 200 mV. Unused receiver input pins may be left open. Internal input resistor (5 kΩ) pulls input LOW, providing a failsafe HIGH output.

R_{OUT} 1–4 (pins 4, 6, 22, and 17)— Receiver output pins are TTL/CMOS compatible. Receiver output HIGH voltage is specified for both CMOS and TTL load conditions.

GND (pin 8)— Ground Pin.

Physical Dimensions inches (millimeters) unless otherwise noted



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