

# DATA SHEET

## **NE/SA/SE5532/5532A**

Internally-compensated dual low noise  
operational amplifier

Product specification

1997 Sept 29

IC11 Data Handbook

# Internally-compensated dual low noise operational amplifier

## NE/SA/SE5532/5532A

### DESCRIPTION

The 5532 is a dual high-performance low noise operational amplifier. Compared to most of the standard operational amplifiers, such as the 1458, it shows better noise performance, improved output drive capability and considerably higher small-signal and power bandwidths.

This makes the device especially suitable for application in high-quality and professional audio equipment, instrumentation and control circuits, and telephone channel amplifiers. The op amp is internally compensated for gains equal to one. If very low noise is of prime importance, it is recommended that the 5532A version be used because it has guaranteed noise voltage specifications.

### FEATURES

- Small-signal bandwidth: 10MHz
- Output drive capability: 600Ω, 10V<sub>RMS</sub>
- Input noise voltage: 5nV/√Hz (typical)
- DC voltage gain: 50000
- AC voltage gain: 2200 at 10kHz
- Power bandwidth: 140kHz
- Slew rate: 9V/μs
- Large supply voltage range: ±3 to ±20V
- Compensated for unity gain

### PIN CONFIGURATIONS

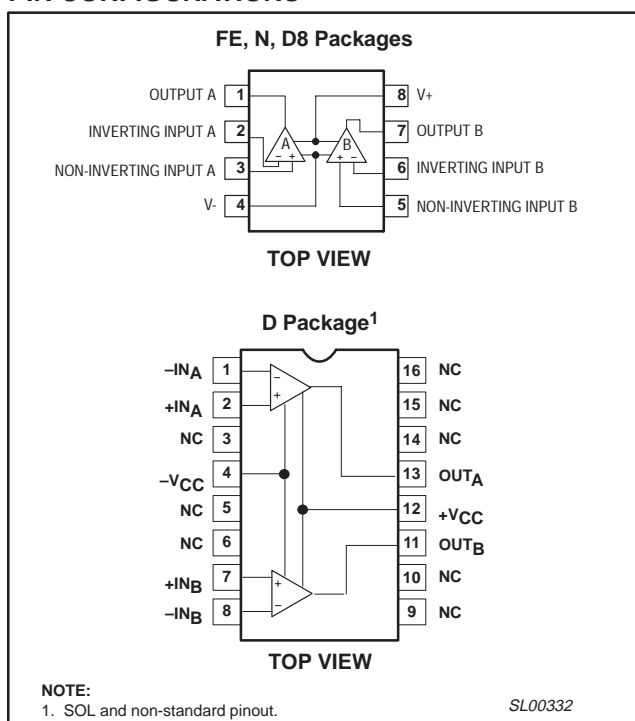


Figure 1. Pin Configurations

### ORDERING INFORMATION

| DESCRIPTION                                      | TEMPERATURE RANGE | ORDER CODE | DWG #    |
|--|-------------------|------------|----------|
| 8-Pin Plastic Dual In-Line Package (DIP)         | 0 to 70°C         | NE5532N    | SOT97-1  |
| 8-Pin Plastic Dual In-Line Package (DIP)         | -40°C to +85°C    | SA5532N    | SOT97-1  |
| 8-Pin Plastic Dual In-Line Package (DIP)         | -40°C to +85°C    | SA5532AN   | SOT97-1  |
| 8-Pin Ceramic Dual In-Line Package (CERDIP)      | 0 to 70°C         | NE5532FE   | 0580A    |
| 8-Pin Plastic Dual In-Line Package (DIP)         | 0 to 70°C         | NE5532AN   | SOT97-1  |
| 8-Pin Ceramic Dual In-Line Package (CERDIP)      | 0 to 70°C         | NE5532AF   | 0580A    |
| 8-Pin Ceramic Dual In-Line Package (CERDIP)      | -55°C to +125°C   | SE5532FE   | 0580A    |
| 8-Pin Ceramic Dual In-Line Package (CERDIP)      | -55°C to +125°C   | SE5532AF   | 0580A    |
| 8-Pin Small Outline Package (SO)                 | 0 to 70°C         | NE5532AD8  | SOT96-1  |
| 8-Pin Small Outline Package (SO)                 | -40°C to 85°C     | SA5532D8   | SOT96-1  |
| 8-Pin Small Outline Package (SO)                 | -40°C to 85°C     | SA5532AD8  | SOT96-1  |
| 8-Pin Small Outline Package (SO)                 | -55°C to +125°C   | SE5532AD8  | SOT96-1  |
| 8-Pin Small Outline Package (SO)                 | 0 to 70°C         | NE5532D8   | SOT96-1  |
| 8-Pin Small Outline Package (SO)                 | -40°C to 85°C     | SA5532D8   | SOT96-1  |
| 8-Pin Small Outline Package (SO)                 | -40°C to 85°C     | SA5532AD8  | SOT96-1  |
| 8-Pin Small Outline Package (SO)                 | -55°C to +125°C   | SE5532D8   | SOT96-1  |
| 16-Pin Plastic Small Outline Large (SOL) Package | 0 to 70°C         | NE5532D    | SOT162-1 |
| 16-Pin Plastic Dual In-Line Package (DIP)        | -55°C to +125°C   | SE5532N    | SOT38-4  |

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EQUIVALENT SCHEMATIC (EACH AMPLIFIER)

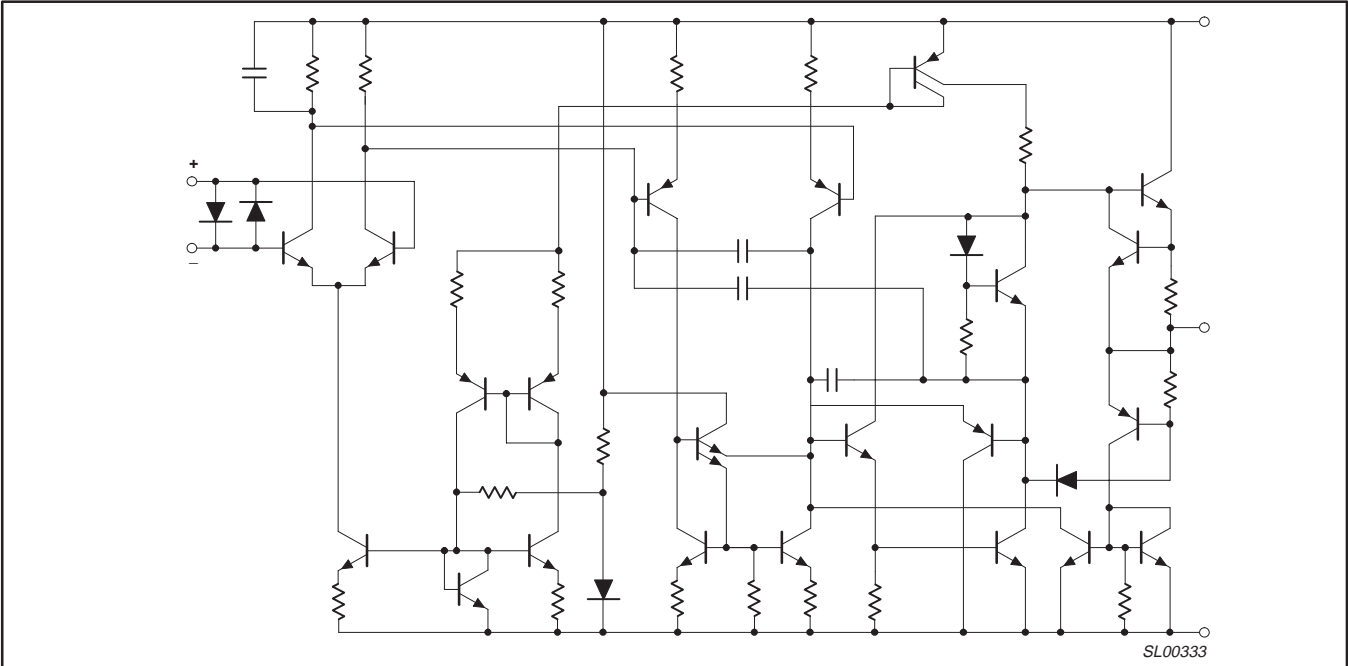


Figure 2. Equivalent Schematic (Each Amplifier)

ABSOLUTE MAXIMUM RATINGS

| SYMBOL            | PARAMETER  | RATING                               | UNIT                 |
|-------------------|--|--------------------------------------|----------------------|
| V <sub>S</sub>    | Supply voltage   | ±22                                  | V                    |
| V <sub>IN</sub>   | Input voltage  | ±V <sub>SUPPLY</sub>                 | V                    |
| V <sub>DIFF</sub> | Differential input voltage <sup>1</sup>  | ±0.5                                 | V                    |
| T <sub>A</sub>    | Operating temperature range<br>SA5532/A<br>NE5532/A<br>SE5532/A  | -40 to +85<br>0 to 70<br>-55 to +125 | °C<br>°C<br>°C       |
| T <sub>STG</sub>  | Storage temperature  | -65 to +150                          | °C                   |
| T <sub>J</sub>    | Junction temperature   | 150                                  | °C                   |
| P <sub>D</sub>    | Maximum power dissipation,<br>T <sub>A</sub> =25°C (still-air) <sup>2</sup><br>8 D8 package<br>8 N package<br>8 FE package<br>16 D package | 780<br>1200<br>1000<br>1200          | mW<br>mW<br>mW<br>mW |
| T <sub>SOLD</sub> | Lead soldering temperature (10sec max)   | 300                                  | °C                   |

NOTES:

- Diodes protect the inputs against over-voltage. Therefore, unless current-limiting resistors are used, large currents will flow if the differential input voltage exceeds 0.6V. Maximum current should be limited to ±10mA.
- Thermal resistances of the above packages are as follows:  
N package at 100°C/W  
F package at 135°C/W  
D package at 105°C/W  
D8 package at 160°C/W

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## DC ELECTRICAL CHARACTERISTICS

T<sub>A</sub>=25°C V<sub>S</sub>=±15V, unless otherwise specified. 1, 2, 3

| SYMBOL               | PARAMETER                    | TEST CONDITIONS                            | SE/5532/5532A |       |      | NE/SA/5532/5532A |       |      | UNIT  |
|----------------------|------------------------------|--|---------------|-------|------|------------------|-------|------|-------|
|                      |                              |  | Min           | Typ   | Max  | Min              | Typ   | Max  |       |
| V <sub>OS</sub>      | Offset voltage               | Over temperature                           |               | 0.5   | 2    |                  | 0.5   | 4    | mV    |
| ΔV <sub>OS</sub> /ΔT |                              |  |               | 5     | 3    |                  | 5     | 5    | mV/°C |
| I <sub>OS</sub>      | Offset current               | Over temperature                           |               |       | 100  |                  | 10    | 150  | nA    |
| ΔI <sub>OS</sub> /ΔT |                              |  |               | 200   | 200  |                  | 200   | 200  | nA/°C |
| I <sub>B</sub>       | Input current                | Over temperature                           |               | 200   | 400  |                  | 200   | 800  | nA    |
| ΔI <sub>B</sub> /ΔT  |                              |  |               | 5     | 700  |                  | 5     | 1000 | nA/°C |
| I <sub>CC</sub>      | Supply current               | Over temperature                           |               | 8     | 10.5 |                  | 8     | 16   | mA    |
|                      |                              |  |               |       | 13   |                  |       |      | mA    |
| V <sub>CM</sub>      | Common-mode input range      |  | ±12           | ±13   |      | ±12              | ±13   |      | V     |
| CMRR                 | Common-mode rejection ratio  |  | 80            | 100   |      | 70               | 100   |      | dB    |
| PSRR                 | Power supply rejection ratio |  |               | 10    | 50   |                  | 10    | 100  | μV/V  |
| A <sub>VOL</sub>     | Large-signal voltage gain    | R <sub>L</sub> ≥2kΩ, V <sub>O</sub> =±10V  | 50            | 100   |      | 25               | 100   |      | V/mV  |
|                      |                              | Over temperature                           | 25            |       |      | 15               |       |      | V/mV  |
|                      |                              | R <sub>L</sub> ≥600Ω, V <sub>O</sub> =±10V | 40            | 50    |      | 15               | 50    |      | V/mV  |
|                      |                              | Over temperature                           | 20            |       |      | 10               |       |      | V/mV  |
| V <sub>OUT</sub>     | Output swing                 | R <sub>L</sub> ≥600Ω                       | ±12           | ±13   |      | ±12              | ±13   |      | V     |
|                      |                              | Over temperature                           | ±10           | ±12   |      | ±10              | ±12   |      |       |
|                      |                              | R <sub>L</sub> ≥600Ω, V <sub>S</sub> =±18V | ±15           | ±16   |      | ±15              | ±16   |      |       |
|                      |                              | Over temperature                           | ±12           | ±14   |      | ±12              | ±14   |      |       |
|                      |                              | R <sub>L</sub> ≥2kΩ                        | ±13           | ±13.5 |      | ±13              | ±13.5 |      |       |
|                      |                              | Over temperature                           | ±12           | ±12.5 |      | ±10              | ±12.5 |      |       |
| R <sub>IN</sub>      | Input resistance             |  | 30            | 300   |      | 30               | 300   |      | kΩ    |
| I <sub>SC</sub>      | Output short circuit current |  | 10            | 38    | 60   | 10               | 38    | 60   | mA    |

### NOTES:

- Diodes protect the inputs against overvoltage. Therefore, unless current-limiting resistors are used, large currents will flow if the differential input voltage exceeds 0.6V. Maximum current should be limited to ±10mA.
- For operation at elevated temperature, derate packages based on the package thermal resistance.
- Output may be shorted to ground at V<sub>S</sub>=±15V, T<sub>A</sub>=25°C Temperature and/or supply voltages must be limited to ensure dissipation rating is not exceeded.

## AC ELECTRICAL CHARACTERISTICS

T<sub>A</sub>=25°C V<sub>S</sub>=±15V, unless otherwise specified.

| SYMBOL           | PARAMETER              | TEST CONDITIONS  | NE/SA/SE5532/5532A |     |     | UNIT |
|------------------|------------------------|--|--------------------|-----|-----|------|
|                  |                        |  | Min                | Typ | Max |      |
| R <sub>OUT</sub> | Output resistance      | A <sub>V</sub> =30dB Closed-loop<br>f=10kHz, R <sub>L</sub> =600Ω  |                    | 0.3 |     | Ω    |
|                  | Overshoot              | Voltage-follower<br>V <sub>IN</sub> =100mV <sub>P-P</sub><br>C <sub>L</sub> =100pF, R <sub>L</sub> =600Ω |                    | 10  |     | %    |
| A <sub>V</sub>   | Gain                   | f=10kHz  |                    | 2.2 |     | V/mV |
| GBW              | Gain bandwidth product | C <sub>L</sub> =100pF, R <sub>L</sub> =600Ω  |                    | 10  |     | MHz  |
| SR               | Slew rate              |  |                    | 9   |     | V/μs |
|                  | Power bandwidth        | V <sub>OUT</sub> =±10V<br>V <sub>OUT</sub> =±14V, R <sub>L</sub> =600Ω,<br>V <sub>CC</sub> =±18V         |                    | 140 |     | kHz  |
|                  |                        |  |                    | 100 |     | kHz  |

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ELECTRICAL CHARACTERISTICS

T<sub>A</sub>=25°C V<sub>S</sub>=±15V, unless otherwise specified.

| SYMBOL             | PARAMETER           | TEST CONDITIONS                              | NE/SE5532 |            |     | NE/SA/SE5532A |            |         | UNIT   |
|--------------------|---------------------|--|-----------|------------|-----|---------------|------------|---------|--|
|                    |                     |  | Min       | Typ        | Max | Min           | Typ        | Max     |  |
| V <sub>NOISE</sub> | Input noise voltage | f <sub>O</sub> =30Hz<br>f <sub>O</sub> =1kHz |           | 8<br>5     |     |               | 8<br>5     | 12<br>6 | nV/ $\sqrt{\text{Hz}}$<br>nV/ $\sqrt{\text{Hz}}$ |
| I <sub>NOISE</sub> | Input noise current | f <sub>O</sub> =30Hz<br>f <sub>O</sub> =1kHz |           | 2.7<br>0.7 |     |               | 2.7<br>0.7 |         | pA/ $\sqrt{\text{Hz}}$<br>pA/ $\sqrt{\text{Hz}}$ |
|                    | Channel separation  | f=1kHz, R <sub>S</sub> =5k $\Omega$          |           | 110        |     |               | 110        |         | dB   |

TYPICAL PERFORMANCE CHARACTERISTICS

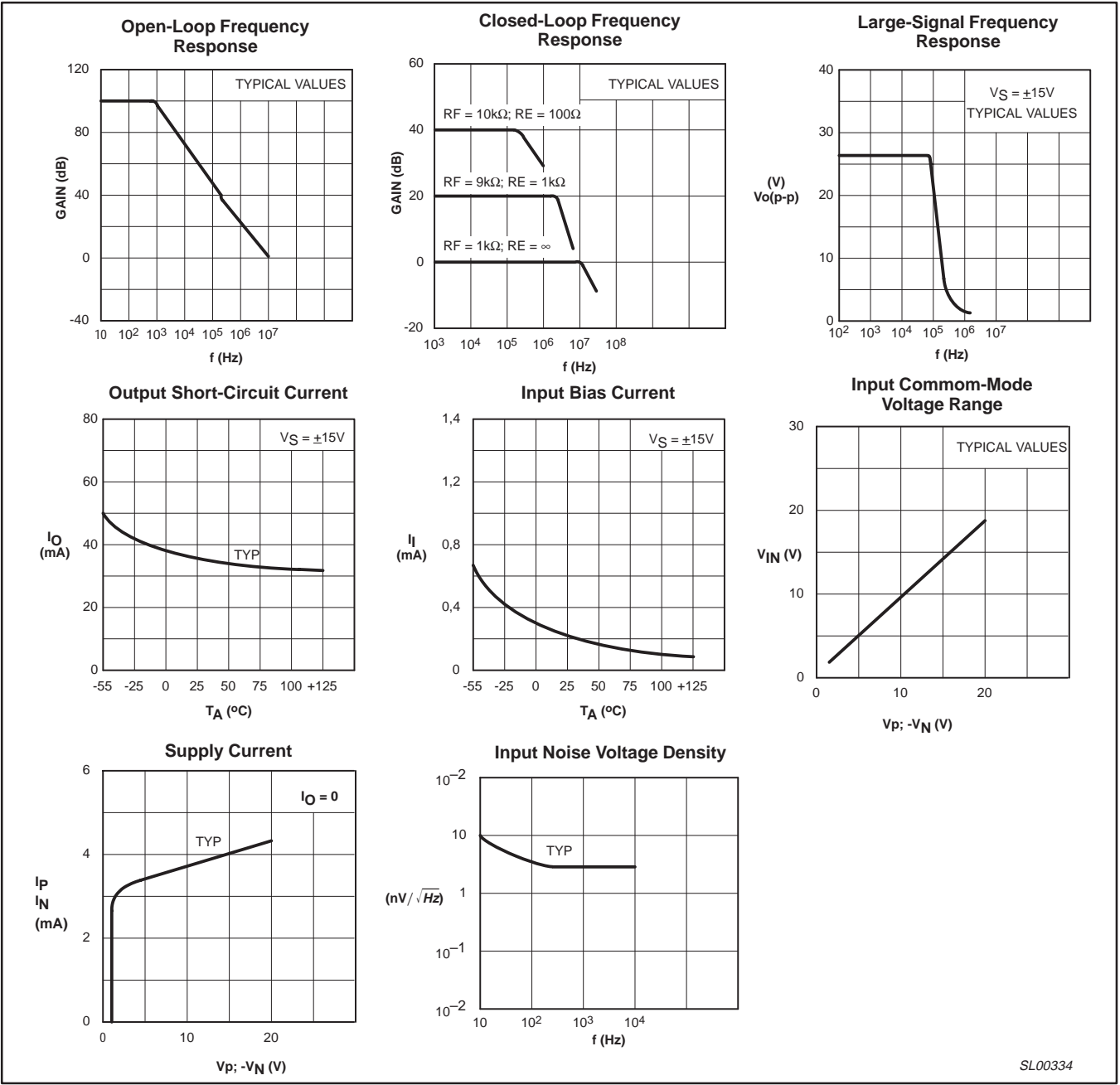


Figure 3. Typical Performance Characteristics

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## TEST CIRCUITS

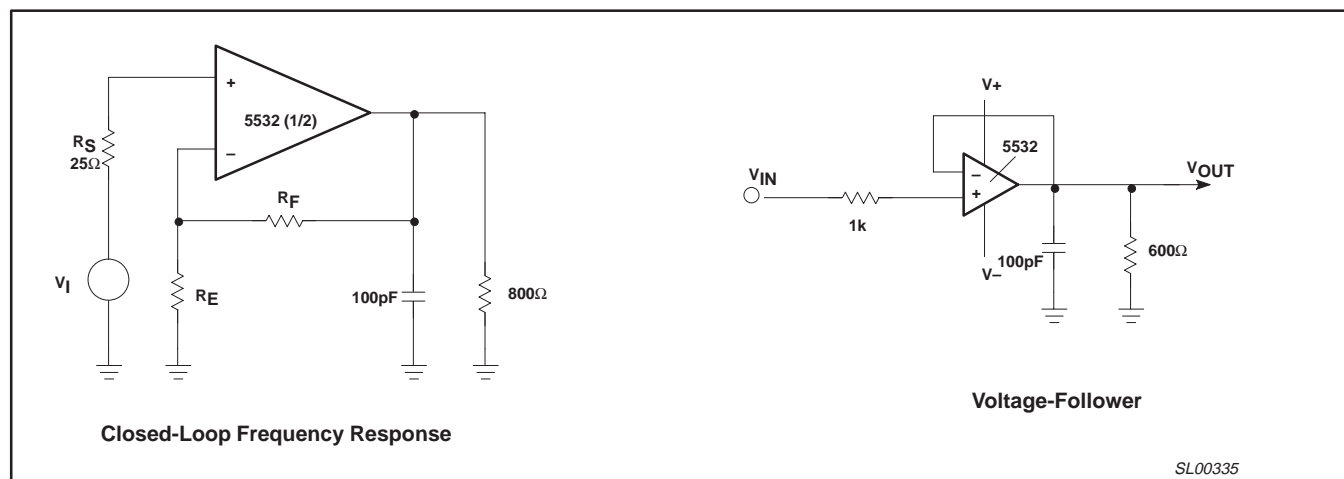


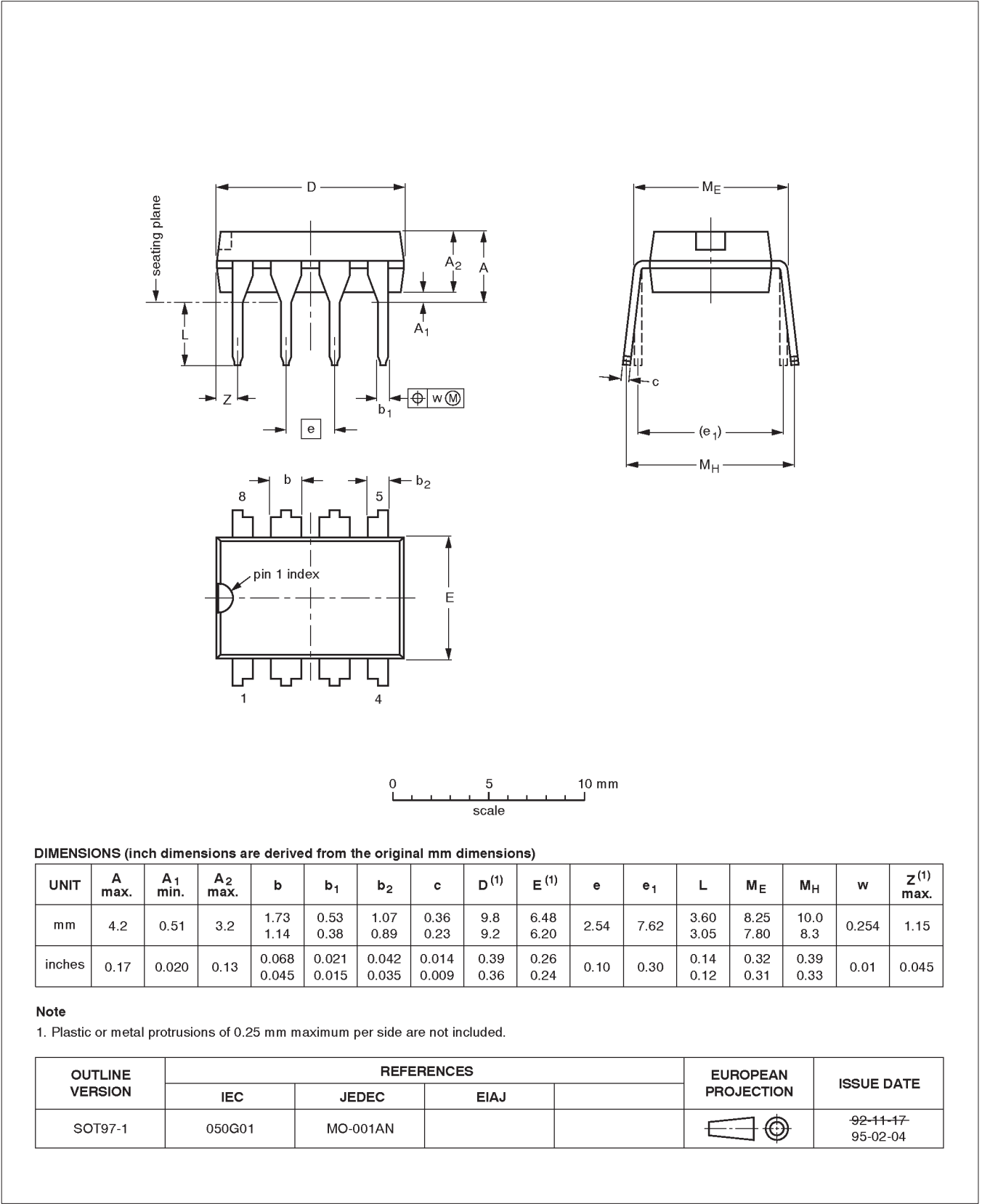
Figure 4. Test Circuits

Internally-compensated dual low noise operational amplifier

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DIP8: plastic dual in-line package; 8 leads (300 mil)

SOT97-1



Internally-compensated dual low noise operational amplifier

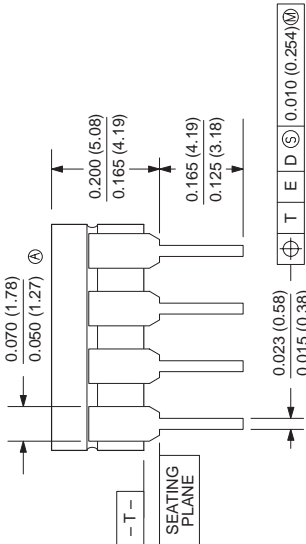
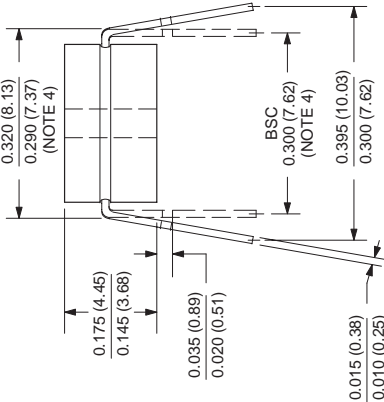
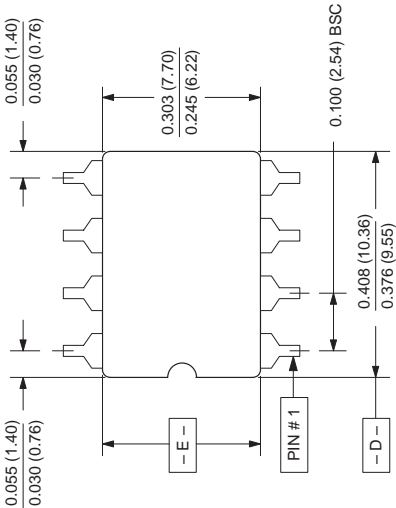
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0580A

8-PIN (300 mils wide) CERAMIC DUAL IN-LINE (F) PACKAGE

NOTES:

- 1. Controlling dimension: Inches. Millimeters are shown in parentheses.
- 2. Dimension and tolerancing per ANSI Y14. 5M-1982.
- 3. "T", "D", and "E" are reference datums on the body and include allowance for glass overrun and meniscus on the seal line, and lid to base mismatch.
- 4. These dimensions measured with the leads constrained to be perpendicular to plane T.
- 5. Pin numbers start with Pin #1 and continue counterclockwise to Pin #8 when viewed from the top.



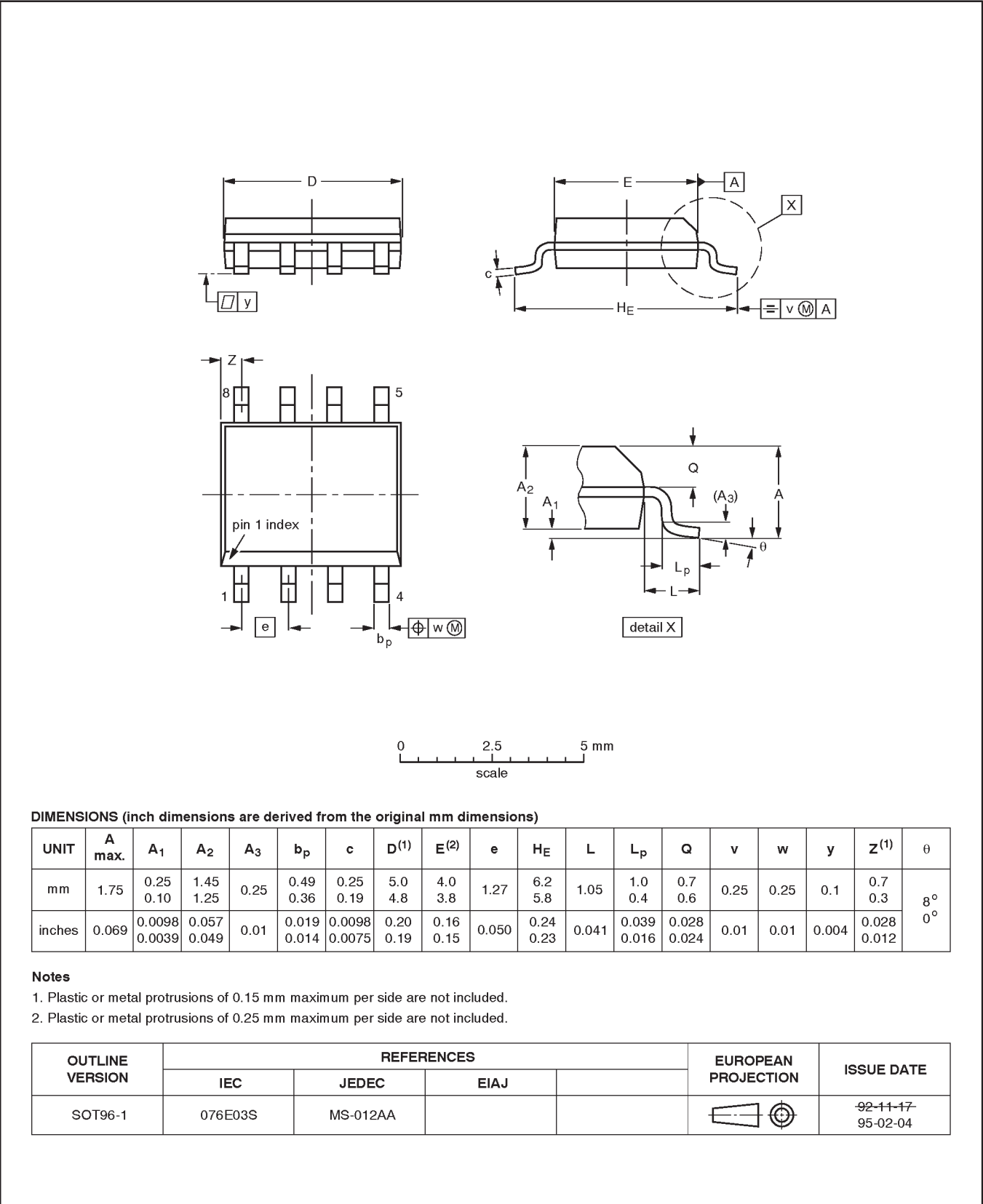


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SO8: plastic small outline package; 8 leads; body width 3.9mm

SOT96-1

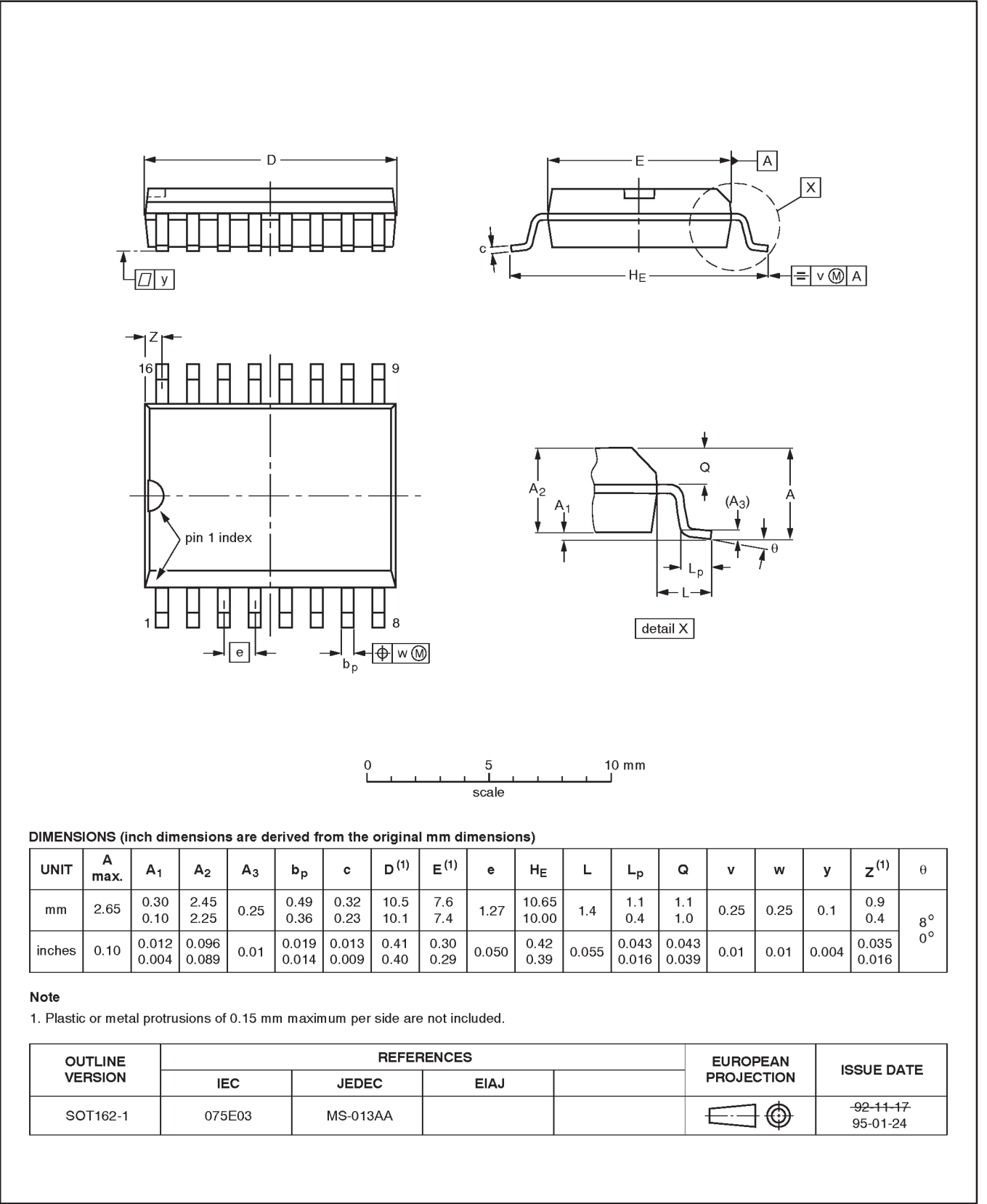


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SO16: plastic small outline package; 16 leads; body width 7.5 mm

SOT162-1

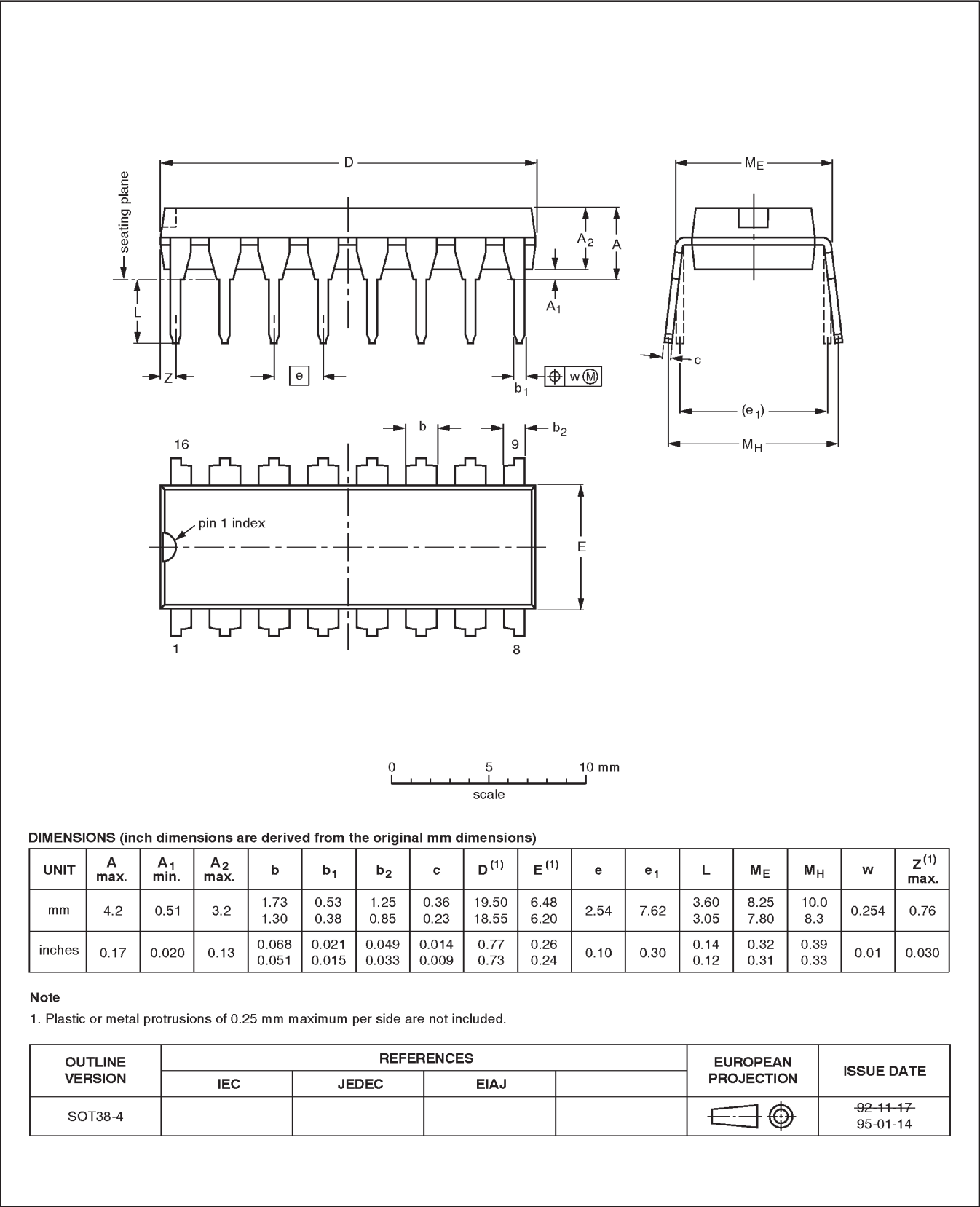


Internally-compensated dual low noise operational amplifier

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DIP16: plastic dual in-line package; 16 leads (300 mil)

SOT38-4



Internally-compensated dual low noise operational amplifier

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| DEFINITIONS               |                        |  |
|---------------------------|------------------------|--|
| Data Sheet Identification | Product Status         | Definition   |
| Objective Specification   | Formative or in Design | This data sheet contains the design target or goal specifications for product development. Specifications may change in any manner without notice.   |
| Preliminary Specification | Preproduction Product  | This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product. |
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