

Picoamp Input Current,
 Microvolt Offset,
 Low Noise Op Amp

FEATURES

- **Guaranteed Bias Current**
 25°C 100pA max.
 -55°C to 125°C 600pA max.
- **Guaranteed Offset Voltage** 120μV max.
- **Guaranteed Drift** 1.5μV/°C max.
- **Low Noise, 0.1Hz to 10Hz** 0.5μVp-p
- **Guaranteed Low Supply Current** 600μA max.
- **Guaranteed CMRR** 114 dB min.
- **Guaranteed PSRR** 114 db min.
- **Guaranteed Voltage Gain with 5mA load current**

APPLICATIONS

- Precision instrumentation
- Charge integrators
- Wide dynamic range logarithmic amplifiers
- Light meters
- Low frequency active filters
- Standard cell buffers
- Thermocouple amplifiers

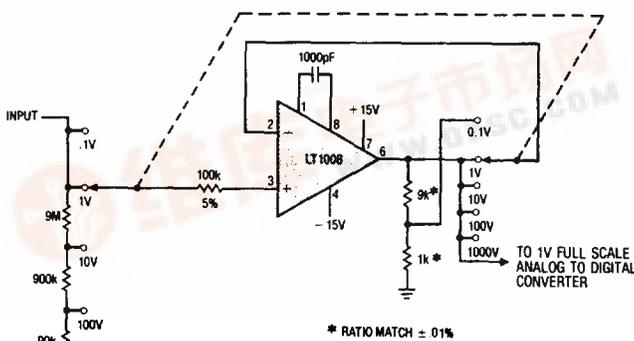
DESCRIPTION

The LT1008 is a universal precision operational amplifier which can be used in practically all precision applications. The LT1008 combines for the first time picoampere bias currents (which are maintained over the full -55°C to 125°C temperature range) microvolt offset voltage (and low drift with time and temperature), low voltage and current noise, and low power dissipation. Extremely high common-mode and power supply rejection ratios, and the ability to deliver 5mA load current with high voltage gain round out the LT1008's superb precision specifications.

The all around excellence of the LT1008 eliminates the necessity of the time consuming error analysis procedure of precision system design in many applications; the LT1008 can be stocked as the universal precision op amp.

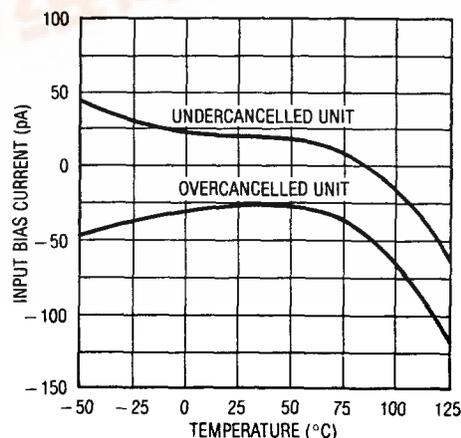
The LT1008 is externally compensated with a single capacitor for additional flexibility in shaping the frequency response of the amplifier. It plugs into and upgrades all standard LM108A/308A applications. For an internally compensated version with even lower offset voltage but otherwise similar performance see the LT1012.

Input Amplifier for 4½ Digit Voltmeter



This application requires low bias current and offset voltage, low noise, and low drift with time and temperature.

Input Bias Current vs Temperature



LT1008

ABSOLUTE MAXIMUM RATING

Supply Voltage	±20V
Differential Input Current (Note 1)	±10mA
Input Voltage	±20V
Output Short Circuit Duration	Indefinite
Operating Temperature Range	
LT1008M	−55°C to 125°C
LT1008C	0°C to 70°C
Storage Temperature Range	
All Devices	−65°C to 150°C
Lead Temperature (Soldering, 10 sec.)	300°C

PACKAGE/ORDER INFORMATION

<p>METAL CAN H PACKAGE</p>	ORDER PART NO.
	LT1008MH LT1008CH
<p>PLASTIC DIP N8 PACKAGE</p>	LT1008CN8

ELECTRICAL CHARACTERISTICS $V_S = \pm 15V$, $V_{CM} = 0V$, $T_A = 25^\circ C$, unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS	LT1008M			LT1008C			UNITS
			MIN	TYP	MAX	MIN	TYP	MAX	
V_{OS}	Input Offset Voltage	Note 2		30	120		30	120	μV
	Long Term Input Offset Voltage Stability			40	180		40	180	μV
I_{OS}	Input Offset Current	Note 2		0.3			0.3		$\mu V/month$
				30	100		30	100	pA
I_B	Input Bias Current	Note 2		40	150		40	150	pA
				±30	±100		±30	±100	pA
e_n	Input Noise Voltage	0.1Hz to 10Hz		±40	±150		±40	±150	pA
	Input Noise Voltage Density		$f_0 = 10Hz$ (Note 3)		0.5			0.5	
i_n	Input Noise Current Density	$f_0 = 10Hz$		17	30		17	30	nV/\sqrt{Hz}
			$f_0 = 1000Hz$ (Note 4)		14	22		14	22
A_{VOL}	Large Signal Voltage Gain	$V_{OUT} = \pm 12V$, $R_L \geq 10k\Omega$ $V_{OUT} = \pm 10V$, $R_L \geq 2k\Omega$		20	2000		200	2000	V/mV
				114	132		114	132	dB
CMRR	Common Mode Rejection Ratio	$V_{CM} = \pm 13.5V$		114	132		114	132	dB
	Power Supply Rejection Ratio		$V_S = \pm 2V$ to $\pm 20V$		114	132		114	132
V_{OUT}	Input Voltage Range				±13.5	±14.0		±13.5	±14.0
	Output Voltage Swing	$R_L = 10k\Omega$		±13	±14		±13	±14	V
I_S	Slew Rate	$C_f = 30pF$		0.1	0.2		0.1	0.2	V/ μsec
	Supply Current		Note 2		380	600		380	600

ELECTRICAL CHARACTERISTICS $V_S = \pm 15V, V_{CM} = 0V, 0^\circ C \leq T_A \leq 70^\circ C$ for the LT1008C and $-55^\circ C \leq T_A \leq 125^\circ C$ for the LT1008M, unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS	LT1008M			LT1008C			UNITS
			MIN	TYP	MAX	MIN	TYP	MAX	
V_{OS}	Input Offset Voltage	Note 2	●	50	250		40	180	μV
			●	60	320		50	250	μV
	Average Temperature Coefficient of Input Offset Voltage		●	0.2	1.5		0.2	1.5	$\mu V/^\circ C$
I_{OS}	Input Offset Current	Note 2	●	60	250		40	180	pA
			●	80	350		50	250	pA
	Average Temperature Coefficient of Input Offset Current		●	0.4	2.5		0.4	2.5	pA/°C
I_B	Input Bias Current	Note 2	●	± 80	± 600		± 40	± 180	pA
			●	± 150	± 800		± 50	± 250	pA
	Average Temperature Coefficient of Input Bias Current		●	0.6	6.0		0.4	2.5	pA/°C
A_{VOL}	Large Signal Voltage Gain	$V_{OUT} = \pm 12V, R_L \geq 10k\Omega$	●	100	1000		150	1500	V/mV
CMRR	Common Mode Rejection Ratio	$V_{CM} = \pm 13.5V$	●	108	128		110	130	dB
PSRR	Power Supply Rejection Ratio	$V_S = \pm 2.5V$ to $\pm 20V$	●	108	126		110	128	dB
			●	± 13.5			± 13.5		V
V_{OUT}	Output Voltage Swing	$R_L = 10k\Omega$	●	± 13	± 14		± 13	± 14	V
I_S	Supply Current		●	400	800		400	800	μA

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The ● denotes the specifications which apply over the full operating temperature range.

Note 1: Differential input voltages greater than 1V will cause excessive current to flow through the input protection diodes unless current limiting resistors are used.

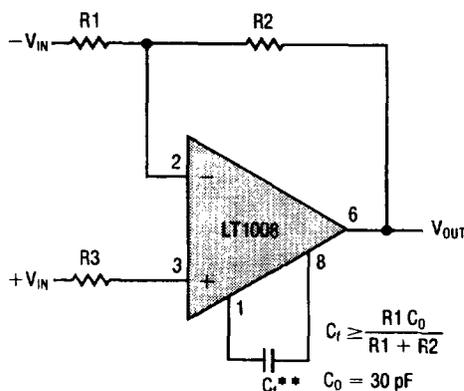
Note 2: These specifications apply for $\pm 2V \leq V_S \leq \pm 20V$ ($\pm 2.5V \leq V_S \leq \pm 20V$ over the temperature range) and $-13.5V \leq V_{CM} \leq 13.5V$ (for $V_S = \pm 15V$).

Note 3: 10Hz noise voltage density is sample tested on every lot. Devices 100% tested at 10Hz are available on request.

Note 4: This parameter is tested on a sample basis only.

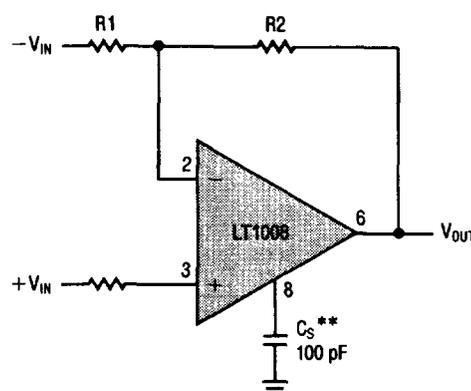
FREQUENCY COMPENSATION CIRCUITS

Standard Compensation Circuit



** BANDWIDTH AND SLEW RATE ARE PROPORTIONAL TO $1/C_f$

Alternate* Frequency Compensation

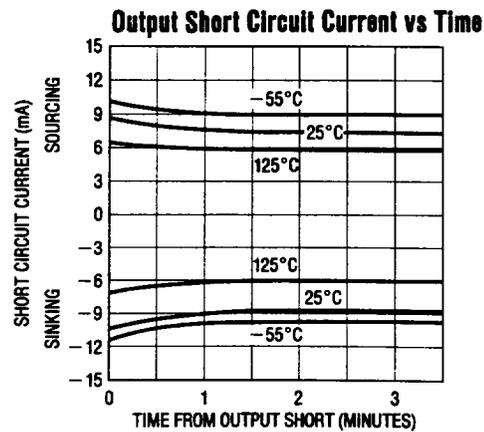
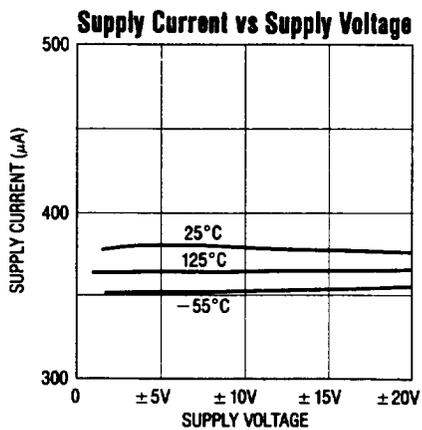
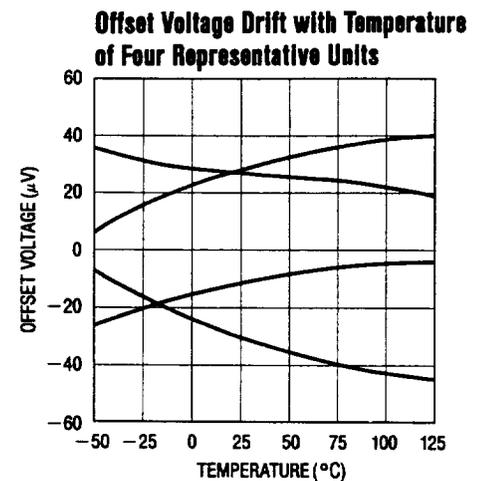
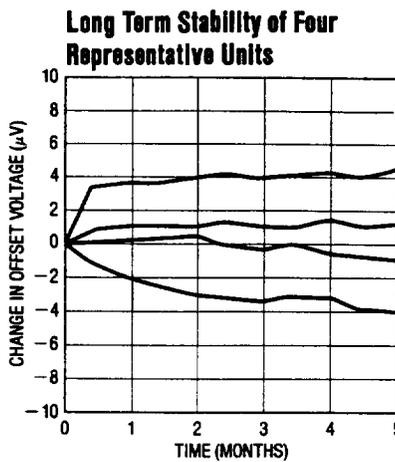
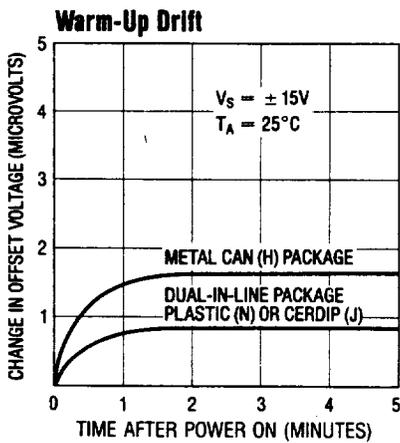
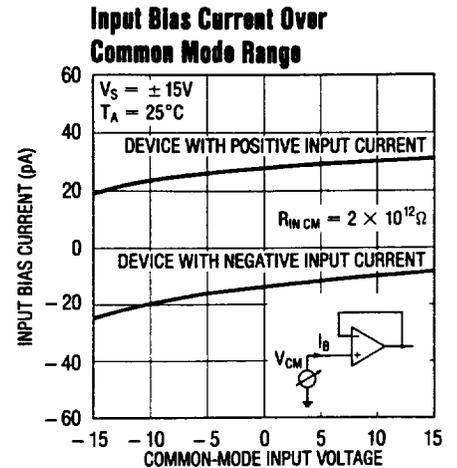
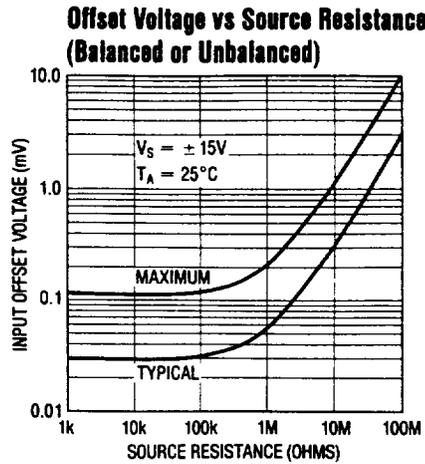
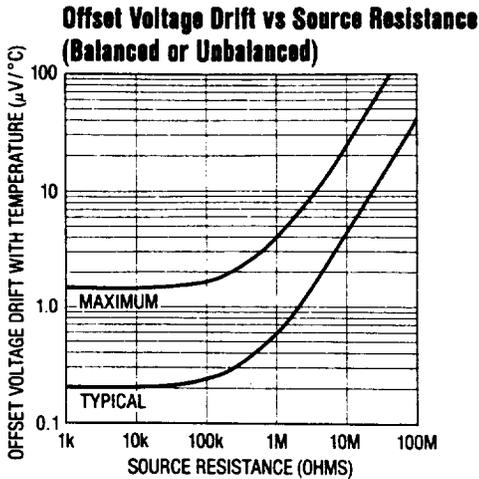


* IMPROVES REJECTION OF POWER SUPPLY NOISE BY A FACTOR OF 5.

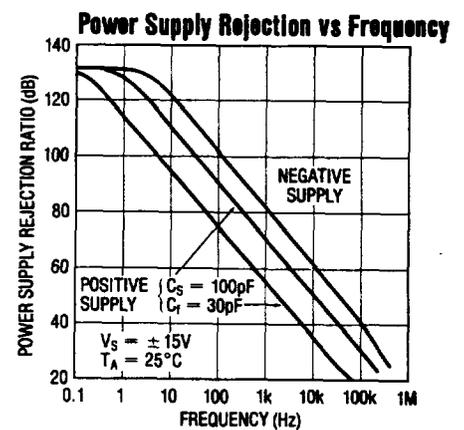
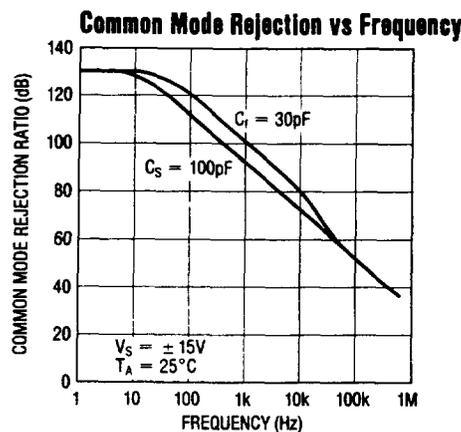
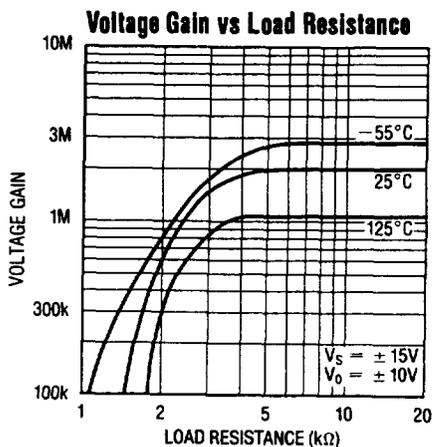
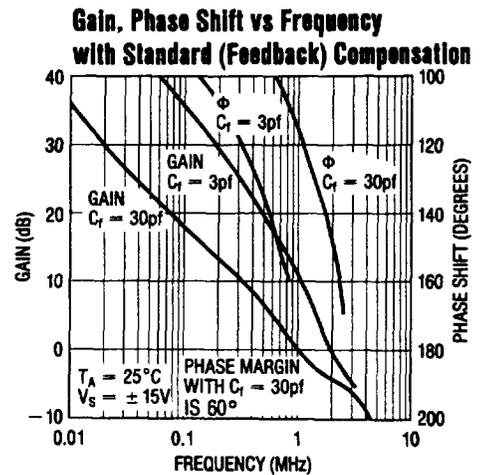
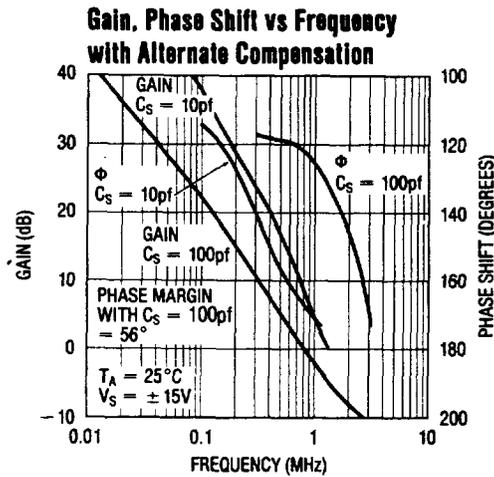
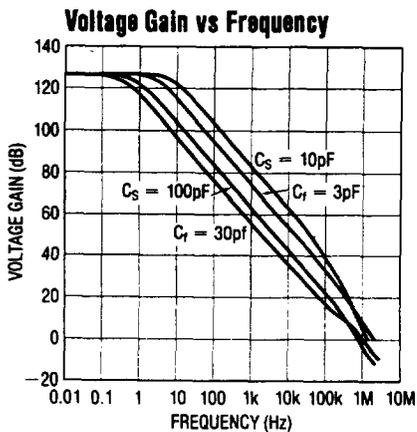
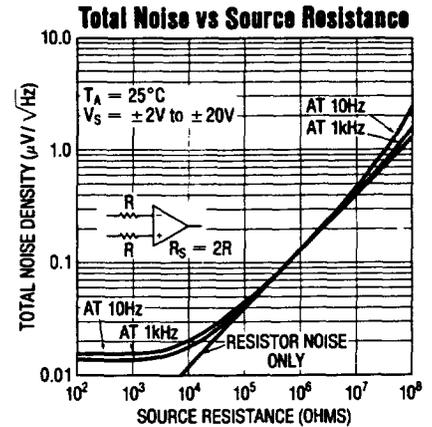
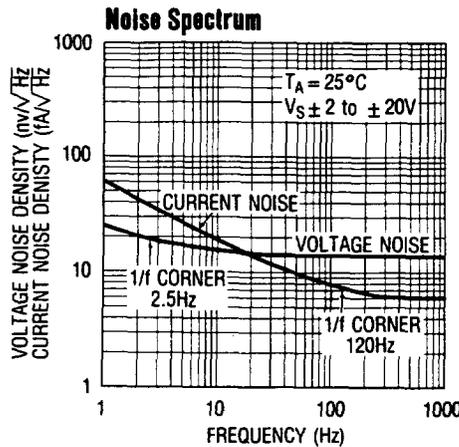
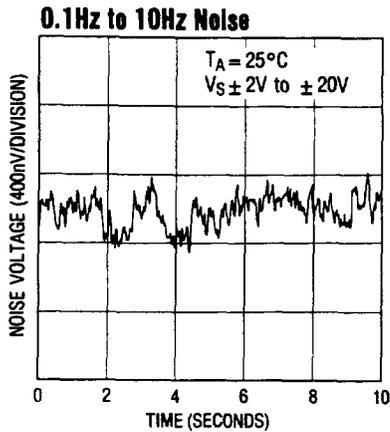
** BANDWIDTH AND SLEW RATE ARE PROPORTIONAL TO $1/C_s$

FOR $\frac{R_2}{R_1} > 200$ NO EXTERNAL FREQUENCY COMPENSATION IS NECESSARY

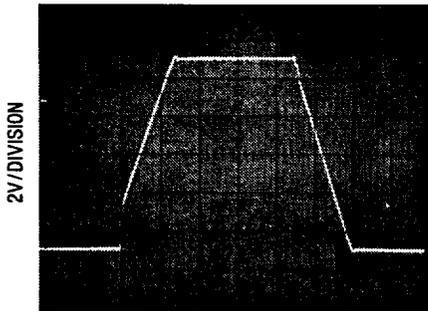
TYPICAL PERFORMANCE CHARACTERISTICS



TYPICAL PERFORMANCE CHARACTERISTICS

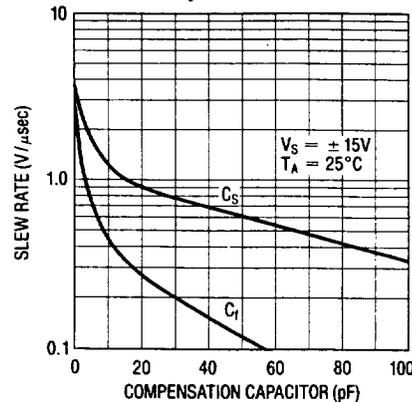


Large Signal Transient Response

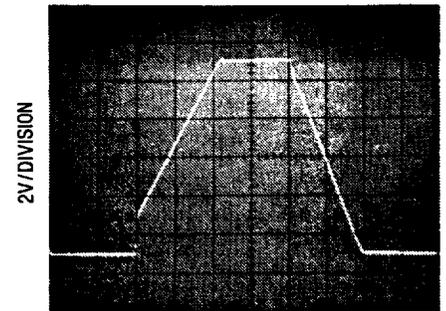


$A_V = +1, C_S = 100\text{pF}, 20\mu\text{sec}/\text{DIV}$

Slew Rate vs Compensation Capacitance

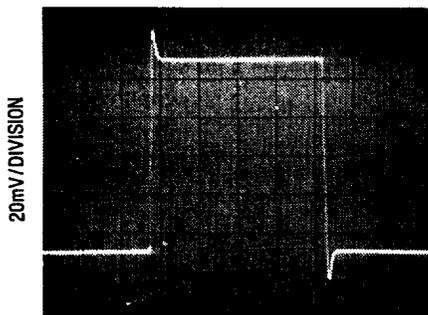


Large Signal Transient Response



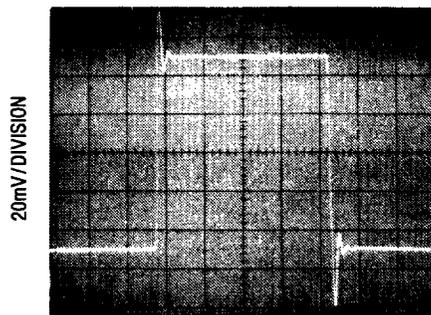
$A_V = +1, C_T = 30\text{pF}, 20\mu\text{sec}/\text{DIV}$

Small Signal Transient Response



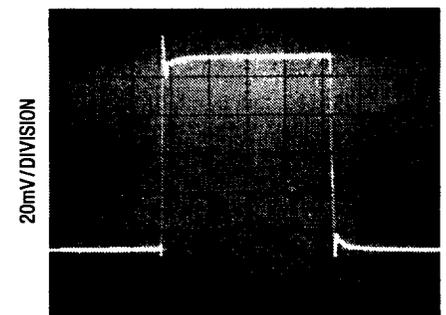
$A_V = +1, C_S = 100\text{pF}, C_{\text{LOAD}} = 100\text{pF}, 5\mu\text{sec}/\text{DIV}$

Small Signal Transient Response



$A_V = +1, C_S = 100\text{pF}, C_{\text{LOAD}} = 600\text{pF}, 5\mu\text{sec}/\text{DIV}$

Small Signal Transient Response



$A_V = +1, C_T = 30\text{pF}, C_{\text{LOAD}} = 100\text{pF}, 5\mu\text{sec}/\text{DIV}$

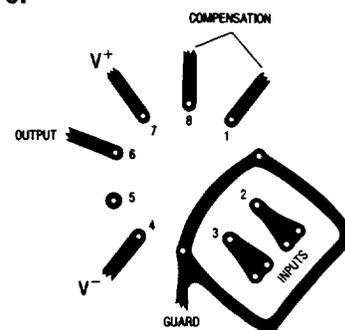
APPLICATIONS INFORMATION

Achieving Picoampere/Microvolt Performance

In order to realize the picoampere — microvolt level accuracy of the LT1008, proper care must be exercised. For example, leakage currents in circuitry external to the op amp can significantly degrade performance. High quality insulation should be used (e.g. Teflon, Kel-F); cleaning of all insulating surfaces to remove fluxes and other residues will probably be required. Surface coating may be necessary to provide a moisture barrier in high humidity environments.

Board leakage can be minimized by encircling the input circuitry with a guard ring operated at a potential close to that of the inputs: in inverting configurations the guard ring should be tied to ground, in non-invert-

ing connections to the inverting input at pin 2. Guarding both sides of the printed circuit board is required. Bulk leakage reduction depends on the guard ring width. Nanoampere level leakage into the compensation terminals can affect offset voltage and drift with temperature.



APPLICATIONS INFORMATION

Frequency Compensation

The LT1008 is externally frequency compensated with a single capacitor. The two standard compensation circuits shown on page 3 are identical to the LM108A/308A frequency compensation schemes. Therefore, the LT1008 operational amplifiers can be inserted directly into LM108A/308A sockets, with similar AC and upgraded DC performance.

External frequency compensation provides the user with additional flexibility in shaping the frequency response of the amplifier. For example, for a voltage gain of ten, and $C_f = 3\text{pF}$, a gain bandwidth product of 5MHz and slew rate of $1.2\text{V}/\mu\text{sec}$ can be realized. For closed loop gains in excess of 200, no external compensation is necessary, and slew rate increases to $4\text{V}/\mu\text{sec}$. The LT1008 can also be overcompensated (i.e. $C_f > 30\text{pF}$ or $C_S > 100\text{pF}$) to improve capacitive load handling capability or to narrow noise band-

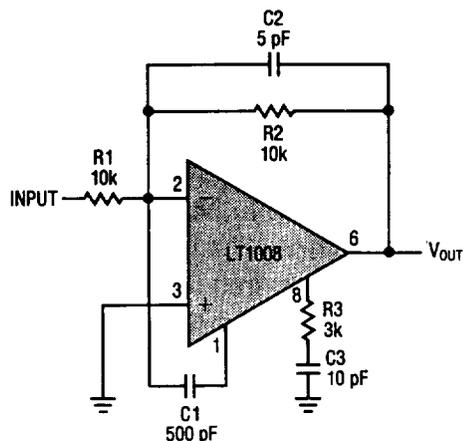
width. In many applications, the feedback loop around the amplifier has gain (e.g. logarithmic amplifiers); overcompensation can stabilize these circuits with a single capacitor.

The availability of the compensation terminals permits the use of feedforward frequency compensation to enhance slew rate in low closed loop gain configurations. The inverter slew rate is increased to $1.4\text{V}/\mu\text{sec}$. The voltage follower feedforward scheme bypasses the amplifier's gain stages and slews at nearly $10\text{V}/\mu\text{sec}$.

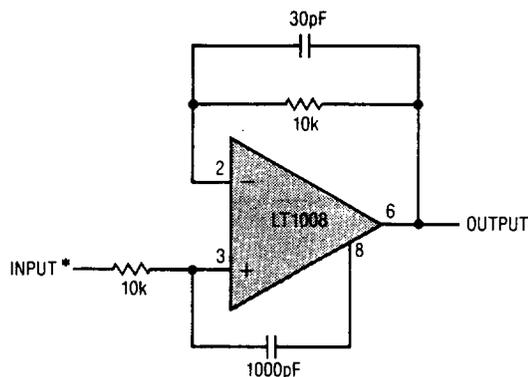
The inputs of the LT1008 are protected with back-to-back diodes. Current limiting resistors are not used, because the leakage of these resistors would prevent the realization of picoampere level bias currents at elevated temperatures. In the voltage follower configuration, when the input is driven by a fast, large signal pulse ($> 1\text{V}$), the input protection diodes effectively short the output to the input during slewing, and a current, limited only by the output short circuit protection will flow through the diodes.

The use of a feedback resistor, as shown in the voltage follower, feedforward diagram, is recommended because this resistor keeps the current below the short circuit limit, resulting in faster recovery and settling of the output.

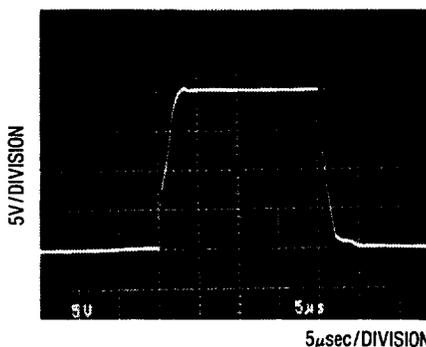
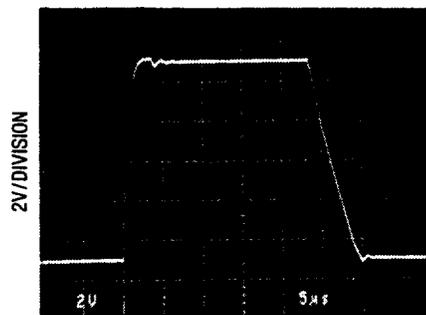
Inverter Feedforward Compensation



Follower Feedforward Compensation

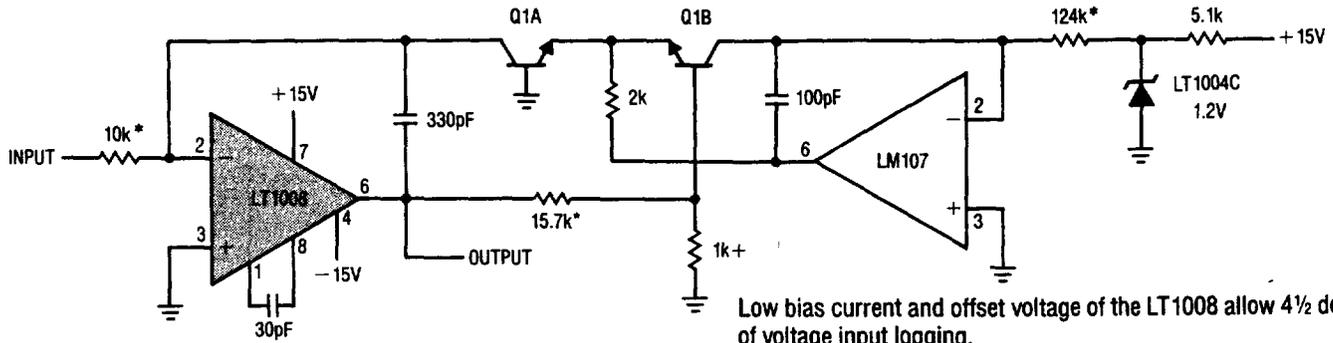


* SOURCE RESISTANCE $\leq 15\text{k}$ FOR STABILITY



APPLICATIONS

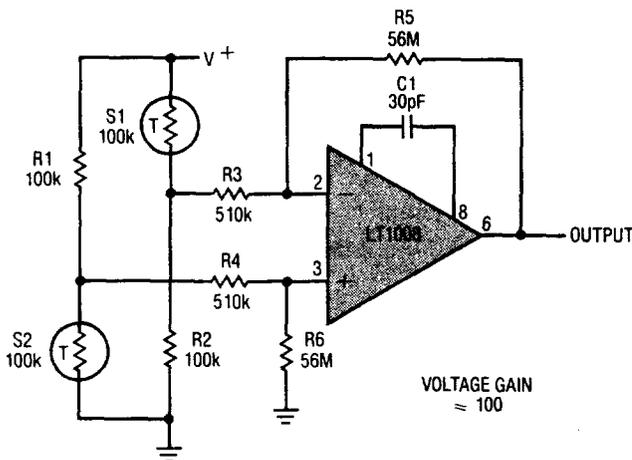
Logarithmic Amplifier



Low bias current and offset voltage of the LT1008 allow 4½ decades of voltage input logging.

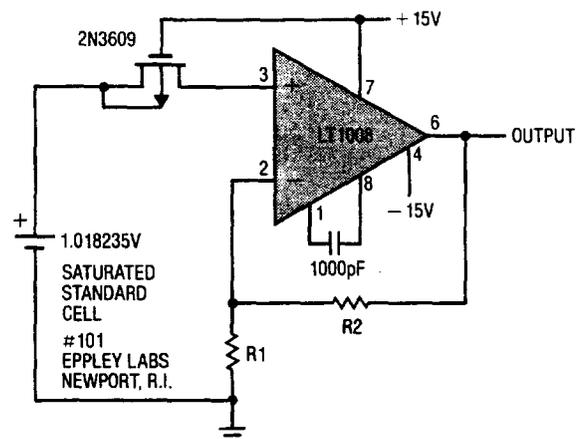
- + = TEL. LABS. TYPE Q81
- * = 1% FILM RESISTOR
- Q1 = 2N2979

Amplifier for Bridge Transducers



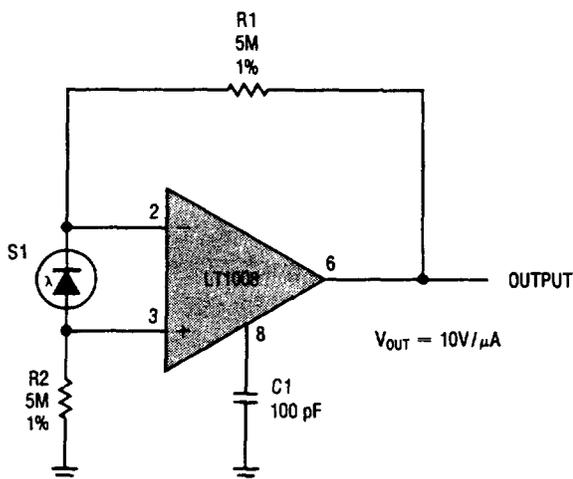
VOLTAGE GAIN ≈ 100

Saturated Standard Cell Amplifier



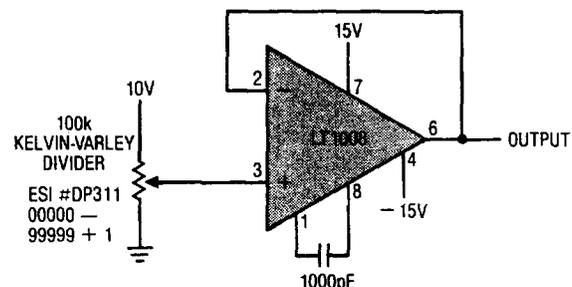
The typical 30pA bias current of the LT1008 will degrade the standard cell by only 1 ppm/year. Noise is a fraction of a ppm. Unprotected gate MOSFET isolates standard cell on power down.

Amplifier For Photodiode Sensor



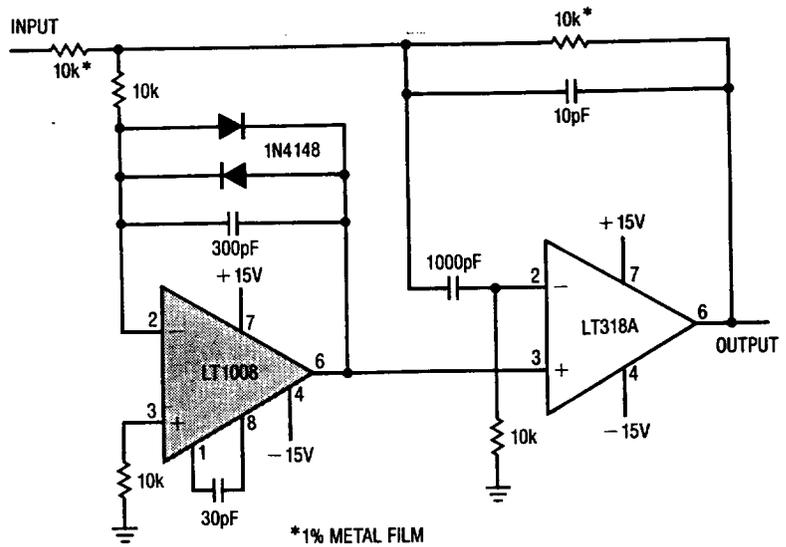
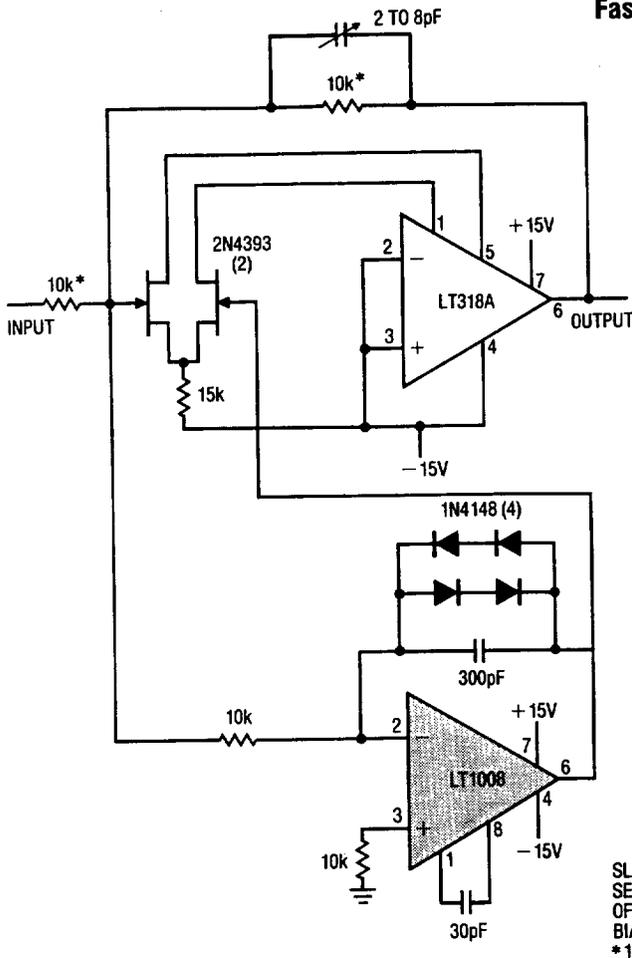
$V_{OUT} = 10V/\mu A$

Five Decade Kelvin-Varley Divider Buffered by the LT1008



Approximate error due to noise, bias current, common-mode rejection, voltage gain of the amplifier is 1/5 of a least significant bit.

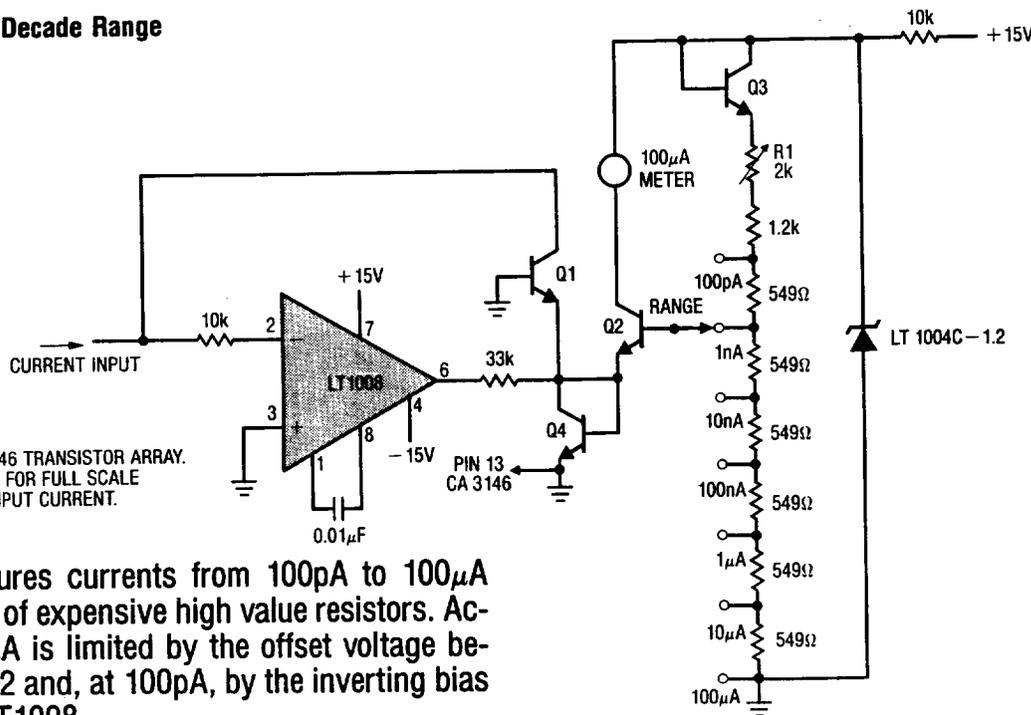
Fast Precision Inverters



FULL POWER BANDWIDTH = 2MHz
 SLEW RATE = 50V/ μ sec
 SETTLEING (10V STEP) = 12 μ S TO 0.01%
 BIAS CURRENT DC = 30pA
 OFFSET DRIFT = 0.3 μ V/ $^{\circ}$ C
 OFFSET VOLTAGE = 30 μ V

SLEW RATE @ 100V/ μ S
 SETTLEING = 5 μ S TO .01%/10 VOLT STEP
 OFFSET VOLTAGE = 30 μ V
 BIAS CURRENT = 30pA
 * 1% METAL FILM

Ammeter With Six Decade Range

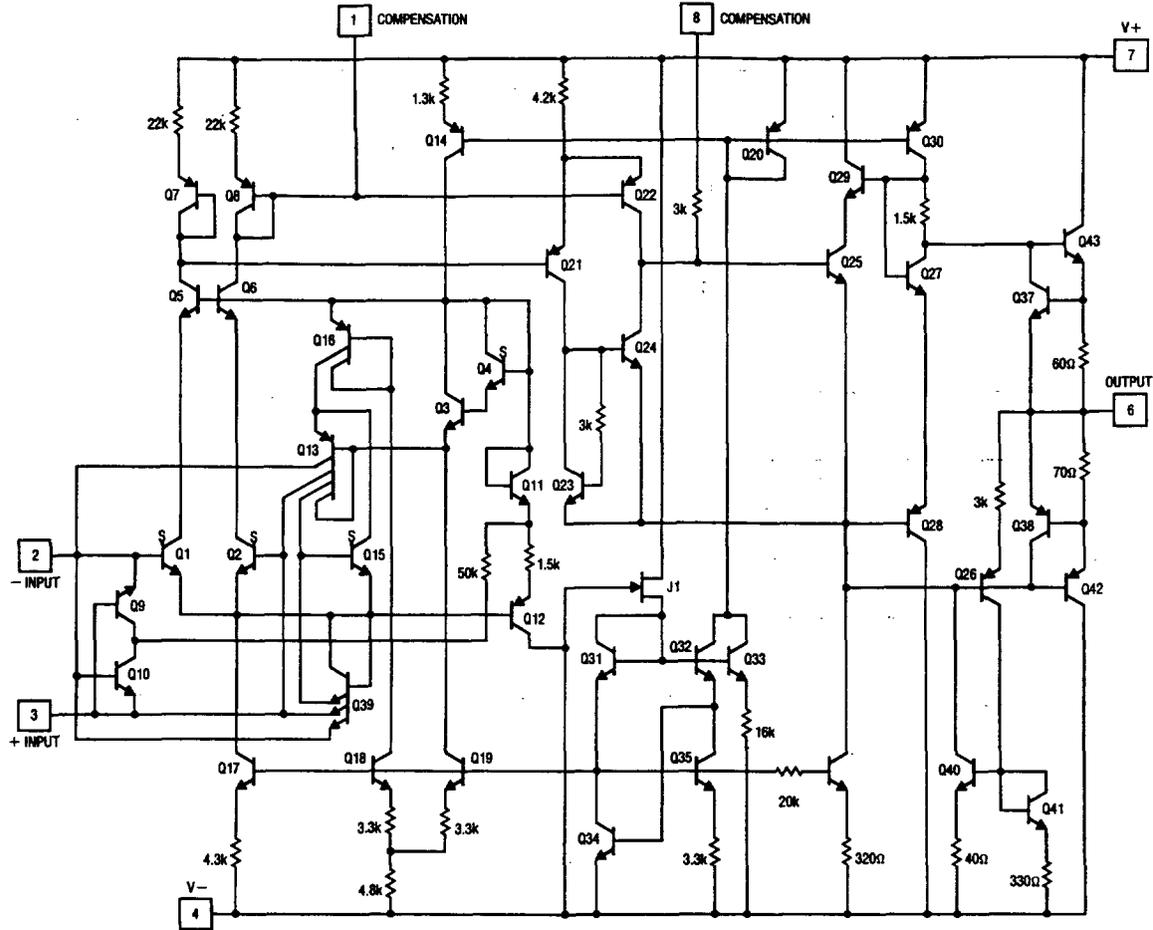


Q1, Q2, Q3, Q4, RCA CA3146 TRANSISTOR ARRAY.
 CALIBRATION: ADJUST R1 FOR FULL SCALE
 DEFLECTION WITH 1 μ A INPUT CURRENT.

Ammeter measures currents from 100pA to 100 μ A without the use of expensive high value resistors. Accuracy at 100 μ A is limited by the offset voltage between Q1 and Q2 and, at 100pA, by the inverting bias current of the LT1008.

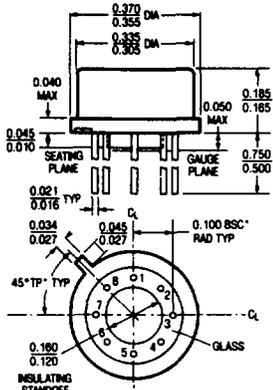
LT1008

SCHEMATIC DIAGRAM



PACKAGE DESCRIPTION

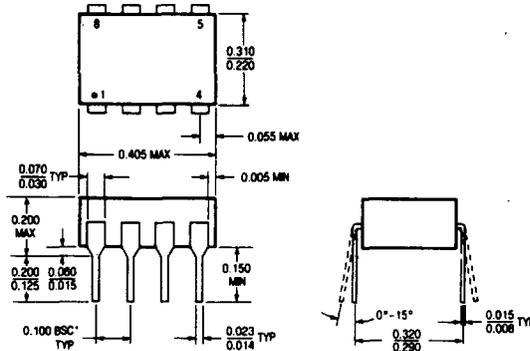
H Package
Metal Can



NOTE: DIMENSIONS IN INCHES

T_{jmax}	θ_{ja}	θ_{jc}
150°C	150°C/W	45°C/W

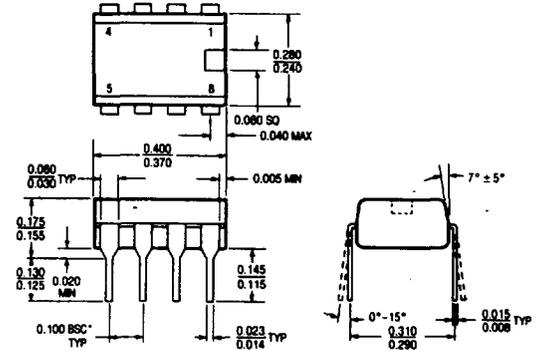
J8 Package
8 Lead Hermetic Dip



NOTE: DIMENSIONS IN INCHES UNLESS OTHERWISE NOTED.
*LEADS WITHIN 0.007 OF TRUE POSITION (TP) AT GAUGE PLANE

T_{jmax}	θ_{ja}
150°C	100°C/W

N8 Package
8 Lead Plastic



NOTE: DIMENSIONS IN INCHES UNLESS OTHERWISE NOTED.
*LEADS WITHIN 0.007 OF TRUE POSITION (TP) AT GAUGE PLANE.

T_{jmax}	θ_{ja}
100°C	130°C/W