



# CR623X—CR6235/6236/6238

## High Precision CC/CV Primary-Side PWM Power Switch

### Features

- 5% Constant Voltage Regulation, 5% Constant Current Regulation at Universal AC input
- Primary-side Sensing and Regulation Without TL431 and Opto-coupler
- Power on Soft-start
- Built-in Leading Edge Blanking (LEB)
- Cycle-by-Cycle Current Limiting
- VDD Under Voltage Lockout with Hysteresis (UVLO)
- Programmable CV and CC Regulation
- Adjustable Constant Current and Output Power Setting
- Built-in Secondary Constant Current Control with Primary Side Feedback
- Built-in adaptive current peak regulation
- Built-in Primary winding inductance compensation
- Program cable drop compensation
- VDD OVP and VDD Clamp
- Pb-free SOP-8L & DIP-8L

### Applications

- Cell Phone /Digital Cameras Charger
- Small Power Adaptor
- Auxiliary Power for PC, TV etc.
- Linear Regulator/RCC Replacement

### General Description

CR623X is a high performance offline PWM Power switch for low power AC/DC charger and adaptor applications. It operates in primary-side sensing and regulation. Consequently, opto-coupler and TL431 could be eliminated. Proprietary Constant Voltage (CV) and Constant Current (CC) control is integrated as shown in the Fig.1.

In CC control, the current and output power setting can be adjusted externally by the sense resistor  $R_S$  at CS pin. In CV control, multi-mode operations are utilized to achieve high performance and high efficiency. In addition, good load regulation is achieved by the built-in cable drop compensation. Device operates in PFM in CC mode as well at large load condition and it operates in PWM with frequency reduction at light/medium load.

CR623X offers power on soft start control and protection coverage with auto-recovery features including Cycle-by-Cycle current limiting, VDD OVP, VDD clamp and UVLO. Excellent EMI performance is achieved with frequency jitter technique.

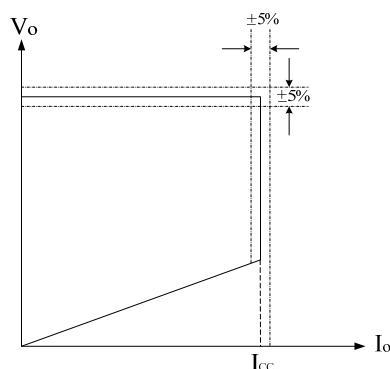
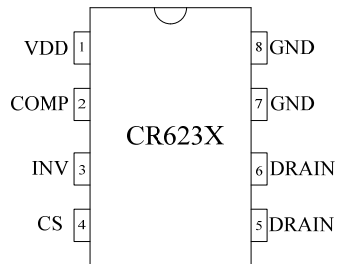


Fig.1. Typical CC/CV Curve

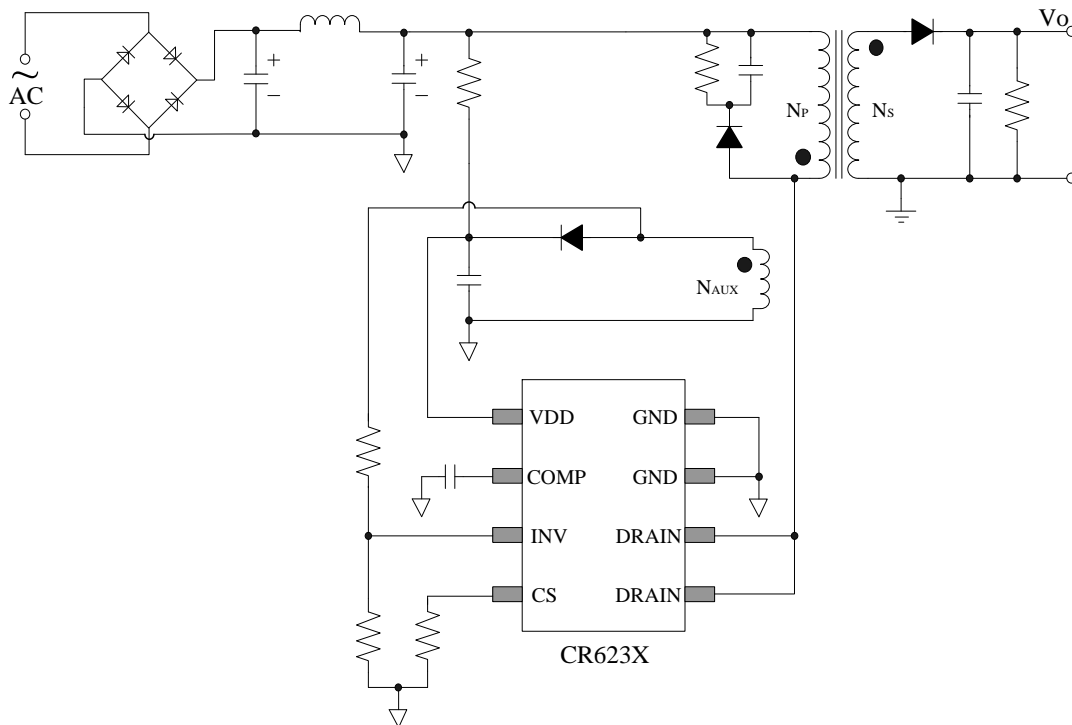
## Pin Assignment (SOP-8L &amp; DIP-8L)



## Pin Description

Pin Num	Pin Name	Description
1	VDD	Power Supply
2	COMP	Loop Compensation for CV Stability
3	INV	The voltage feedback from auxiliary winding. Connected to resistor divider from auxiliary winding reflecting output voltage. PWM duty cycle is determined by EA output and current sense signal at pin 4.
4	CS	Current sense input
5/6	DRAIN	HV MOSFET Drain Pin. The Drain pin is connected to the primary lead of the transformer
7/8	GND	Ground

## Typical Application





## Electrical Characteristics

(Ta=25°C unless otherwise noted, V<sub>DD</sub> = 16V)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
<b>Supply Voltage (VDD) Section</b>						
I <sub>DD_ST</sub>	Standby current	VDD=13V		5	20	uA
I <sub>DD_OP</sub>	Operation Current	Operation supply current INV=2V,CS=0V, VDD=VDDG=20V		2.5	3.5	mA
UVLO(ON)	VDD Under Voltage Lockout Enter	VDD falling	7.5	8.5	10	V
UVLO(OFF)	VDD Under Voltage Lockout Exit	VDD rising	13.5	14.5	16.0	V
OVP	Over voltage protection voltage	Ramp up VDD until gate clock is off	27.5	29.5	31.5	V
V <sub>DD_clamp</sub>	Maximum VDD operation voltage	I <sub>DD</sub> =10mA	30.5	32.5	34.5	V
<b>Current Sense Input Section</b>						
T <sub>LEB</sub>	LEB time			540		ns
V <sub>th_oc</sub>	Over current threshold		870	900	930	mV
T <sub>d_oc</sub>	OCP Propagation delay			150		ns
Z <sub>SENSE_IN</sub>	Input Impedance			50		Kohm
T <sub>ss</sub>	Soft start time			10		ms
<b>CV Section</b>						
Freq_Nom	System Nominal switch frequency			60		
Freq_startup		INV=0V,Comp=5V		14		KHZ
Δf/Freq	Frequency jitter range			+/-4		%
<b>Error Amplifier section</b>						
V <sub>ref_EA</sub>	Reference voltage for EA		1.97	2	2.03	V
G <sub>dc</sub>	DC gain of the EA			60		dB
I <sub>COMP_MAX</sub>	Max. Cable compensation current	INV=2V,COMP=0V		42		uA
<b>Power MOSFET Section</b>						
BV <sub>dss</sub>	MOSFET Drain-Source Breakdown Voltage			650		V
R <sub>DS(on)</sub>	Static Drain to Source On Resistance	CR6235S		12	15	Ω
		CR6236T		9.2	12	Ω
		CR6238T		3.0	3.6	Ω

## Operation Description

CR623X is a cost effective PWM power switch optimized for off-line low power AC/DC applications including battery chargers and adaptors. It operates in primary side sensing and regulation, thus opto-coupler and TL431 are not required. Proprietary built-in CV and CC control can achieve high precision CC/CV control meeting most adaptor and charger application requirements.

### Startup Current and Start up Control

Startup current of CR623X is designed to be very low so that VDD could be charged up above UVLO threshold and starts up quickly. A large value startup resistor can therefore be used to minimize the power loss in application.

### Operating Current

The Operating current of CR623X is as low as 2.5mA. Good efficiency is achieved with the low operating current together with Multi-mode control features.

### Soft Start

CR623X features an internal soft start to minimize the component electrical over-stress during power on startup. As soon as VDD reaches UVLO (OFF), the control algorithm will ramp peak current voltage threshold gradually from nearly zero to normal setting of 0.90V. Every restart is a soft start.

### CC/CV Operation

CR623X is designed to produce good CC/CV control characteristic as shown in the Fig. 1.

In charger applications, a discharged battery charging starts in the CC portion of the curve until it is nearly full charged and smoothly switches to operate in CV portion

of the curve.

In an AC/DC adapter, the normal operation occurs only on the CV portion of the curve. The CC portion provides output current limiting. In CV operation, the output voltage is regulated through the primary side control. In CC operation mode, CR623X will regulate the output current constant regardless of the output voltage drop.

### Principle of Operation

To support CR623X proprietary CC/CV control, system needs to be designed in DCM mode for flyback system (Refer to Typical Application Diagram).

In the DCM flyback converter, the output voltage can be sensed via the auxiliary winding. During MOSFET turn-on time, the load current is supplied from the output filter capacitor  $C_O$ . The current in the primary winding ramps up. When MOSFET turns off, the primary current transfers to the secondary at the amplitude of

$$I_s = \frac{N_p}{N_s} \cdot I_p$$

The auxiliary voltage reflects the output voltage as shown in fig.2 and it is given by

$$V_{AUX} = \frac{N_{AUX}}{N_s} \cdot (V_o + \Delta V)$$

Where  $\Delta V$  indicates the drop voltage of the output Diode.

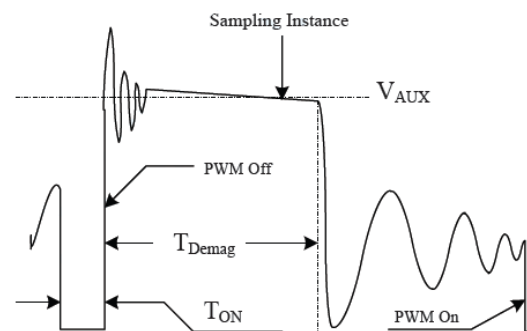


Fig.2. Auxiliary voltage waveform

Via a resistor divider connected between the auxiliary winding and INV (pin 3), the auxiliary voltage is sampled at the end of the demagnetization and it is hold until the next sampling.

The sampled voltage is compared with Vref (2.0V) and the error is amplified. The error amplifier output COMP reflects the load condition and controls the PWM switching frequency to regulate the output voltage, thus constant output voltage can be achieved.

When sampled voltage is below Vref and the error amplifier output COMP reaches its maximum, the switching frequency is controlled by the sampled voltage thus the output voltage to regulate the output current, thus the constant output current can be achieved.

#### Adjustable CC point and Output Power

In CR623X, the CC point and maximum output power can be externally adjusted by external current sense resistor  $R_S$  at CS pin as illustrated in Typical Application Diagram. The output power is adjusted through CC point change. The larger  $R_S$ , the smaller CC point is, and the smaller output power becomes, and vice versa as shown in Fig.3.

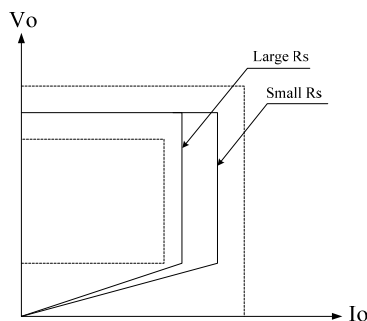


Fig.3. Adjustable output power by changing  $R_S$

#### Operation switching frequency

The switching frequency of CR623X is adaptively controlled according to the load conditions and the operation modes. No

external frequency setting components are required. The operation switching frequency at maximum output power is set to 60KHz internally.

For flyback operating in DCM, The maximum output power is given by

$$P_{O\_MAX} = \frac{1}{2} L_P F_{SW} I_P^2$$

Where  $L_P$  indicate the inductance of primary winding and  $I_P$  is the peak current of primary winding.

Refer to the equation 3, the change of the primary winding inductance results in the change of the maximum output power and the constant output current in CC mode. To compensate the change from variations of primary winding inductance, the switching frequency is locked by an internal loop such that the switching frequency is

$$F_{SW} = \frac{1}{2T_{Demag}}$$

Since  $T_{Demag}$  is inversely proportional to the inductance, as a result, the product  $L_P$  and  $F_{SW}$  is constant, thus the maximum output power and constant current in CC mode will not change as primary winding inductance changes. Up to +/-10% variation of the primary winding inductance can be compensated.

#### Frequency jitter for EMI improvement

The frequency jitter (switching frequency modulation) is implemented in CR623X. The oscillation frequency is modulated so that the tone energy is spread out. The spread spectrum minimizes the conduction band EMI and therefore eases the system design.

#### Current Sensing and Leading Edge Blanking

Cycle-by-Cycle current limiting is offered in CR623X current mode PWM control. The switch current is detected by a sense resistor into the CS pin. An internal leading

edge blanking circuit chops off the sensed voltage spike at initial internal power MOSFET on state so that the external RC filtering on sense input is no longer needed. The PWM duty cycle is determined by the current sense input voltage and the EA output voltage.

winding output. The output of CR623X is shut down when VDD drops below UVLO (ON) limit and Switcher enters power on start-up sequence thereafter.

**Gate Drive**

The internal power MOSFET in CR623X is driven by a dedicated gate driver for power switch control. Too weak the gate drive strength results in higher conduction and switch loss of MOSFET while too strong gate drive compromises EMI.

A good tradeoff is achieved through the built-in totem pole gate design with right output strength control.

**Programmable Cable drop Compensation**

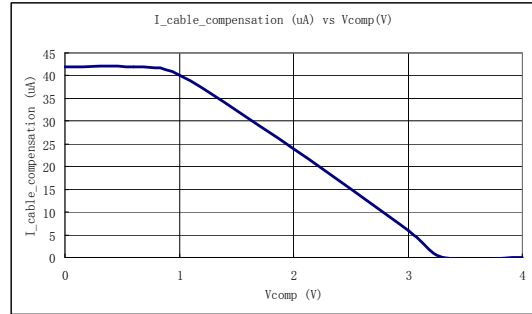
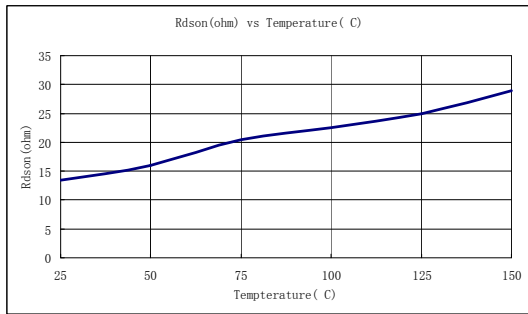
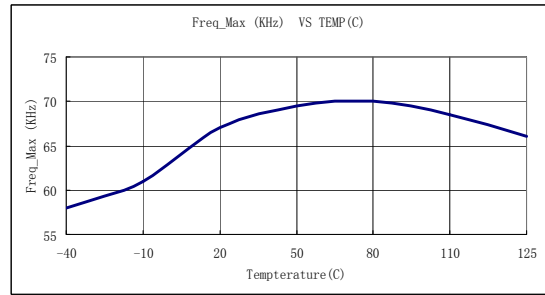
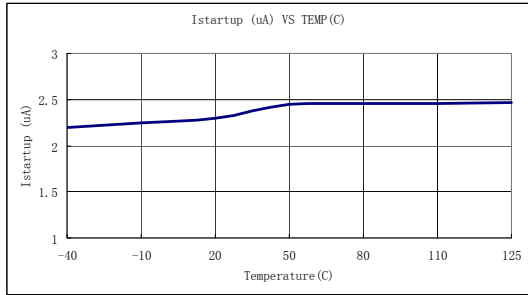
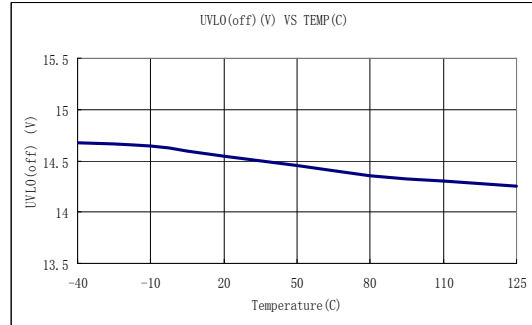
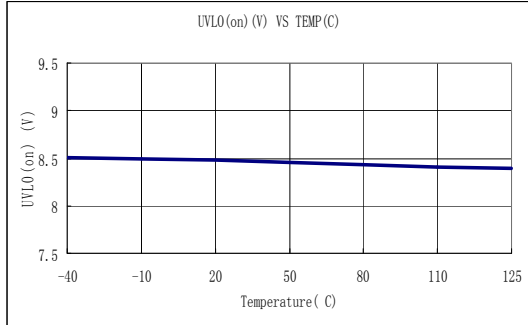
In CR623X, cable drop compensation is implemented to achieve good load regulation. An offset voltage is generated at INV by an internal current flowing into the resistor divider. The current is inversely proportional to the voltage across pin COMP, as a result, it is inversely proportional to the output load current, thus the drop due to the cable loss can be compensated. As the load current decreases from full-load to no-load, the offset voltage at INV will increase. It can also be programmed by adjusting the resistance of the divider to compensate the drop for various cable lines used.

**Protection Control**

Good power supply system reliability is achieved with its rich protection features including Cycle-by-Cycle current limiting (OCP), VDD clamp, Power on Soft Start, and Under Voltage Lockout on VDD(UVLO). VDD is supplied by transformer auxiliary

### Characterization Plots

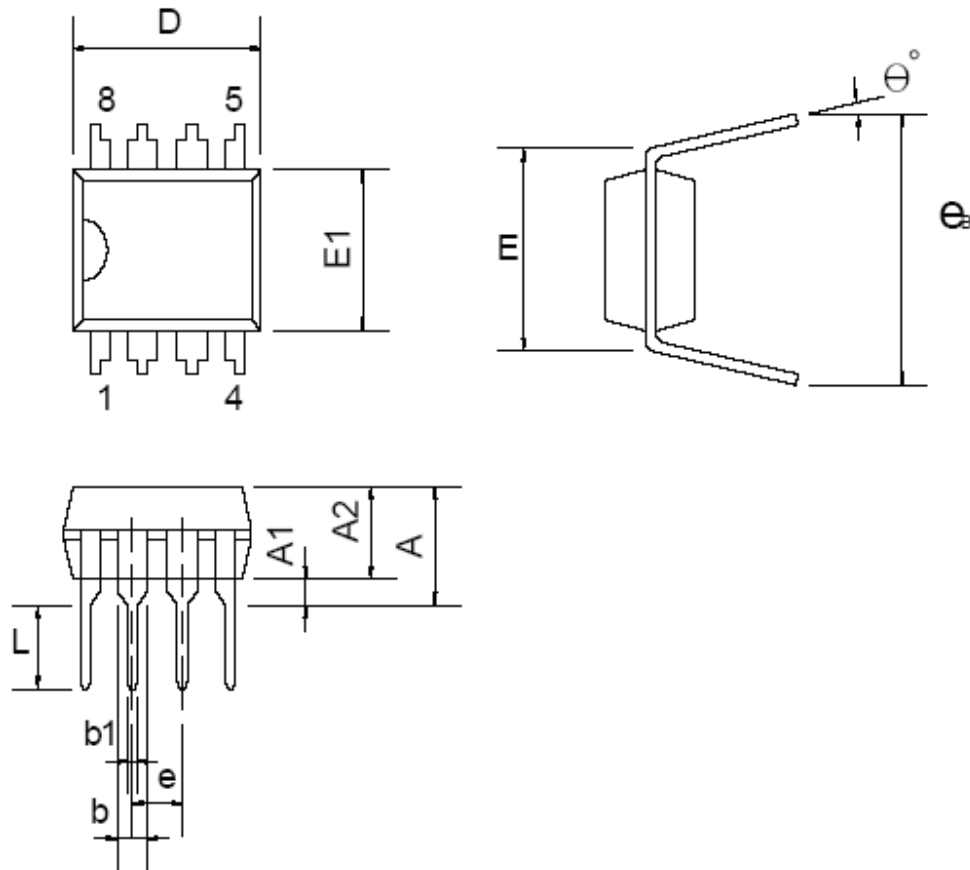
The characteristic graphs are normalized at  $T_A=25^\circ\text{C}$ .





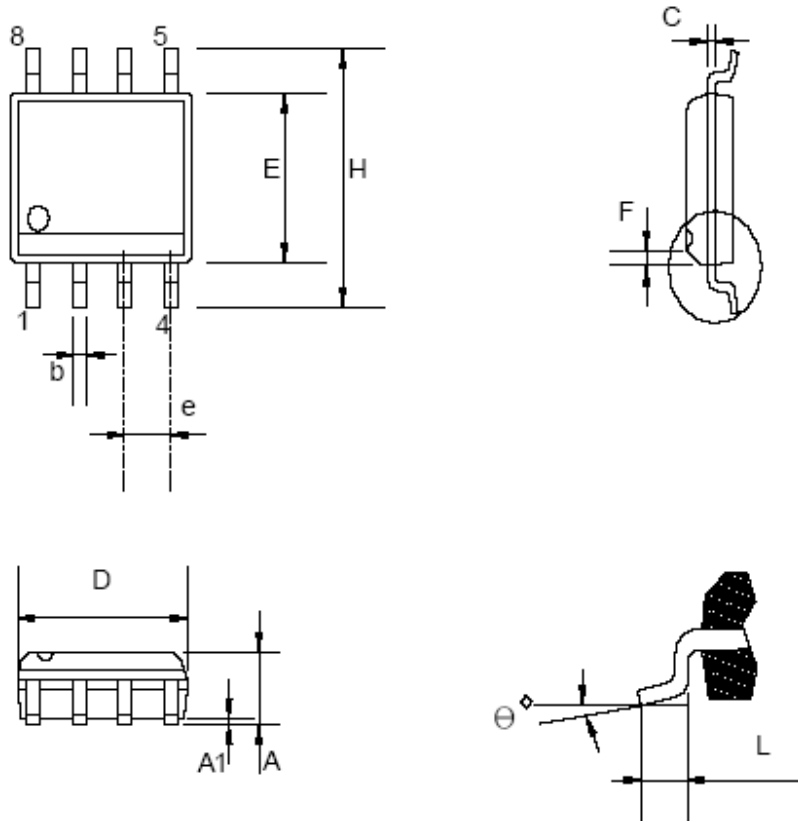
## Package Information

### DIP-8L



Symbol	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A			5.334			0.210
A1	0.381			0.015		
A2	3.175	3.302	3.429	0.125	0.130	0.135
b		1.524			0.060	
b1		0.457			0.018	
D	9.017	9.271	10.160	0.355	0.365	0.400
E		7.620			0.300	
E1	6.223	6.350	6.477	0.245	0.250	0.255
e		2.540			0.100	
L	2.921	3.302	3.810	0.115	0.130	0.150
eB	8.509	9.017	9.525	0.335	0.355	0.375
$\theta^\circ$	0°	7°	15°	0°	7°	15°

SOP-8L



Symbol	Millimeter			Inch		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	1.346		1.752	0.053		0.069
A1	0.101		0.254	0.004		0.010
b		0.406			0.016	
c		0.203			0.008	
D	4.648		4.978	0.183		0.196
E	3.810		3.987	0.150		0.157
e	1.016	1.270	1.524	0.040	0.050	0.060
F		0.381X45			0.015X45	
H	5.791		6.197	0.228		0.244
L	0.406		1.270	0.016		0.050
$\theta$	0°		8°	0°		8°