



**ELECTROSTATIC SENSITIVE DEVICE**  
OBSERVE HANDLING PRECAUTIONS

**MITSUBISHI RF MOSFET MODULE**  
**RA30H3340M**

**RoHS Compliance , 330-400MHz 30W 12.5V, 3 Stage Amp. For MOBILE RADIO**

**DESCRIPTION**

The RA30H3340M is a 30-watt RF MOSFET Amplifier Module for 12.5-volt mobile radios that operate in the 330- to 400-MHz range.

The battery can be connected directly to the drain of the enhancement-mode MOSFET transistors. Without the gate voltage ( $V_{GG}=0V$ ), only a small leakage current flows into the drain and the RF input signal attenuates up to 60 dB. The output power and drain current increase as the gate voltage increases. With a gate voltage around 4.0V (minimum), output power and drain current increases substantially. The nominal output power becomes available at 4.5V (typical) and 5V (maximum). At  $V_{GG}=5V$ , the typical gate current is 1 mA.

This module is designed for non-linear FM modulation, but may also be used for linear modulation by setting the drain quiescent current with the gate voltage and controlling the output power with the input power.

**FEATURES**

- Enhancement-Mode MOSFET Transistors ( $I_{DD} \approx 0$  @  $V_{DD}=12.5V$ ,  $V_{GG}=0V$ )
- $P_{out} > 30W$ ,  $\eta_T > 40\%$  @  $V_{DD}=12.5V$ ,  $V_{GG}=5V$ ,  $P_{in}=50mW$
- Broadband Frequency Range: 330-400MHz
- Low-Power Control Current  $I_{GG}=1mA$  (typ) at  $V_{GG}=5V$
- 66 x 21 x 9.8 mm
- Linear operation is possible by setting the quiescent drain current with the gate voltage and controlling the output power with the input power

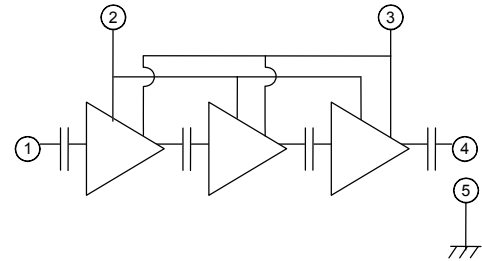
**RoHS COMPLIANCE**

- RA30H3340M-101 is a RoHS compliant products.
  - RoHS compliance is indicate by the letter "G" after the Lot Marking.
  - This product include the lead in the Glass of electronic parts and the lead in electronic Ceramic parts.
- How ever, it is applicable to the following exceptions of RoHS Directions.
1. Lead in the Glass of a cathode-ray tube, electronic parts, and fluorescent tubes.
  2. Lead in electronic Ceramic parts.

**ORDERING INFORMATION:**

| ORDER NUMBER   | SUPPLY FORM                         |
|----------------|-------------------------------------|
| RA30H3340M-101 | Antistatic tray,<br>10 modules/tray |

**BLOCK DIAGRAM**



- ① RF Input ( $P_{in}$ )
- ② Gate Voltage ( $V_{GG}$ ), Power Control
- ③ Drain Voltage ( $V_{DD}$ ), Battery
- ④ RF Output ( $P_{out}$ )
- ⑤ RF Ground (Case)

**PACKAGE CODE: H2S**



**MAXIMUM RATINGS** ( $T_{case}=+25^{\circ}\text{C}$ , unless otherwise specified)

| SYMBOL         | PARAMETER                        | CONDITIONS   | RATING      | UNIT               |
|----------------|----------------------------------|--|-------------|--------------------|
| $V_{DD}$       | Drain Voltage                    | $V_{GG}<5\text{V}$                                   | 17          | V                  |
| $V_{GG}$       | Gate Voltage                     | $V_{DD}<12.5\text{V}$ , $P_{in}=0\text{mW}$          | 6           | V                  |
| $P_{in}$       | Input Power                      | $f=330\text{-}400\text{MHz}$ ,<br>$Z_G=Z_L=50\Omega$ | 100         | mW                 |
| $P_{out}$      | Output Power                     |  | 45          | W                  |
| $T_{case(OP)}$ | Operation Case Temperature Range |  | -30 to +110 | $^{\circ}\text{C}$ |
| $T_{stg}$      | Storage Temperature Range        |  | -40 to +110 | $^{\circ}\text{C}$ |

Above Parameters are guaranteed independently

**ELECTRICAL CHARACTERISTICS** ( $T_{case}=+25^{\circ}\text{C}$ ,  $Z_G=Z_L=50\Omega$ , unless otherwise specified)

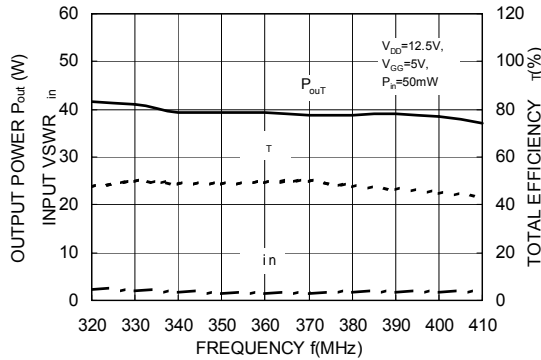
| SYMBOL      | PARAMETER                | CONDITIONS  | MIN                       | TYP | MAX | UNIT |
|-------------|--------------------------|---|---------------------------|-----|-----|------|
| f           | Frequency Range          |   | 330                       |     | 400 | MHz  |
| $P_{out}$   | Output Power             | $V_{DD}=12.5\text{V}$ ,<br>$V_{GG}=5\text{V}$ ,<br>$P_{in}=50\text{mW}$   | 30                        |     |     | W    |
| $\eta_T$    | Total Efficiency         |   | 40                        |     |     | %    |
| $2f_o$      | 2 <sup>nd</sup> Harmonic |   |                           |     | -25 | dBc  |
| $\rho_{in}$ | Input VSWR               |   |                           |     | 3:1 | —    |
| $I_{GG}$    | Gate Current             |   |                           |     | 1   | mA   |
| —           | Stability                | $V_{DD}=10.0\text{-}15.2\text{V}$ , $P_{in}=25\text{-}70\text{mW}$ ,<br>$P_{out}<40\text{W}$ ( $V_{GG}$ control), Load VSWR=3:1 | No parasitic oscillation  |     |     | —    |
| —           | Load VSWR Tolerance      | $V_{DD}=15.2\text{V}$ , $P_{in}=50\text{mW}$ , $P_{out}=30\text{W}$ ( $V_{GG}$ control),<br>Load VSWR=20:1                      | No degradation or destroy |     |     | —    |

All Parameters, Conditions, Ratings and Limits are subject to change without notice

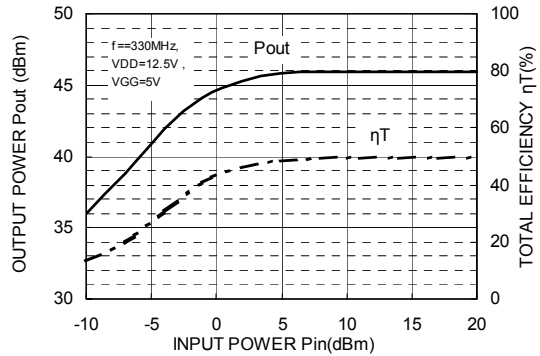


TYPICAL PERFORMANCE ( $T_{case}=+25^{\circ}C$ ,  $Z_G=Z_L=50\Omega$ , unless otherwise specified)

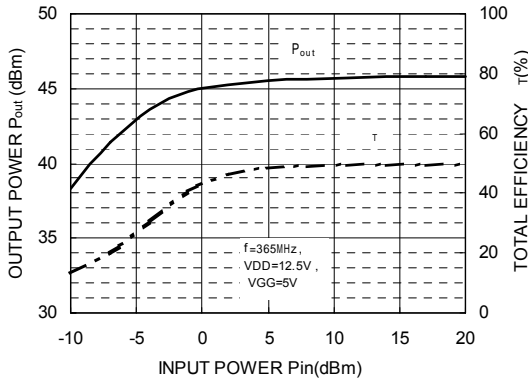
OUTPUT POWER, TOTAL EFFICIENCY, and INPUT VSWR versus FREQUENCY



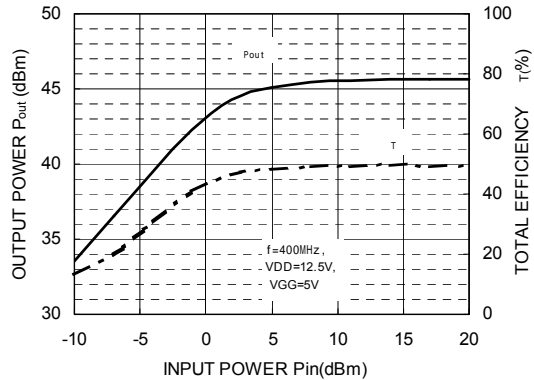
OUTPUT POWER and TOTAL EFFICIENCY versus INPUT POWER



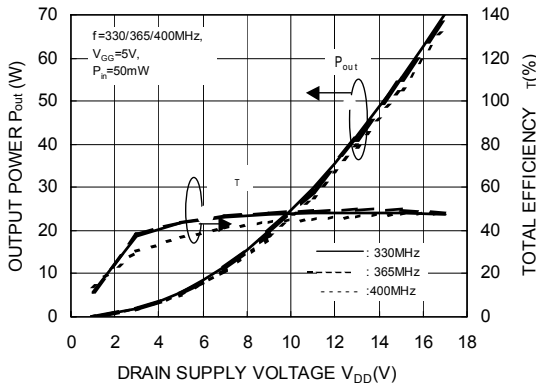
OUTPUT POWER and EFFICIENCY versus INPUT POWER



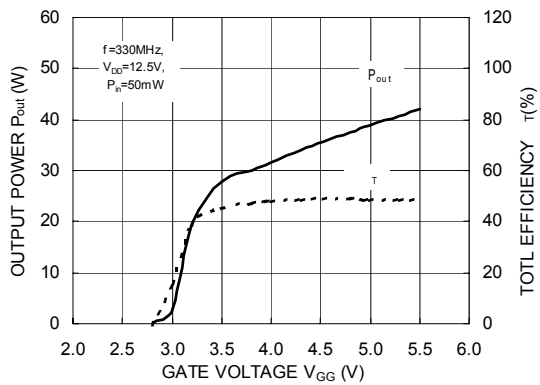
OUTPUT POWER and EFFICIENCY versus INPUT POWER



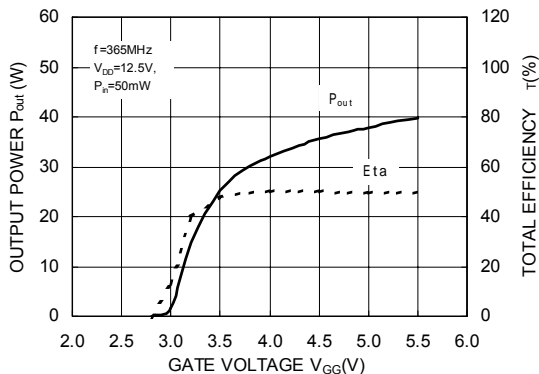
OUTPUT POWER and EFFICIENCY versus DRAIN VOLTAGE



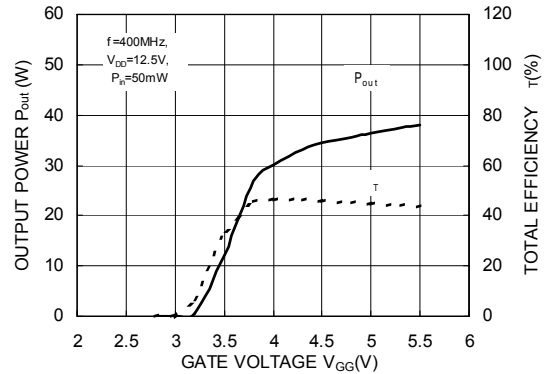
OUTPUT POWER and EFFICIENCY versus GATE VOLTAGE



OUTPUT POWER and EFFICIENCY versus GATE VOLTAGE

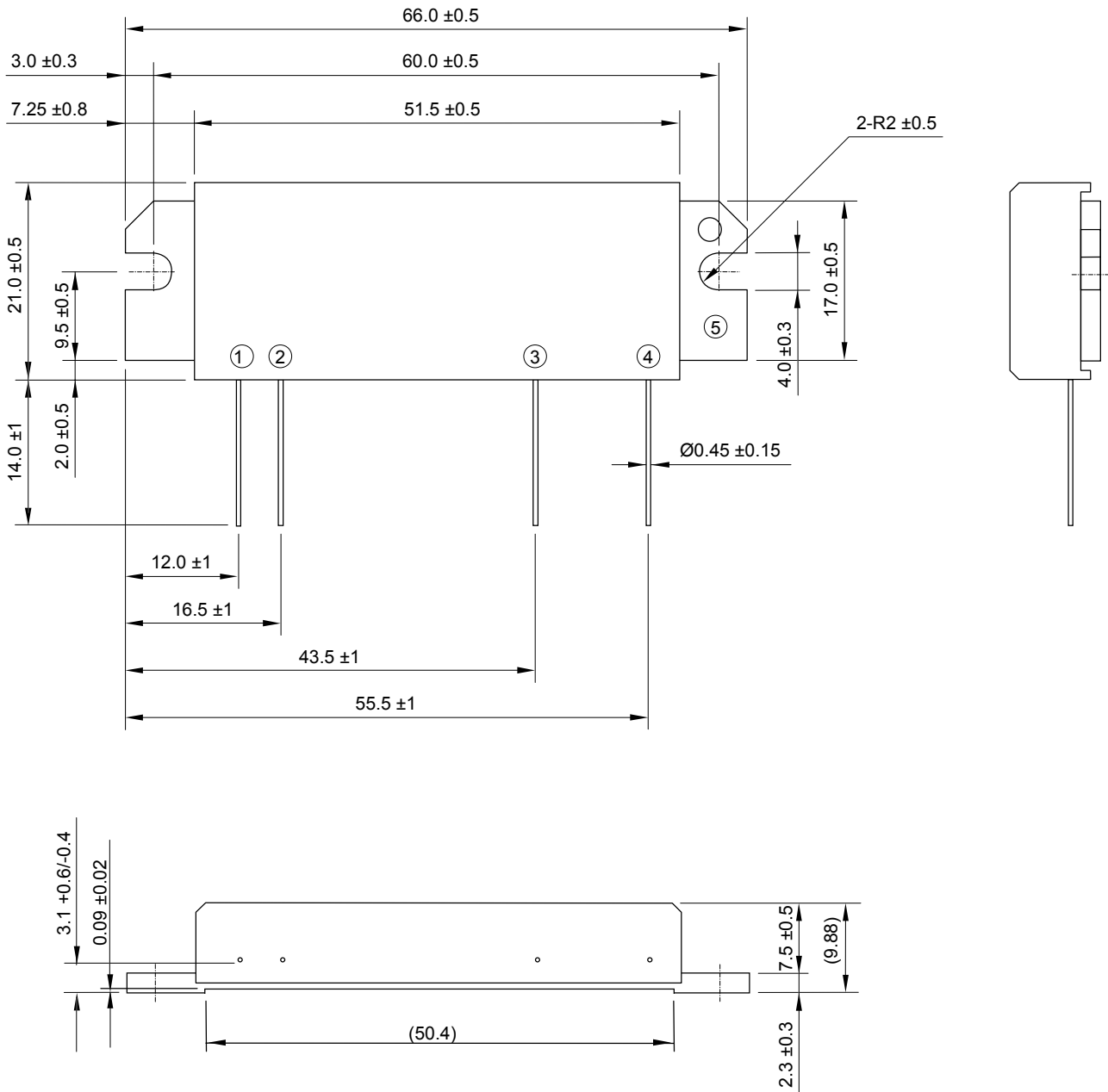


OUTPUT POWER and EFFICIENCY versus GATE VOLTAGE





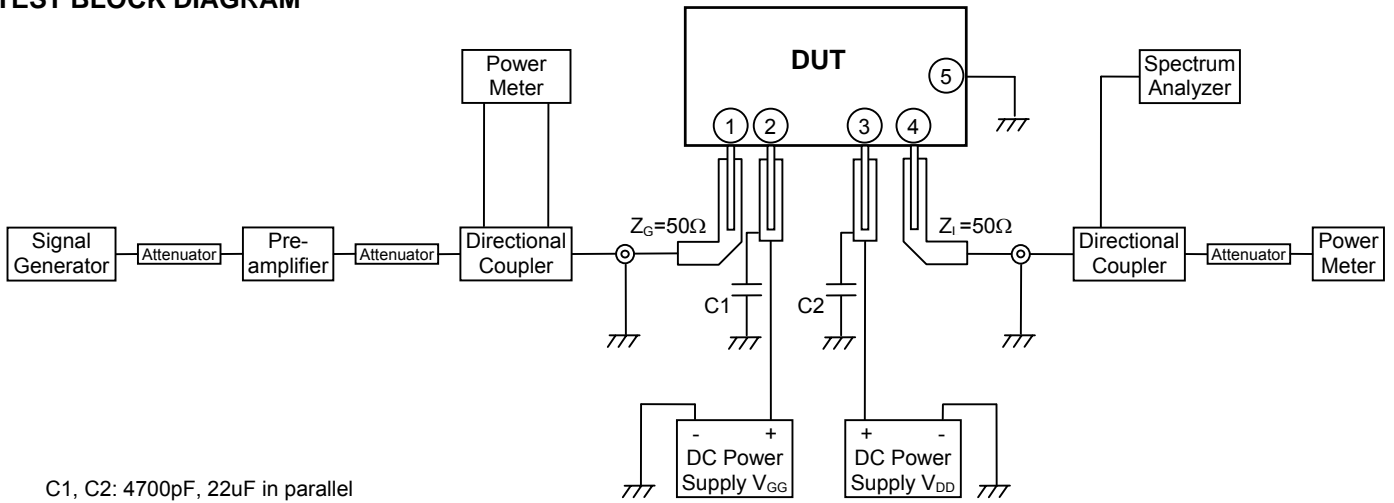
OUTLINE DRAWING (mm)



- ① RF Input ( $P_{in}$ )
- ② Gate Voltage ( $V_{GG}$ )
- ③ Drain Voltage ( $V_{DD}$ )
- ④ RF Output ( $P_{out}$ )
- ⑤ RF Ground (Case)



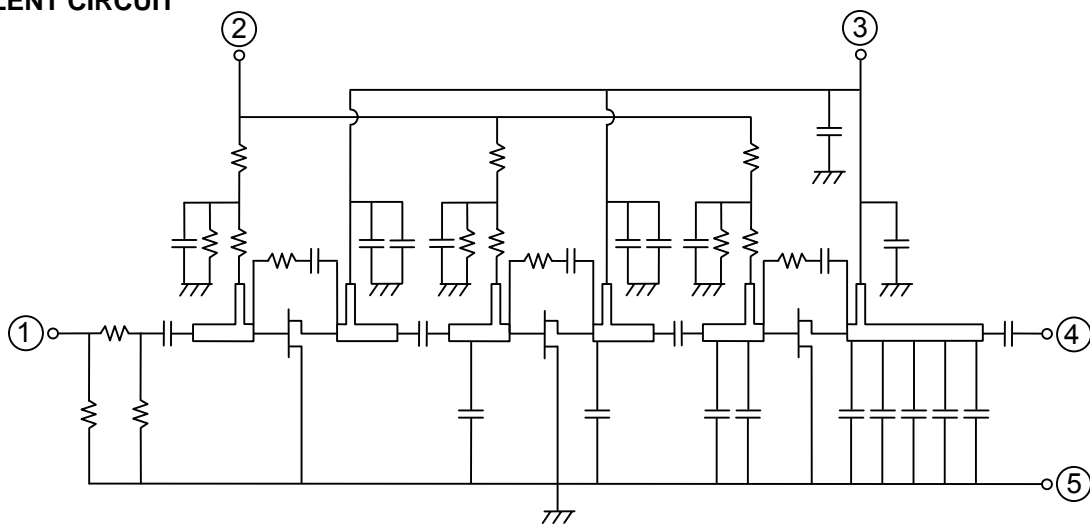
### TEST BLOCK DIAGRAM



C1, C2: 4700pF, 22uF in parallel

- ① RF Input ( $P_{in}$ )
- ② Gate Voltage ( $V_{GG}$ )
- ③ Drain Voltage ( $V_{DD}$ )
- ④ RF Output ( $P_{out}$ )
- ⑤ RF Ground (Case)

### EQUIVALENT CIRCUIT





**PRECAUTIONS, RECOMMENDATIONS and APPLICATION INFORMATION:**

**Construction:**

This module consists of an alumina substrate soldered on a copper flange. For mechanical protection a plastic cap is attached by Silicone. The MOSFET transistor chips are die bonded onto metal, wire bonded to the substrate and coated by resin. Lines on the substrate (eventually inductors), chip capacitors and resistors form the bias and matching circuits. Wire leads soldered onto the alumina substrate provide DC and RF connection.

Following conditions shall be avoided:

- a) Bending forces on the alumina substrate (for example during screwing or by fast thermal changes)
- b) Mechanical stress on the wire leads (for example by first soldering then screwing or by thermal expansion)
- c) Defluxing solvents reacting with the resin coating the MOSFET chips (for example Trichloroethylene)
- d) Frequent on/off switching causing thermal expansion of the resin
- e) ESD, surge, overvoltage in combination with load VSWR, oscillation, etc.

**ESD:**

This MOSFET module is sensitive to ESD voltages down to 1000V. Appropriate ESD precautions are required.

**Mounting:**

The heat sink flatness shall be less than 50µm (not flat heat sink or particles between module and heat sink may cause the ceramic substrate in the module to crack by bending forces, either immediately when screwing or later when thermal expansion forces are added).

Thermal compound between module and heat sink is recommended for low thermal contact resistance and to reduce the bending stress on the ceramic substrate caused by temperature difference to the heat sink.

The module shall first be screwed to the heat sink, after this the leads can be soldered to the PCB.

M3 screws are recommended with tightening torque 0.4 to 0.6Nm.

**Soldering and Defluxing:**

This module is designed for manual soldering.

The leads shall be soldered after the module is screwed onto the heat sink.

The temperature of the lead (terminal) soldering should be lower than 350°C and shorter than 3 second.

Ethyl Alcohol is recommend for removing flux. Trichloroethylene solvents must not be used (they may cause bubbles in the coating of the transistor chips which can lift off the bond wires).

**Thermal Design of the Heat Sink:**

At  $P_{out}=30W$ ,  $V_{DD}=12.5V$  and  $P_{in}=50mW$  each stage transistor operating conditions are:

| Stage           | $P_{in}$<br>(W) | $P_{out}$<br>(W) | $R_{th(ch-case)}$<br>(°C/W) | $I_{DD} @ \eta_T=40\%$<br>(A) | $V_{DD}$<br>(V) |
|-----------------|-----------------|------------------|-----------------------------|-------------------------------|-----------------|
| 1 <sup>st</sup> | 0.05            | 1.5              | 29.0                        | 0.18                          | 12.5            |
| 2 <sup>nd</sup> | 1.5             | 9.0              | 2.4                         | 1.60                          |                 |
| 3 <sup>rd</sup> | 9.0             | 30.0             | 1.2                         | 4.20                          |                 |

The channel temperatures of each stage transistor  $T_{ch} = T_{case} + (V_{DD} \times I_{DD} - P_{out} + P_{in}) \times R_{th(ch-case)}$  are:

$$T_{ch1} = T_{case} + (12.5V \times 0.18A - 1.5W + 0.05W) \times 29.0^\circ C/W = T_{case} + 23.2^\circ C$$

$$T_{ch2} = T_{case} + (12.5V \times 1.60A - 9.0W + 1.50W) \times 2.4^\circ C/W = T_{case} + 30.0^\circ C$$

$$T_{ch3} = T_{case} + (12.5V \times 4.20A - 30.0W + 9.0W) \times 1.2^\circ C/W = T_{case} + 37.8^\circ C$$

For long term reliability the module case temperature  $T_{case}$  is better kept below 90°C. For an ambient temperature  $T_{air}=60^\circ C$  and  $P_{out}=30W$  the required thermal resistance  $R_{th(case-air)} = (T_{case} - T_{air}) / ((P_{out} / \eta_T) - P_{out} + P_{in})$  of the heat sink, including the contact resistance, is:

$$R_{th(case-air)} = (90^\circ C - 60^\circ C) / (30W/40\% - 30W + 0.05W) = 0.67^\circ C/W$$

When mounting the module with the thermal resistance of 0.67 °C/W, the channel temperature of each stage transistor is:

$$T_{ch1} = T_{air} + 53.2^\circ C$$

$$T_{ch2} = T_{air} + 60.0^\circ C$$

$$T_{ch3} = T_{air} + 67.8^\circ C$$

175°C maximum rating for the channel temperature ensures application under derated conditions.



**Output Power Control:**

Depending on linearity following 2 methods are recommended to control the output power:

a) Non-linear FM modulation:

By Gate voltage  $V_{GG}$ .

When the Gate voltage is close to zero, the RF input signal is attenuated up to 60dB and only a small leakage current is flowing from the battery into the Drain.

Around  $V_{GG}=3.5V$  the output power and Drain current increases strongly.

Around  $V_{GG}=4.5V$ , latest at  $V_{GG}=5V$ , the nominal output power becomes available.

b) Linear AM modulation:

By RF input power  $P_{in}$ .

The Gate voltage is used to set the Drain quiescent current for the required linearity.

**Oscillation:**

To test RF characteristic this module is put on a fixture with 2 bias decoupling capacitors each on Gate and Drain, a 4.700pF chip capacitor, located close to the module, and a 22 $\mu$ F electrolytic capacitor.

When an amplifier circuit around this module shows oscillation following may be checked:

a) Do the bias decoupling capacitors have a low inductance pass to the case of the module ?

b) Is the load impedance  $Z_L=50\Omega$  ?

c) Is the source impedance  $Z_G=50\Omega$  ?

**Frequent on/off switching:**

In Base Stations frequent on/off switching can result in reduced or no output power, when the resin that coats the transistor chips gets thermally expanded by the on/off switching. The bond wires in the resin will break after long time thermally induced mechanical stress.

**Quality and Failure in Time :**

Mitsubishi Electric is not liable for failures resulting from base station operation time or operating conditions exceeding those of mobile radios.

This module technology results from more than 20 years of experience, field proven in tens of millions of mobile radios. Currently, most returned modules show failures such as ESD, substrate crack, and transistor burnout, which are caused by improper handling or exceeding recommended operating conditions. Few degradation failures are found.

**Keep safety first in your circuit designs!**

Mitsubishi Electric Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of non-flammable material or (iii) prevention against any malfunction or mishap.