



SEMICONDUCTOR

FORWARD INTERNATIONAL ELECTRONICS LTD.

TECHNICAL DATA

78L05

LINEAR INTEGRATED CIRCUIT

### 3-Terminal 0.1A Positive Voltage Regulator

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#### FEATURES

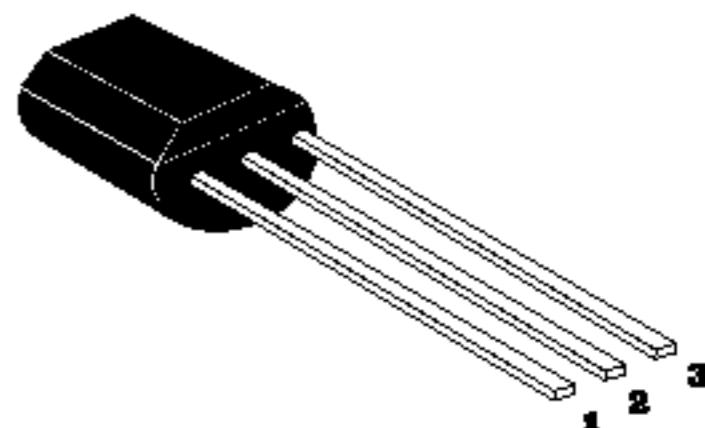
- \*Output current up to 100mA
- \*Fixed output voltage of 5V available
- \*Thermal overload shutdown protection
- \*Short circuit current limiting

#### ABSOLUTE MAXIMUM RATINGS

(Operating temperature range applies unless otherwise specified)

| Characteristic                       | Symbol           | Rating  | Unit |
|--------------------------------------|------------------|---------|------|
| Input voltage                        | V <sub>I</sub>   | 30      | V    |
| Output Current                       | I <sub>O</sub>   | 100     | mA   |
| Power Dissipation                    | P <sub>D</sub>   | 500     | mW   |
| Operating Junction Temperature Range | T <sub>OPR</sub> | 0~150   | °C   |
| Storage Temperature Range            | T <sub>STG</sub> | -55~150 | °C   |

Package: TO-92



| PIN:<br>STYLE | 1    | 2 | 3 |
|---------------|------|---|---|
|               | NO.1 | 0 | G |

#### ELECTRICAL CHARACTERISTICS at Tamb=25°C

(V<sub>I</sub>=10V, I<sub>O</sub>=40mA, 0°C < T<sub>j</sub> < 125°C, C<sub>i</sub>=0.33μF, C<sub>o</sub>=0.1μF, unless otherwise specified) (Note 1)

| Characteristic           | Symbol          | Min  | Typ | Max  | Unit | Test Conditions   |
|--------------------------|-----------------|------|-----|------|------|---|
| Output Voltage           | V <sub>O</sub>  | 4.8  | 5   | 5.2  | V    | T <sub>j</sub> =25°C  |
| Output Voltage           | V <sub>O</sub>  | 4.75 |     | 5.25 | V    | 7V ≤ V <sub>I</sub> ≤ 20V, I <sub>O</sub> =1mA-40mA                   |
| Output Voltage (Note 2)  | V <sub>O</sub>  | 4.75 |     | 5.25 | V    | V <sub>I</sub> =10V, I <sub>O</sub> =1mA-70mA                         |
| Load Regulation          | ΔV <sub>O</sub> |      | 11  | 60   | mV   | T <sub>j</sub> =25°C, I <sub>O</sub> =1mA-100mA                       |
| Load Regulation          | ΔV <sub>O</sub> |      | 5   | 30   | mV   | T <sub>j</sub> =25°C, I <sub>O</sub> =1mA-40mA                        |
| Line Regulation          | ΔV <sub>O</sub> |      | 55  | 150  | mV   | 7V ≤ V <sub>I</sub> ≤ 20V, I <sub>O</sub> =40mA, T <sub>j</sub> =25°C |
| Line Regulation          | ΔV <sub>O</sub> |      | 45  | 100  | mV   | 8V ≤ V <sub>I</sub> ≤ 20V, I <sub>O</sub> =40mA, T <sub>j</sub> =25°C |
| Quiescent Current        | I <sub>Q</sub>  |      | 4   | 6    | mA   | V <sub>IN</sub> =10V, I <sub>O</sub> =0mA, T <sub>j</sub> =25°C       |
| Quiescent Current Change | ΔI <sub>Q</sub> |      |     | 1.5  | mA   | 8V ≤ V <sub>I</sub> ≤ 20V   |
| Quiescent Current Change | ΔI <sub>Q</sub> |      |     | 0.1  | mA   | 1mA ≤ I <sub>O</sub> ≤ 40mA   |
| Output Noise Voltage     | V <sub>N</sub>  |      | 40  |      | uV   | 10Hz ≤ f ≤ 100kHz Ta=25°C   |
| Ripple Rejection         | RR              | 41   | 80  |      | dB   | 8V ≤ V <sub>I</sub> ≤ 18V, f=120Hz, T <sub>j</sub> =25°C              |
| Dropout Voltage          | V <sub>D</sub>  |      | 1.7 |      | V    | T <sub>j</sub> =25°C  |

Note1: The maximum steady state usable output current is dependent on input voltage, heat sinking, lead length of the package and copper pattern of PCB. The data above represent pulse test conditions with junction temperatures specified at the initiation of test.

Note2: Power dissipation<0.5W