



Polymeric PTC Resistor

Product: SMD1812P200TFT

Revision: A

Date: April 29, 2008

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Device Specification



ELECTRICAL CHARACTERISTICS

						Maximum Time To Trip		Resistance	
Part Number	I hold (A)	$I_{\text{trip}} $ (A)	V _{max} (Vdc)	I max (A)	$P_d^{\text{max.}}$ (W)	Current (A)	Time (Sec.)	R_{min} (Ω)	R_{1max} (Ω)
SMD1812P200TFT	2.00	3.50	8	100	0.80	8.00	2.00	0.020	0.070

Note: I_{hold} = Hold current: maximum current device will pass without tripping in 20°C still air.

I $_{\text{trip}}$ = Trip current: minimum current at which the device will trip in 20°C still air.

 V_{max} = Maximum voltage device can withstand without damage at rated current (I_{max})

 I_{max} = Maximum fault current device can withstand without damage at rated voltage (V_{max})

 P_d = Power dissipated from device when in the tripped state at 20°C still air.

R _{min}= Minimum resistance of device in initial (un-soldered) state.

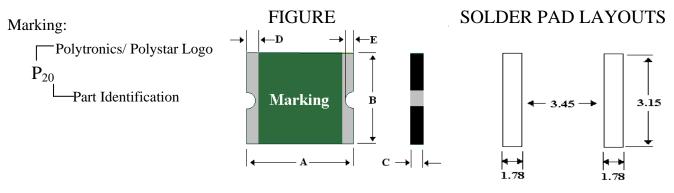
R $_{lmax}$ = Maximum resistance of device at 20°C measured one hour after tripping or reflow soldering of 260°C for 20 sec.

*Value specified were determined using the PWB with 0.030"*1.5oz copper traces.

Caution: Operation beyond the specified rating may result in damage and possible arcing and flame.

Recognitions:





Note: Polystar is Polytronics's manufacturing site in China. The Polystar ID marking shall appear on smallest package.

PHYSICAL DIMENSIONS (mm)

· /										
Part Number	A		В		С		D		Е	
	Min.	Max.								
SMD1812P200TFT	4.37	4.73	3.07	3.41	0.20	0.60	0.30	1.20	0.25	0.65

OSpecifications are subject to change without notice.

^{*}Customer should verify the device performance in their specified conditions.