

**General Description**

The MDD3N50 uses advanced Magnachip’s MOSFET Technology, which provides low on-state resistance, high switching performance and excellent quality.

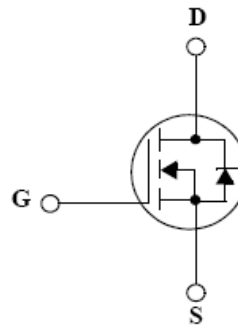
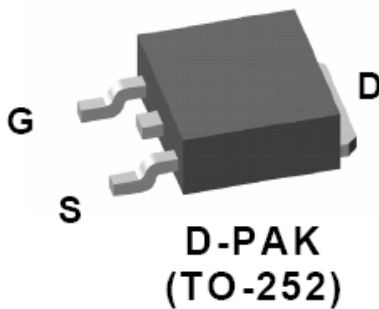
MDD3N50 is suitable device for SMPS, HID and general purpose applications.

**Features**

- $V_{DS} = 500V$
  - $I_D = 2.8A$
  - $R_{DS(ON)} \leq 2.5\Omega$
- @ $V_{GS} = 10V$   
@ $V_{GS} = 10V$

**Applications**

- Power Supply
- PFC
- Ballast



**Absolute Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Rating	Unit	
Drain-Source Voltage	$V_{DSS}$	500	V	
Gate-Source Voltage	$V_{GSS}$	±30	V	
Continuous Drain Current	$I_D$	$T_C=25^\circ C$	2.8	A
		$T_C=100^\circ C$	1.7	A
Pulsed Drain Current <sup>(1)</sup>	$I_{DM}$	11.2	A	
Power Dissipation	$P_D$	$T_C=25^\circ C$	45	W
		Derate above 25 °C	0.36	W/°C
Peak Diode Recovery dv/dt <sup>(3)</sup>	Dv/dt	4.5	V/ns	
Repetitive Pulse Avalanche Energy <sup>(4)</sup>	$E_{AR}$	4.5	mJ	
Single Pulse Avalanche Energy <sup>(4)</sup>	$E_{AS}$	170	mJ	
Junction and Storage Temperature Range	$T_J, T_{stg}$	-55~150	°C	

**Thermal Characteristics**

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient <sup>(1)</sup>	$R_{\theta JA}$	62.5	°C/W
Thermal Resistance, Junction-to-Case <sup>(1)</sup>	$R_{\theta JC}$	2.75	

## Ordering Information

Part Number	Temp. Range	Package	Packing	RoHS Status
MDD3N50RH	-55~150°C	D-Pak	Tape and reel	Halogen Free

## Electrical Characteristics (Ta =25°C)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$I_D = 250\mu A, V_{GS} = 0V$	500	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	3.0	-	5.0	
Drain Cut-Off Current	$I_{DSS}$	$V_{DS} = 500V, V_{GS} = 0V$	-	-	1	$\mu A$
Gate Leakage Current	$I_{GSS}$	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	-	100	nA
Drain-Source ON Resistance	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 1.4A$	-	2.1	2.5	$\Omega$
Forward Transconductance	$g_{fs}$	$V_{DS} = 30V, I_D = 1.4A$	-	5	-	S
<b>Dynamic Characteristics</b>						
Total Gate Charge	$Q_g$	$V_{DS} = 400V, I_D = 2.8A, V_{GS} = 10V^{(3)}$	-	6	8	nC
Gate-Source Charge	$Q_{gs}$		-	2	2.5	
Gate-Drain Charge	$Q_{gd}$		-	2.5	3.5	
Input Capacitance	$C_{iss}$	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$	-	300	390	pF
Reverse Transfer Capacitance	$C_{rss}$		-	1.7	2.5	
Output Capacitance	$C_{oss}$		-	30	40	
Turn-On Delay Time	$t_{d(on)}$	$V_{GS} = 10V, V_{DS} = 250V, I_D = 2.8A, R_G = 25\Omega^{(3)}$	-	7	25	ns
Rise Time	$t_r$		-	16	45	
Turn-Off Delay Time	$t_{d(off)}$		-	17	45	
Fall Time	$t_f$		-	14	40	
<b>Drain-Source Body Diode Characteristics</b>						
Maximum Continuous Drain to Source Diode Forward Current	$I_S$	$I_S = 2.8A, V_{GS} = 0V$	-	2.8	-	A
Source-Drain Diode Forward Voltage	$V_{SD}$		-	-	1.4	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_F = 2.8A, di/dt = 100A/\mu s^{(3)}$	-	220	-	ns
Body Diode Reverse Recovery Charge	$Q_{rr}$		-	1.2	-	$\mu C$

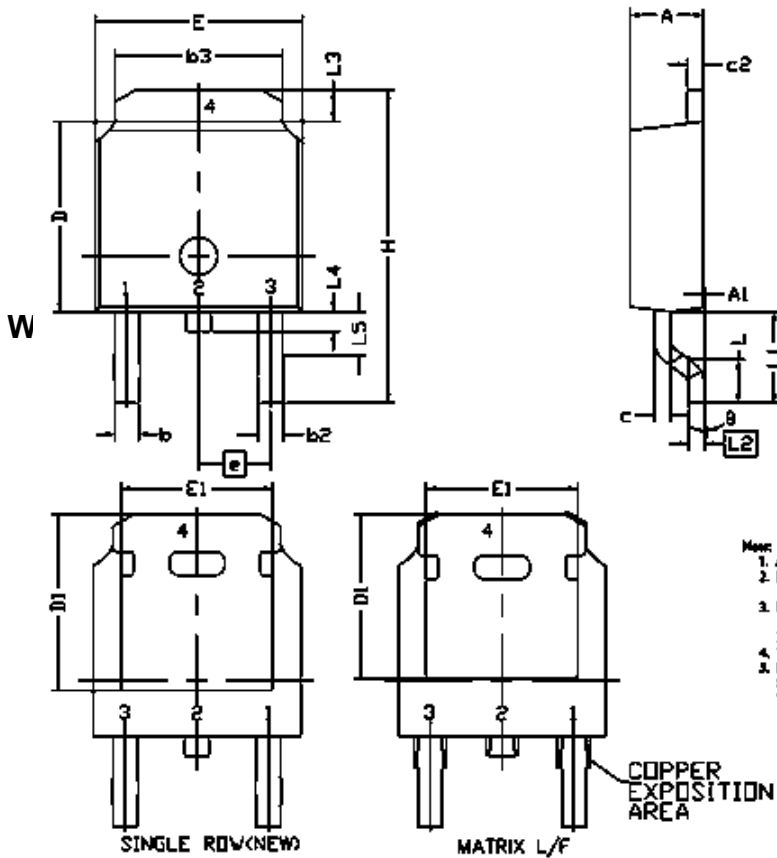
Note :

1. Pulse width is based on  $R_{\theta JC}$  &  $R_{\theta JA}$  and the maximum allowed junction temperature of 150°C.
2. Pulse test: pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ , pulse width limited by junction temperature  $T_J(MAX) = 150^\circ C$ .
3.  $I_{SD} \leq 3.0A$ ,  $di/dt \leq 200A/\mu s$ ,  $V_{DD} = 50V$ ,  $R_G = 25\Omega$ , Starting  $T_J = 25^\circ C$
4.  $L = 22.0mH$ ,  $I_{AS} = 3.0A$ ,  $V_{DD} = 50V$ ,  $R_G = 25\Omega$ , Starting  $T_J = 25^\circ C$

Physical Dimension

D-PAK, 3L

Dimensions are in millimeters, unless otherwise specified



SYMBOL	DIMENSIONAL RIGHTS		
	MIN	NOM	MAX
E	6.40	6.60	6.731
L	1.40	1.52	1.77
L1	2.743 REF		
L2	0.508 BSC		
L3	0.89	--	1.27
L4	0.64	--	1.01
L5	--	--	--
D	6.00	6.10	6.223
H	9.40	10.00	10.40
b	0.64	0.76	0.88
b2	0.77	0.84	1.14
b3	5.21	5.34	5.46
e	2.286 BSC		
A	2.20	2.30	2.39
A1	0	--	0.127
c	0.45	0.50	0.60
c2	0.45	0.50	0.58
D1	5.30	--	--
E1	4.40	--	--
θ	0°	--	10°

Note:

1. All Dimensions Are In mm.
2. Package Body Steps Exclude Mold Flash, Protrusion Or Gate Bump. Mold Flash, Protrusion Or Gate Bump Shall Not Exceed 0.10 mm Per Side.
3. Package Body Steps Determined At The Outermost Extremity Of The Plastic Body Exclusive Of Mold Flash, Gate Bump And Interlead Flash, Not Including Any Fillets Between The Top And Bottom Of The Plastic Body.
4. The Package Top May Be Smaller Than The Package Bottom.
5. Dimension "W" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.10 mm Total In Excess Of "W" Dimension At Maximum Molded Condition. The Dambar Cannot Be Located On The Lower Surface Of The Foot.

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