DSC1101 | DSC1121

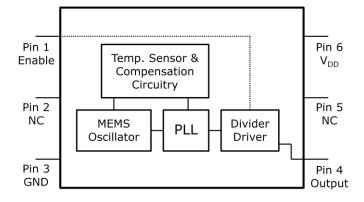
Low-Jitter Precision CMOS Oscillator

General Description

The DSC1101 & DSC1121 series of high performance oscillators utilizes a proven silicon MEMS technology to provide excellent jitter and stability over a wide range of supply voltages and temperatures. By eliminating the need for quartz or SAW technology, MEMS oscillators significantly enhance reliability and accelerate product development, while meeting stringent clock performance criteria for а variety of communications, storage, and networking applications.

DSC1101 has a standby feature allowing it to completely power-down when EN pin is pulled low; whereas for DSC1121, only the outputs are disabled when EN is low. Both oscillators are available in industry standard packages, including the small 3.2x2.5 mm², and are "drop-in" replacement for standard 4-pin CMOS quartz oscillators.

Block Diagram



Output Enable Modes

EN Pin	DSC1101	DSC1121
High	Output Active	Output Active
NC	Output Active	Output Active
Low	Standby	Output Disabled

Features

E025

• Low RMS Phase Jitter: <1 ps (typ)

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Preliminary Datasheet

- High Stability: ±10, ±25, ±50 ppm
- Wide Temperature Range
 - $_{\odot}$ Automotive: -55° to 125° C
 - $_{\odot}$ Ext. Industrial: -40° to 105° C
 - $_{\odot}$ Industrial: -40° to 85° C
 - $_{\odot}$ Ext. commercial: -20° to 70° C
- High Supply Noise Rejection: -50 dBc
- Short Lead Times: <3 Weeks
- Wide Freq. Range: 10 to 170 MHz
- Small Industry Standard Footprints o 3.2x2.5, 5.0x3.2, and 7.0x5.0 mm
- Excellent Shock & Vibration Immunity • Qualified to MIL-STD-883
- High Reliability • 20x better MTF than quartz oscillators
- Low Current Consumption
- Supply Range of 2.25 to 3.6 V
- Standby & Output Enable Function
- Lead Free & RoHS Compliant

Applications

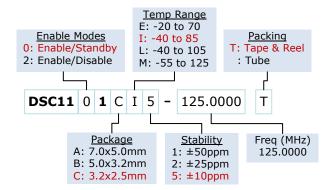
- Storage Area Networks • SATA, SAS, Fibre Channel
- Passive Optical Networks • EPON, 10G-EPON, GPON, 10G-PON
- Ethernet o 1G, 10GBASE-T/KR/LR/SR, and FCoE
- HD/SD/SDI Video & Surveillance
- PCI Express
- DisplayPort



Absolute Maximum Ratings

Item	Min	Мах	Unit	Condition
Supply Voltage	-0.3	+4.0	V	
Input Voltage	-0.3	V _{DD} +0.3	V	
Junction Temp	-	+150	°C	
Storage Temp	-55	+150	°C	
Soldering Temp	-	+260	°C	40sec max.
ESD	-		V	
HBM		4000		
MM		400		
CDM		1500		

Ordering Code



Specifications

Parameter		Condition	Min.	Тур.	Max.	Unit
Supply Voltage ¹	V_{DD}		2.25		3.6	V
Supply Current	I _{DD}	EN pin low – output is disabled DSC1101 DSC1121		20	0.1 26	mA
Frequency Stability Ext Comm. & Ind. only All temp ranges All temp ranges	Δf	Includes frequency variations due to initial tolerance, temp. and power supply voltage			±10 ±25 ±50	ppm
Aging	Δf	1 year @25°C			±5	ppm
Startup Time ²	t _{su}	T=25°C			10	ms
Input Logic Levels Input logic high Input logic low	V _{IH} V _{IL}		0.75xV _{DD} -		- 0.25xV _{DD}	v
Output Disable Time ³	t _{DA}				100	ns
Output Enable Time	t _{EN}	DSC1101 DSC1121			10 0.005	ms
Enable Pull-Up Resistor ⁴		Pull-up resistor exist		33		kΩ
		CMOS Output				
Supply Current ⁴	I_{DD}	output is enabled $C_L=15pF$, $F_0=125 MHz$		31	35	mA
Output Logic Levels Output logic high Output logic low	V _{OH} V _{OL}	I=±6mA	0.9xV _{DD} -		- 0.1xV _{DD}	v
Output Transition time ³ Rise Time Fall Time	t _R t _F	20% to 80% C _L =15pF		1.1 1.3	2 2	ns
Frequency	f ₀	All temp range except Auto Auto temp range	10		170 100	MHz
Output Duty Cycle	SYM		45		55	%
Period Jitter	J _{PER}	C _L =2pF C _L =15pF		4.3 6		ps _{RMS}
Integrated Phase Noise	J _{PH}	200kHz to 20MHz @ 125MHz 100kHz to 20MHz @ 125MHz 12kHz to 20MHz @ 125MHz		0.35 0.5 1.8	3	ps _{RMS}
Spurious Frequencies					-50	dBc

Notes:

1.

Pin 4 V_{DD} should be filtered with 0.1uf capacitor. Output frequency will be within 100ppm of final stable output frequency. 2.

3. Output Waveform and Test Circuit figures below define the parameters.

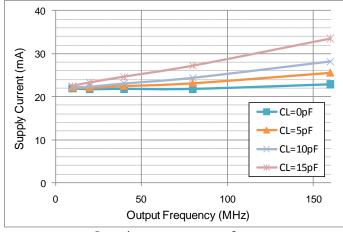
4. Output is enabled if pad is floated or not connected.

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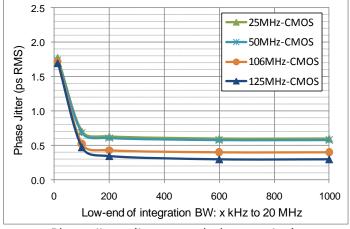
DSC1101 DSC1121

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Nominal Performance Parameters (Unless specified otherwise: T=25° C, V_{DD}=3.3 V)

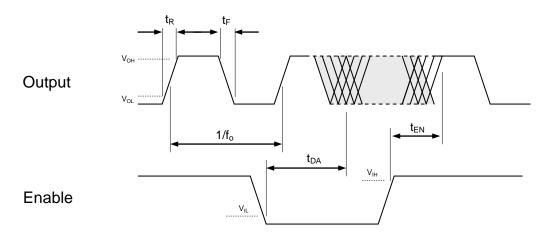


Supply current over freq

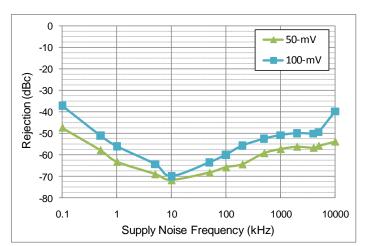


Phase jitter (integrated phase noise)

Output Waveform



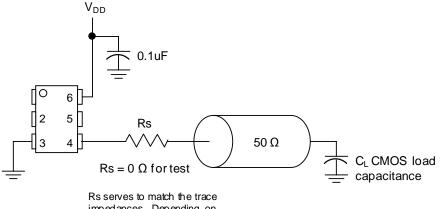
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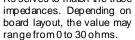


Power supply rejection ratio

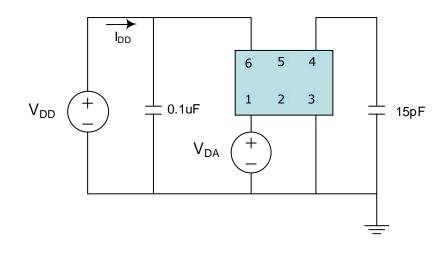
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Typical Termination Scheme

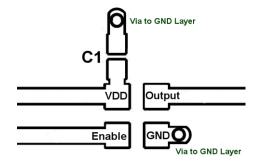




Test Circuit



Board Layout (recommended)



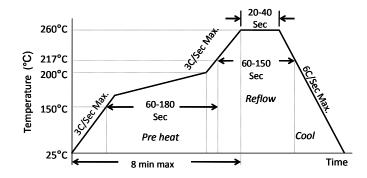
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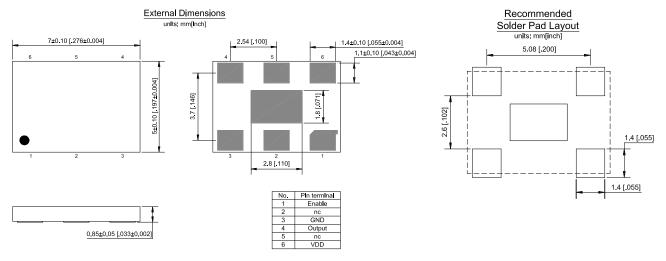
Solder Reflow Profile



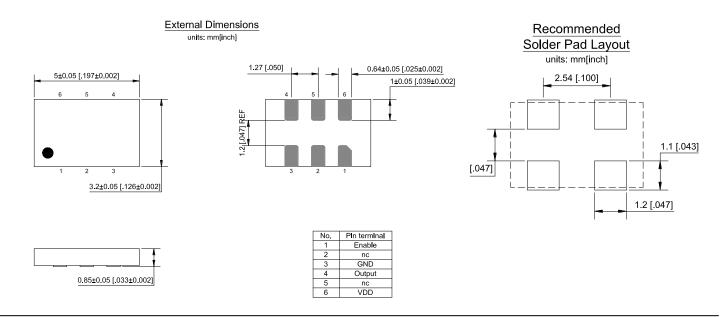
MSL 1 @ 260°C refer to JSTD-020C				
Ramp-Up Rate (200°C to Peak Temp)	3°C/Sec Max.			
Preheat Time 150°C to 200°C	60-180 Sec			
Time maintained above 217°C	60-150 Sec			
Peak Temperature	255-260°C			
Time within 5°C of actual Peak	20-40 Sec			
Ramp-Down Rate	6°C/Sec Max.			
Time 25°C to Peak Temperature	8 min Max.			

Package Dimensions

7.0 x 5.0 mm Plastic Package



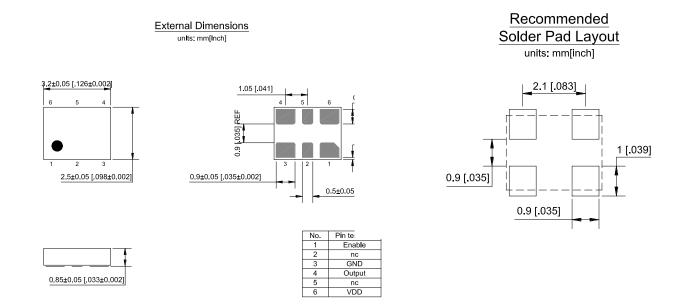
5.0 x 3.2 mm Plastic Package



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3.2 x 2.5 mm Plastic Package



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