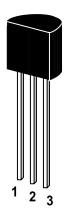
### **NPN Silicon Epitaxial Planar Transistor**

for switching and amplifier applications. Especially suitable for AF-driver stages and low power output stages.

The transistor is subdivided into three groups, G, H and I, according to its DC current gain. As complementary type the PNP transistor ST 9012 is recommended.

On special request, these transistors can be manufactured in different pin configurations.



1. Emitter 2. Base 3. Collector

TO-92 Plastic Package Weight approx. 0.19g

#### Absolute Maximum Ratings ( $T_a = 25^{\circ}C$ )

	Symbol	Value	Unit
Collector Emitter Voltage	V <sub>CEO</sub>	30	V
Emitter Base Voltage	$V_{EBO}$	5	V
Collector Current	I <sub>C</sub>	800	mA
Peak Collector Current	I <sub>CM</sub>	1	А
Base Current	I <sub>B</sub>	100	mA
Power Dissipation	P <sub>tot</sub>	625 <sup>1)</sup>	mW
Junction Temperature	T <sub>j</sub>	150	°C
Storage Temperature Range	Ts	-55 to +150	°C
1) Valid provided that leads are kept at ambie	ent temperature at a distan	ce of 2 mm from case	







## ST 9013

### Characteristics at T<sub>amb</sub>=25 °C

	Symbol	Min.	Тур.	Max.	Unit
DC Current Gain					
at V <sub>CE</sub> =1V, I <sub>C</sub> =50mA					
Current Gain Group G	$h_{FE}$	110	-	183	-
Н	$h_{FE}$	177	-	250	-
I i	$h_{FE}$	250	-	380	-
at V <sub>CE</sub> =1V, I <sub>C</sub> =500mA	$h_{FE}$	40	-	-	-
Collector Cutoff Current					
at V <sub>CB</sub> =31V	$I_{CBO}$	-	-	100	nA
Collector Emitter Breakdown Voltage					
at I <sub>C</sub> =1mA	$V_{(BR)CEO}$	30	-	-	V
Emitter Base Cutoff Current					
at V <sub>EB</sub> =5.1V	$I_{EBO}$	-	-	100	nA
Collector Saturation Voltage					
at $I_C$ =500mA, $I_B$ =20mA	$V_{CE(sat)}$	-	-	0.5	V
Base Saturation Voltage					
at $I_C$ =500mA, $I_B$ =20mA	$V_{BE(sat)}$	-	-	1.2	V
Base Emitter Voltage					
at $V_{CE}$ =1V, $I_{C}$ =50mA	$V_{BE}$	0.6	-	0.75	V
Gain Bandwidth Product					
at $V_{CE}$ =5V, $I_C$ =10mA, f=50MHz	$f_T$	-	100	-	MHz
Collector Base Capacitance					
at V <sub>CB</sub> =10V, f=1MHz	$C_{CBO}$	-	12	-	pF
Thermal Resistance Junction to Ambient	R <sub>thA</sub>	-	-	200 <sup>1)</sup>	K/W

Valid provided that leads are kept at ambient temperature at a distance of 2 mm from case









# SEMTECH ELECTRONICS LTD.

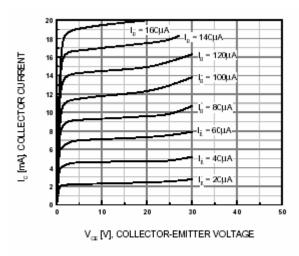


Figure 1. Static Characteristic

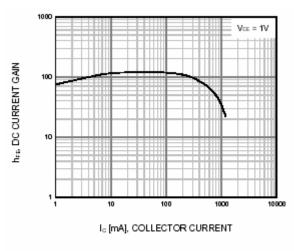


Figure 2. DC current Gain

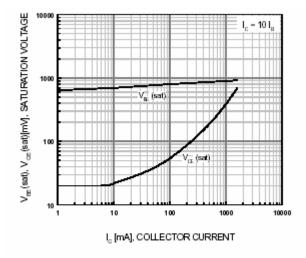


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

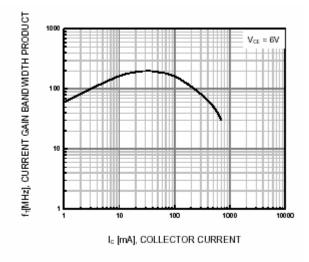


Figure 4. Current Gain Bandwidth Product







