

April 1987

# LM3189 FM IF System

### **General Description**

The LM3189N is a monolithic integrated circuit that provides all the functions of a comprehensive FM IF system. The block diagram of the LM3189N includes a three stage FM IF amplifier/limiter configuration with level detectors for each stage, a doubly balanced quadrature FM detector and an audio amplifier that features the optional use of a muting (squelch) circuit.

The advanced circuit design of the IF system includes desirable deluxe features such as programmable delayed AGC for the RF tuner, an AFC drive circuit, and an output signal to drive a tuning meter and/or provide stereo switching logic. In addition, internal power supply regulators maintain a nearly constant current drain over the voltage supply range of  $+8.5\mathrm{V}$  to  $+16\mathrm{V}$ .

The LM3189N is ideal for high fidelity operation. Distortion in an LM3189N FM IF system is primarily a function of the phase linearity characteristic of the outboard detector coil.

The LM3189N has all the features of the LM3089N plus additions.

The LM3189N utilizes the 16-lead dual-in-line plastic package and can operate over the ambient temperature range of  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}.$ 

### **Features**

- $\blacksquare$  Exceptional limiting sensitivity: 12  $\mu$ V typ at -3 dB point
- Low distortion: 0.1% typ (with double-tuned coil)
- Single-coil tuning capability
- Improved (S + N)/N ratio
- Externally programmable recovered audio level
- Provides specific signal for control of inter-channel muting (squelch)
- Provides specific signal for direct drive of a tuning meter
- On channel step for search control
- Provides programmable AGC voltage for RF amplifier
- Provides a specific circuit for flexible audio output
- Internal supply voltage regulators
- Externally programmable ON channel step width, and deviation at which muting occurs

# Block Diagram TO INTERNAL DI 11 TO INTERNAL DI 11

All resistance values are in  $\boldsymbol{\Omega}$ 

\*L tunes with 100 pF (C) at 10.7 MHz,  $Q_0 \simeq 75$  (Toko No. KACS K586HM or equivalent)

TL/H/7960-1

# **Absolute Maximum Ratings**

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage Between Pin 11 and Pins 4, 14

DC Current Out of Pin 12

DC Current Out of Pin 13

DC Current Out of Pin 15

2 mA

Power Dissipation (Note 2) 1500 mW Operating Temperature Range  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$  Storage Temperature Range  $-65^{\circ}\text{C}$  to  $+150^{\circ}\text{C}$  Lead Temperature (Soldering, 10 sec.) 260°C

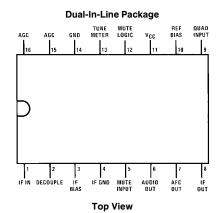
# **Electrical Characteristics** $T_A = 25^{\circ}C$ , $V^+ = 12V$

Symbol	Parameter	Conditions (See Single-Tuned Test Circuit)		Min	Тур	Max	Units
STATIC (D	C) CHARACTERISTICS						
I <sub>11</sub>	Quiescent Circuit Current	No Signal Input, Non Muted		20	31	44	mA
V1 V2 V3 V15 V10	DC Voltages: Terminal 1 (IF Input) Terminal 2 (AC Return to Input) Terminal 3 (DC Bias to Input) Terminal 15 (RF AGC) Terminal 10 (DC Reference)			1.2 1.2 1.2 7.5 5	2.0 2.0 2.0 9.5 5.75	2.4 2.4 2.4 11 6	V V V V
DYNAMIC	CHARACTERISTICS						
V <sub>I</sub> (lim)	Input Limiting Voltage ( $-3$ dB Point)		$f_0 = 10.7 \text{ MHz},$ $f_{mod} = 400 \text{ Hz},$ Deviation $\pm 75 \text{ kHz}$		12	25	μV
AMR	AM Rejection (Term. 6)	$V_{IN} = 0.1V$		45	55		dB
V <sub>O</sub> (AF)	Recovered AF Voltage (Term. 6)	AM Mod. = 30%		325	500	650	mV
THD	Total Harmonic Distortion (Note 1) Single Tuned (Term. 6) Double Tuned (Term. 6)	V <sub>IN</sub> = 0.1V			0.5 0.1	1	% %
S + N/N	Signal Plus Noise to Noise Ratio (Term. 6)			65	80		dB
f <sub>DEV</sub>	Deviation Mute Frequency		$f_{mod} = 0$		±40		kHz
V16	RF AGC Threshold				1.25		٧
V12	On Channel Step	V <sub>IN</sub> = 0.1V	$f_{\mbox{DEV}} < \pm 40 \mbox{ kHz} \ f_{\mbox{DEV}} > \pm 40 \mbox{ kHz}$		0 5.6		٧

Note 1: THD characteristics are essentially a function of the phase characteristics of the network connected between terminals 8, 9, and 10.

Note 2: For operation in ambient temperatures above 25°C, the device must be derated based on a 150°C maximum junction temperature and a thermal resistance of 80°C/W junction to ambient.

# **Connection Diagram**

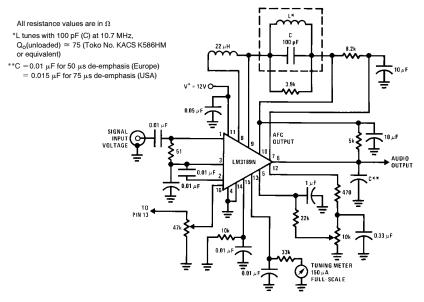


TL/H/7960-2

Order Number LM3189N See NS Package Number N16E

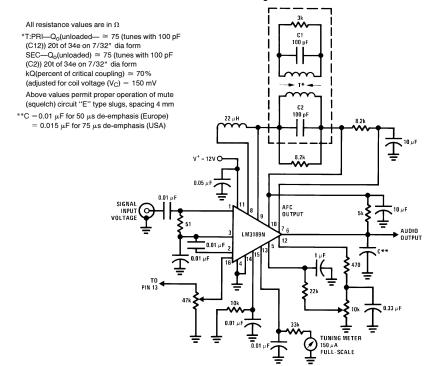
# **Test Circuits**

### Test Circuit for LM3189N Using a Single-Tuned Detector Coil



TL/H/7960-3

### Test Circuit for LM3189N Using a Double-Tuned Detector Coil



TL/H/7960-4

# Complete FM IF System for High Quality Tuners

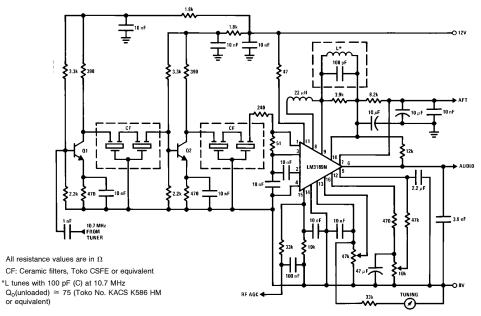
The circuit provides a complete FM IF system for a high quality receiver. Either one or two stages of amplification and bandpass filtering may be desired, depening on the

receiver requirements. See graph for Typical Limiting and Noise Characteristics for each circuit configuration which can be compared to the LM3189N alone.

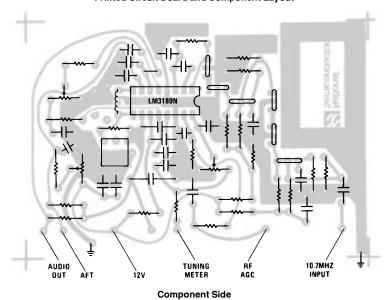
TL/H/7960-5

TL/H/7960-6

### Complete FM IF System for High Quality Receivers



### **Printed Circuit Board and Component Layout**



# **Typical Performance Characteristics**

AM Rejection (30% Mod) vs

IF Input Signal OUTPUT (0 dB REF = RECOVERED AUDIO FOR 100% MOD) (dB) -20 -30 -40 -50

-60

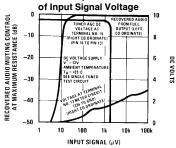
-70

100 1k 10k 100k 1000k IF INPUT VOLTAGE (μV)

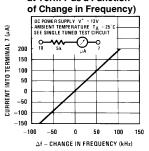
**Mute Control Output** (Pin 12) vs IF Input Signal OUTPUT VOLTAGE (PIN 12) 3 5 10 20 30 IF INPUT VOLTAGE (uV)

**Typical Audio Attenuation** (Pin 6) vs Mute Input Voltage (Pin 5) **AUDIO ATTENUATION (PIN 6) (dB)** 10 20 30 40 50 60 70 0.5 1.5 2.5 MUTE INPUT VOLTAGE (PIN 5) (V)

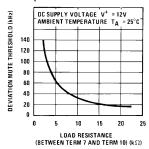
Muting Action, Tuner AGC, and Tuning Meter Output as a Function



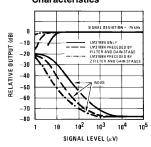
**AFC Characteristics (Current** at Term 7 as a Function



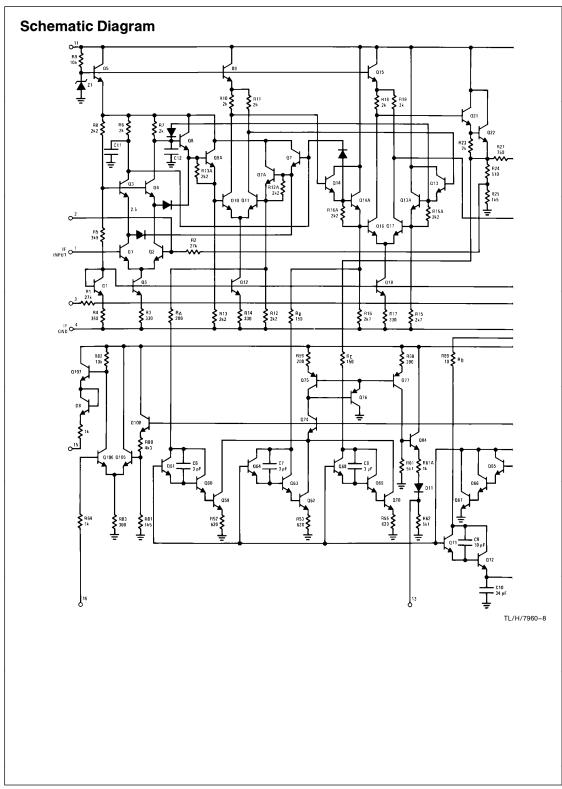
**Deviation Mute Threshold as** a Function of Load Resistance (Between Term 7 and Term 10)

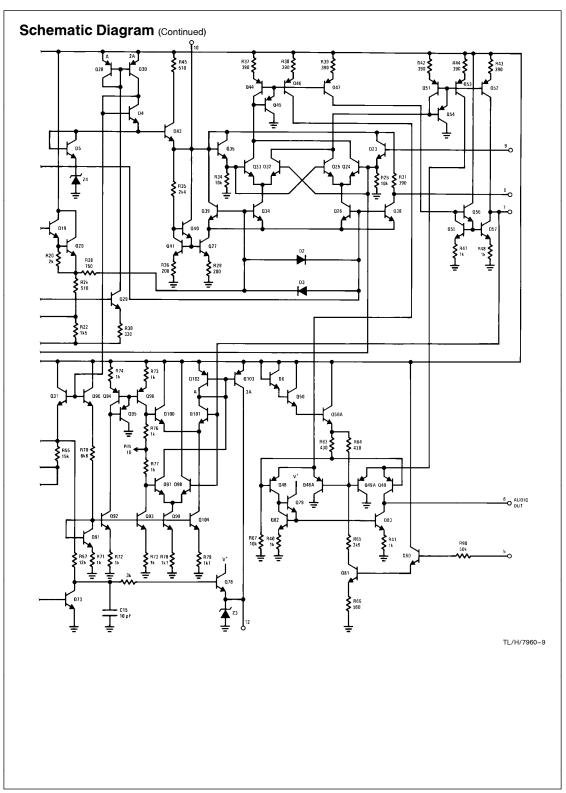


**Typical Limiting and Noise** Characteristics

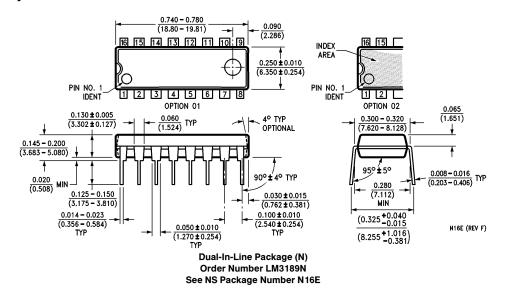


TL/H/7960-7





# Physical Dimensions inches (millimeters)



### LIFE SUPPORT POLICY

NATIONAL'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF NATIONAL SEMICONDUCTOR CORPORATION. As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform, when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.



National Semiconductor Corporation 1111 West Bardin Road Arlington, TX 76017 Tel: 1(800) 272-9959 Fax: 1(800) 737-7018 National Semiconductor Europe

Fax: (+49) 0-180-530 85 86
Email: cnjwge@tevm2.nsc.com
Deutsch Tel: (+49) 0-180-530 85 85
English Tel: (+49) 0-180-532 78 32
Français Tel: (+49) 0-180-532 93 58
Italiano Tel: (+49) 0-180-534 16 80

National Semiconductor Hong Kong Ltd. 13th Floor, Straight Block, Ocean Centre, 5 Canton Rd. Tsimshatsui, Kowloon Hong Kong Tel: (852) 2737-1600 Fax: (852) 2736-9960 National Semiconductor Japan Ltd. Tel: 81-043-299-2309 Fax: 81-043-299-2408