

Description

The RLST23A712C transient voltage suppressor (TVS) diode is designed for asymmetrical (12V to -7V) protection in multi-point data transmission standard RS-485 applications.

The RLST23A712C may be used to protect devices from transient voltages resulting from electrostatic discharge (ESD), electrical fast transients (EFT), and lightning.

The RLST23A712C features 400 Watts ($t_p = 8/20\mu s$) of power handling capability to accommodate the higher transient voltage levels which may be expected in extended common mode applications. This provides higher equipment reliability and eliminates the "guess work" required when using zener diodes that are not rated to handle such transient conditions.

The RLST23A712C replaces four discrete components by integrating two 12V and two 7V TVS diodes in a single package. The integrated design aids in reducing voltage over-shoot associated with trace inductance. The low clamping voltage of the RLST23A712C minimizes the stress on the protected transceiver. The SOT23 package allows flexibility in the design of "crowded" circuit boards.

Features

- 400 watts peak pulse power ($t_p = 8/20\mu s$)
- Transient protection for asymmetrical data lines to IEC 61000-4-2 (ESD) $\pm 15kV$ (air), $\pm 8kV$ (contact) IEC 61000-4-4 (EFT) 40A (5/50ns) IEC 61000-4-5 (Lightning) 12A (8/20 μs)
- Protects two +12V to -7V lines
- Low capacitance
- Low clamping voltage
- Solid-state silicon avalanche technology

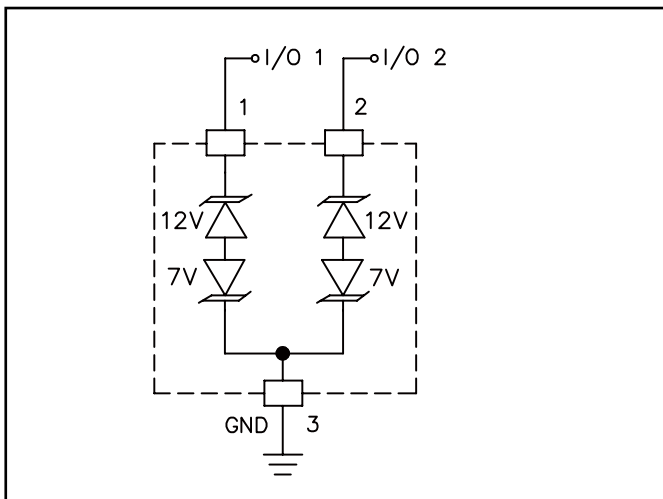
Mechanical Characteristics

- JEDEC SOT23 package
- Molding compound flammability rating: UL 94V-0
- Packaging : Tape and Reel per EIA 481

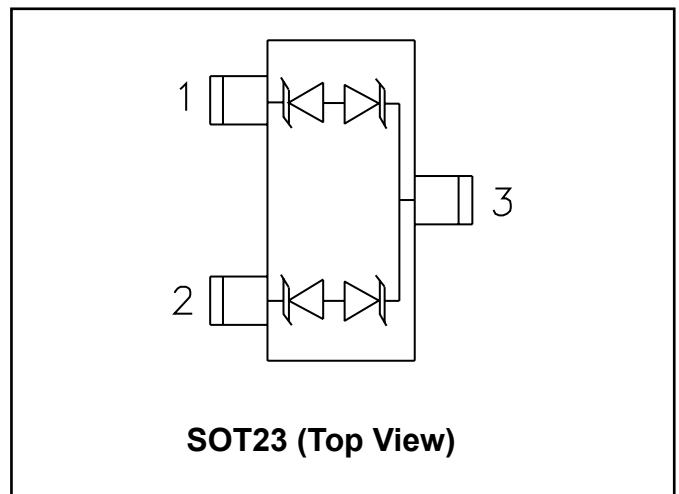
Applications

- Protection of RS-485 transceivers with extended common-mode range
- Security systems
- Automatic Teller Machines
- HFC systems
- Networks

Circuit Diagram



Pin Configuration



Maximum Ratings

Rating	Symbol	Value	Units
Peak Pulse Power ($t_p = 8/20\mu s$)	P_{PK}	400	Watts
Peak Pulse Current ($t_p = 8/20\mu s$)	I_{PP}	17	A
Lead Soldering Temperature	T_L	260 (10 sec.)	$^{\circ}C$
Operating Temperature	T_J	-55 to +125	$^{\circ}C$
Storage Temperature	T_{STG}	-55 to +150	$^{\circ}C$

Electrical Characteristics (T=25 $^{\circ}C$)

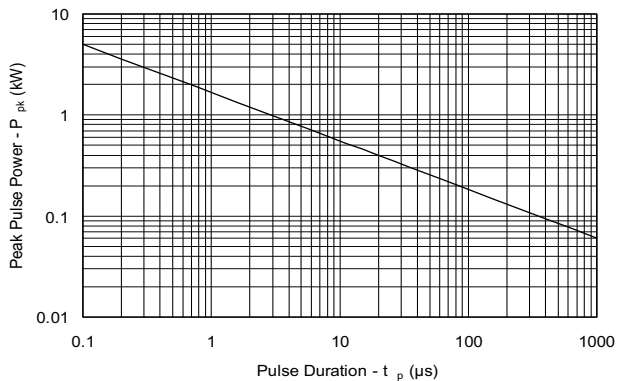
RLST23A712C

Parameter	Symbol	Conditions	MIN	TYP	MAX	MIN	TYP	MAX	Units
Reverse Stand-Off Voltage	V_{RWM}	Pin 3 to 1 or Pin 2 to 1	-	-	12	-	-	7	V
Reverse Breakdown Voltage	V_{BR}	$I_{PT} = 1mA$	13.3	-	-	7.5	-	-	V
Reverse Leakage Current	I_R	$V_R = V_{RWM}$	-	-	1	-	-	20	μA
Clamping Voltage	V_C	$I_{PP} = 5A, t_p = 8/20\mu s$	-	-	20	-	-	10	V
Clamping Voltage	V_C	$I_{PP} = 17A, t_p = 8/20\mu s$	-	-	26	-	-	12	V
Junction Capacitance	C_J	$V_R = 0V, f = 1MHz$	-	-	75	-	-	75	pF
		$V_R = V_{RWM}, f = 1MHz$	-	45	-	-	45	-	pF

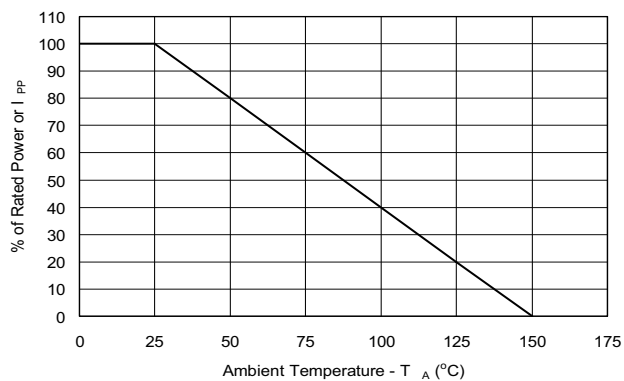
Protection Products

Typical Characteristics

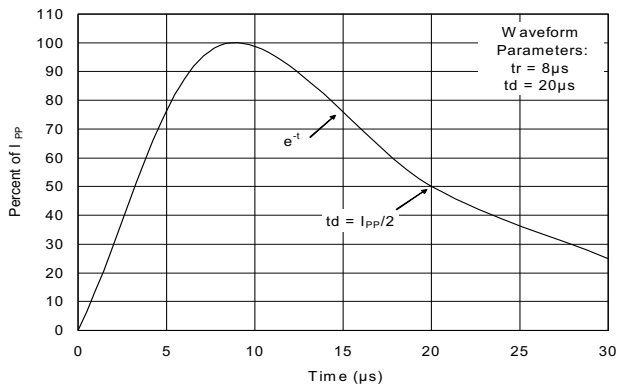
Non-Repetitive Peak Pulse Power vs. Pulse Time



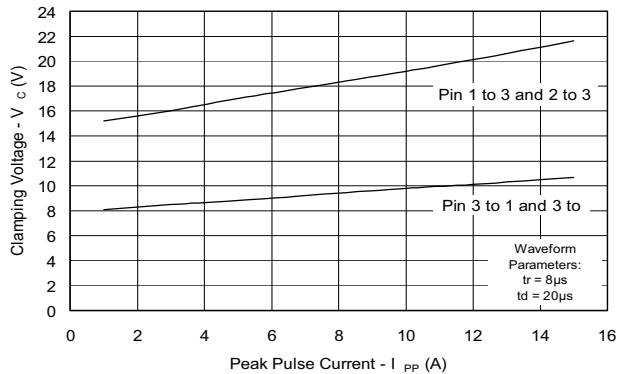
Power Derating Curve



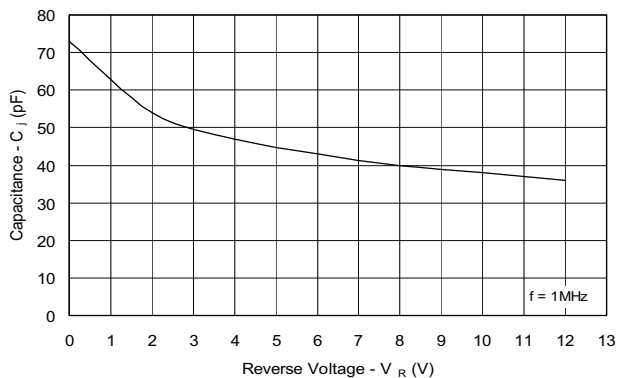
Pulse Waveform



Clamping Voltage vs. Peak Pulse Current



Capacitance vs. Reverse Voltage



Protection Products

Applications Information

Device Connection for Protection of Two RS-485 Data Lines

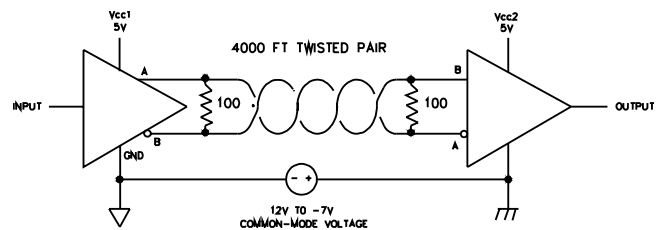
EIA RS-485 specifies a $\pm 7V$ ground difference between devices on the bus. This permits the bus voltage to range from +12V (5V + 7V) to -7V (0 - 7V). The RLST23A712C is designed to protect two RS-485 data lines in extended common mode applications. The RLST23A712C may be used to protect devices from transient voltages resulting from ESD, EFT, and lightning. The device is designed with asymmetrical operating voltages for optimum protection. The TVS diodes at pins 1 and 2 have a working voltage of 12 volts. These pins are connected to the differential data line pairs.

The TVS diodes at pin 3 have a working voltage of 7 volts. Pin 3 is connected to ground. The internal TVS diodes of the RLST23A712C will protect the transceiver input from positive transient voltage spikes greater than 12V and negative spikes greater than 7V.

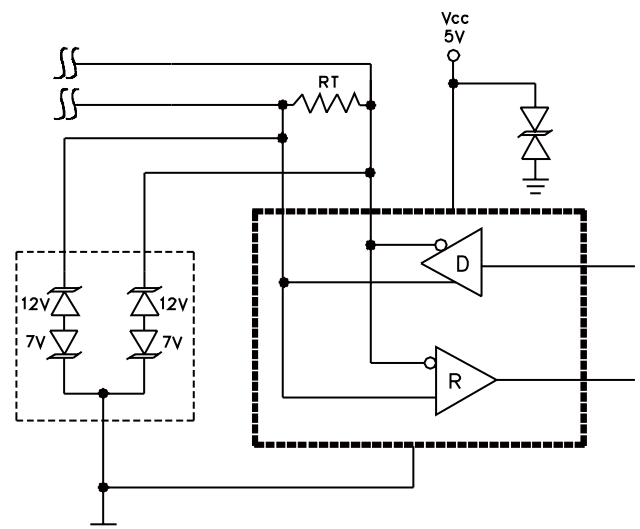
A series current limiting resistor may be added in applications requiring enhanced surge immunity. Circuit Board Layout Recommendations. Good circuit board layout is critical for the suppression of fast rise-time transients such as ESD. The following guidelines are recommended:

- Place the RLST23A712C near the input terminals or connectors to restrict electromagnetic coupling.
- Minimize the path length between the RLST23A712C and the protected line. This minimizes voltage overshoot due to parasitic inductance of board traces.
- Use ground planes whenever possible.
- Long, single trace ground conductors should be avoided. The ground pin (Pin 3) should be connected directly to a ground plane on the circuit board for best results.
- Minimize all conductive loops including power and ground loops.
- Never run critical signals near board edges.

RS-485 Common Mode Voltages

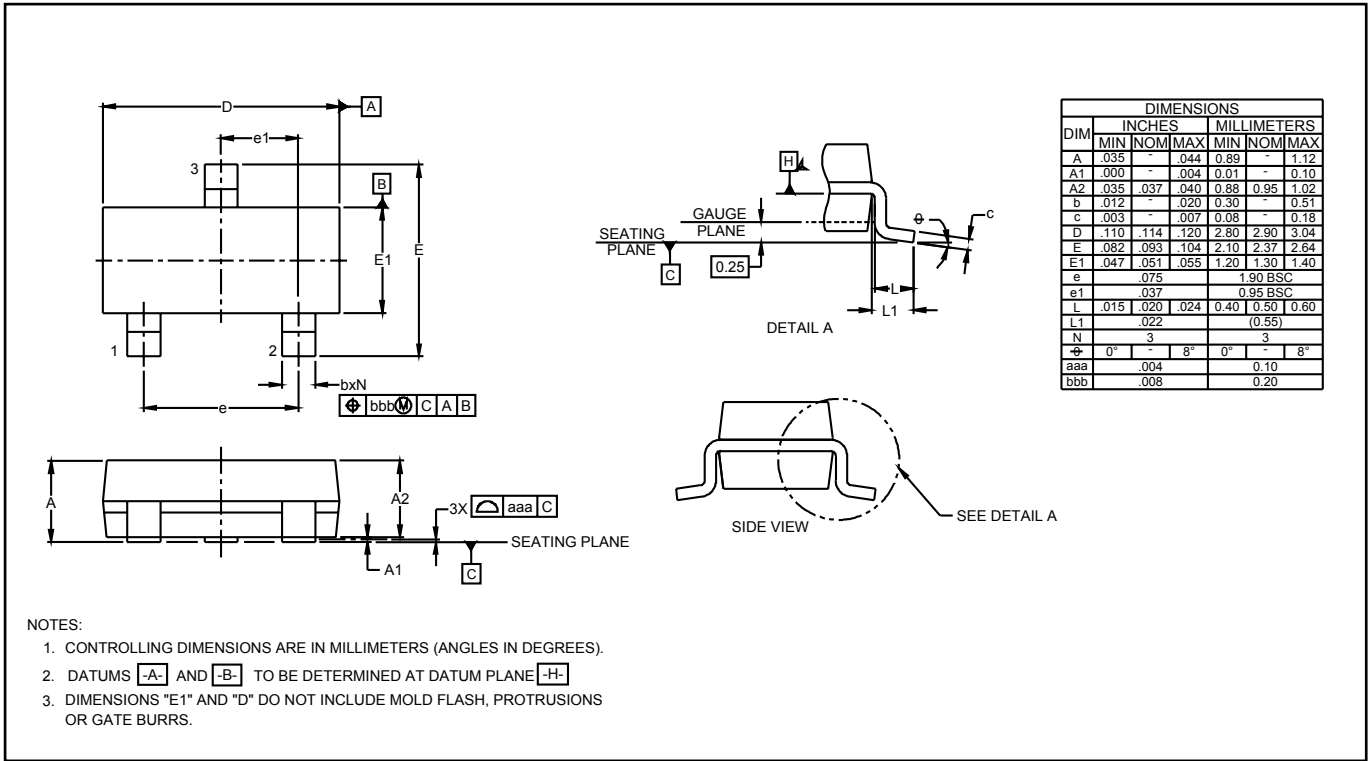


RS-485 Protection Circuit



PROTECTION PRODUCTS

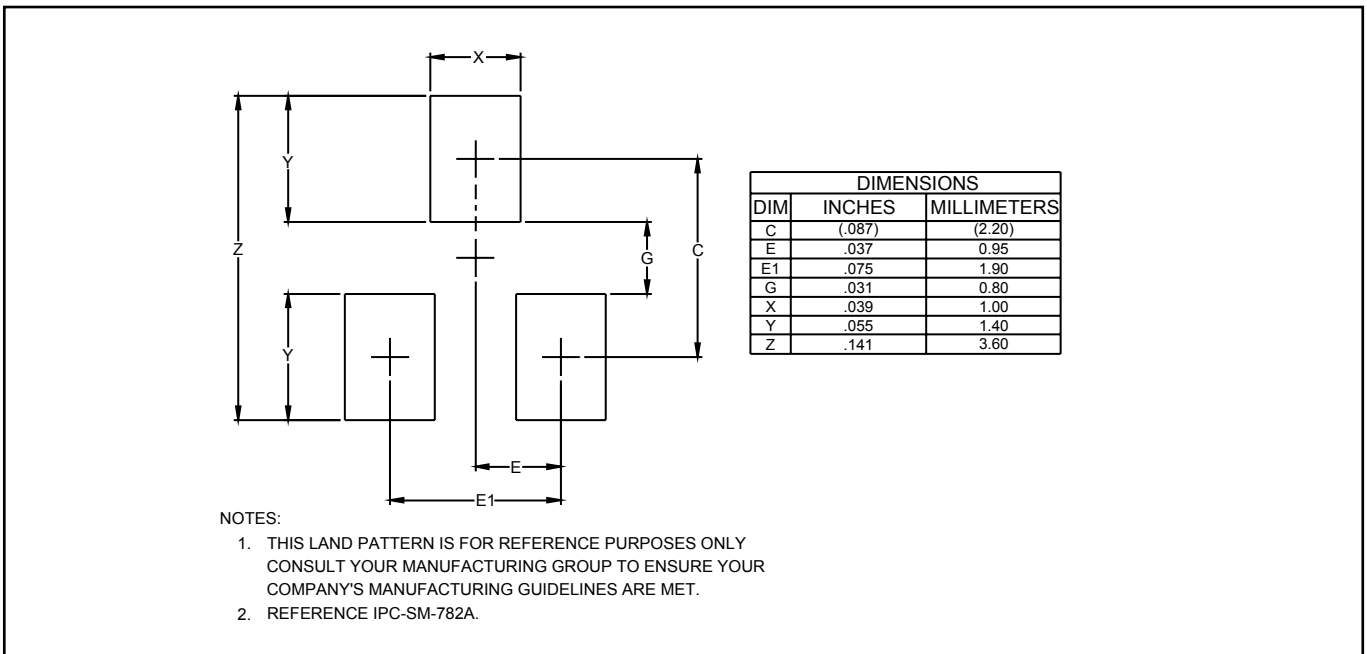
Outline Drawing -SOT23



NOTES:

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
2. DATUMS $-A-$ AND $-B-$ TO BE DETERMINED AT DATUM PLANE $-H-$
3. DIMENSIONS "E1" AND "D" DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

Land Pattern -SOT23



NOTES:

1. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY
CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR
COMPANY'S MANUFACTURING GUIDELINES ARE MET.
2. REFERENCE IPC-SM-782A.

Protection Products

Ordering Information

Part Number	Lead Finish	Qty per Reel	Reel Size
RLST23A712C.TC	SnPb	3,000	7 Inch
RLST23A712C.TCT	Pb-Free Matte Sn	3,000	7 Inch

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