

Qualification requirements are removed for device types 02, 03 and 09 (see 1.1)

MIL-M-38510/10C
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SUPERSEDING
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MILITARY SPECIFICATION

MICROCIRCUITS, DIGITAL, BIPOLAR, TTL, DECODERS, MONOLITHIC SILICON

This specification is approved for use by all Departments and Agencies of the Department of Defense.

1. SCOPE.

1.1 Scope. This specification covers the detail requirements for monolithic silicon, TTL, microcircuit decoders. Qualification inspection requirements are removed for device types 02, 03, and 09. These device types are inactive for new design after the date of this revision. For the remaining device types 01, 04, 05, 06, 07, and 08, two product assurance classes and a choice of case outlines and lead finishes are provided and are reflected in the complete part number.

1.2 Part number. The part number shall be in accordance with MIL-M-38510, with the exception that the "JAN" or "J" certification mark shall not be used for devices 02, 03, and 09.

1.2.1 Device type. The device type shall be as shown in the following:

Device type	Circuit
01	BCD-to-decimal decoder
02 1/	Excess-3-to-decimal decoder
03 1/	Excess-3-gray-to-decimal decoder
04	BCD-to-decimal decoder/driver (30-volt, open collector output)
05	BCD-to-decimal decoder/driver (15-volt, open collector output)
06	BCD-to-seven-segment decoder/driver (30-volt, open collector output)
07	BCD-to-seven-segment decoder/driver (15-volt, open collector output)
08	BCD-to-seven-segment decoder/driver
09 1/	BCD-to-seven-segment decoder/driver (5.5-volt, open collector output)

1.2.2 Device class. The device class shall be the product assurance level as defined in MIL-M-38510.

1.2.3 Case outline. The case outline shall be designated as follows:

Letter	Case outline (see MIL-M-38510, appendix C)
A	F-1 (14-pin, 1/4" x 1/4"), flat-package
B	F-3 (14-pin, 3/16" x 1/4"), flat-package
C	D-1 (14-pin, 1/4" x 3/4"), dual-in-line package
D	F-2 (14-pin, 1/4" x 3/8"), flat-package
E	D-2 (16-pin, 1/4" x 7/8"), dual-in-line package
F	F-5 (16-pin, 1/4" x 3/8"), flat-package

1.2.4 Absolute maximum ratings.

Supply voltage range	- - - - -	-0.5 to 7.0 V dc
Input voltage range	- - - - -	-1.5 V dc at -12 mA to 5.5 V dc
Storage temperature range	- - - - -	-65°C to 150°C

1/ Qualification requirements are removed for these device types.

Beneficial comments (recommendations, additions, deletions) and any pertinent data which may be of use in improving this document should be addressed to: Rome Air Development Center (RBE-2), Griffiss AFB, NY 13441, by using the self-addressed Standardization Document Improvement Proposal (DD Form 1426) appearing at the end of this document or by letter.

Maximum power dissipation, (P_D) 2/- - - - -	226 mW dc (device types 01, 02 and 03) 3/ 341 mW dc (device types 04 and 05) 467 mW dc (device types 06, 07, 08 and 09) 3/
Lead temperature (soldering, 10 seconds) - - - - -	300°C
Thermal resistance, junction to case (θ_{JC}): Cases A, B, C, D, E, F - - - - -	(See MIL-M-38510, appendix C)
Junction temperature (T_J) 4/- - - - -	+175°C
Maximum current into any output (output off): Device types 04, 05, 06, 07 and 09 3/ - - - - -	1 mA

1.2.5 Recommended operating conditions.

Supply voltage (V_{CC}) - - - - -	4.5 V dc minimum to 5.5 V dc maximum
Minimum high level input voltage (V_{IH}) - - - - -	2.0 V dc
Maximum low level input voltage (V_{IL}) - - - - -	0.8 V dc
Sink-current capability by device type:	
01, 02, 03 3/- - - - -	16 mA
04, 05 - - - - -	20 mA
06, 07 outputs a-g. - - - - -	40 mA
BI/RBO node - - - - -	8 mA
08 outputs a-g. - - - - -	6.4 mA
BI/RBO node - - - - -	8 mA
09 3/ - - - - -	10 mA
Case operating temperature range (T_C) - - - - -	-55°C to +125°C

2. APPLICABLE DOCUMENT

2.1 Government documents.

2.1.1 Specification and standard. The following specification and standard form a part of this specification to the extent specified herein. Unless otherwise specified, the issues of these documents shall be those listed in the issue of the Department of Defense Index of Specifications and Standards (DODISS) and supplement thereto, cited in the solicitation.

SPECIFICATION

MILITARY

MIL-M-38510 - Microcircuits, General Specification for.

STANDARD

MILITARY

MIL-STD-883 - Test Methods and Procedures for Microcircuits.

(Copies of the specification and standard required by contractors in connection with specific acquisition functions should be obtained from the contracting activity or as directed by the contracting activity.)

2.2 Order of precedence. In the event of a conflict between the text of this specification and the references cited herein (except for associated detail specifications, specification sheets or MS standards), the text of this specification shall take precedence. Nothing in this specification, however, shall supersede applicable laws and regulations unless a specific exemption has been obtained.

3. REQUIREMENTS

3.1 Detail specifications. The individual item requirements shall be in accordance with MIL-M-38510, and as specified herein.

- 2/ Must withstand the added P_D due to short circuit condition (i.e., I_{OS}) at one output for 5 seconds duration
- 3/ Qualification requirement are removed for devices 02, 03, and 09.
- 4/ Maximum junction temperature shall not be exceeded except for allowable short duration burn-in screening per method 5004 of MIL-STD-883.

3.2 Design, construction, and physical dimensions. The design, construction, and physical dimensions shall be as specified in MIL-M-38510 and herein.

3.2.1 Logic diagram and terminal connections. The logic diagram and terminal connections shall be as specified on figures 1 and 2.

3.2.2 Truth tables. The truth tables shall be as specified on figure 3.

3.2.3 Case outlines. Case outlines shall be in accordance with 1.2.3.

3.2.4 Schematic circuits. Schematic circuits shall be submitted to the preparing activity prior to inclusion of a manufacturer's device in the specification and shall be submitted to the qualifying activity and agent activity (DESC-ECS) as a prerequisite for qualification. All qualified manufacturers schematics shall be maintained by the agent activity and will be available upon request.

3.3 Lead material and finish. The lead material and finish shall be in accordance with MIL-M-38510 (see 6.4).

3.4 Electrical performance characteristics. Unless otherwise specified, the electrical performance characteristics are as specified in table I, and apply over the full recommended case operating temperature range.

TABLE I. Electrical performance characteristics, device types 01, 02 and 03 1/

Test	Symbol	Conditions $-55^{\circ}\text{C} \leq T_C \leq +125^{\circ}\text{C}$	Limits		Units
			Min	Max	
High-level output voltage	V_{OH}	$V_{CC} = 4.5 \text{ V}, I_{OH} = -0.8 \text{ mA}$	2.4	---	V
Low-level output voltage	V_{OL}	$V_{CC} = 4.5 \text{ V}, I_{OL} = 16 \text{ mA}$ $V_{IN} = 0.8 \text{ V and } 2.0 \text{ V}$	---	0.4	V
Input clamp voltage	V_{IC}	$V_{CC} = 4.5 \text{ V}, I_{IN} = -12 \text{ mA}$	---	-1.5	V
Low-level input current	I_{IL}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0.4 \text{ V } 2/$	-0.7	-1.6	mA
High-level input current	I_{IH1}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 2.4 \text{ V } 3/$	---	40	μA
High-level input current	I_{IH2}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 5.5 \text{ V } 3/$		100	μA
Short-circuit output current	I_{OS}	$V_{CC} = 5.5 \text{ V } 4/$	-20	-55	mA
Supply current	I_{CC}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0 \text{ V}$	---	41	mA
Propagation delay time through two logic levels	t_{PHL}	$C_L = 50 \text{ pF minimum}$ $R_L = 390 \Omega \pm 5\%$ (Figure 5)	5	39	ns
Propagation delay time through two logic levels	t_{PLH}		5	39	ns
Propagation delay time through three logic levels	t_{PHL}		5	46	ns
Propagation delay time through three logic levels	t_{PLH}		5	46	ns

1/ Qualification requirement are removed for device types 02, and 03.

2/ All unspecified inputs at 5.5 volts.

3/ All unspecified inputs grounded.

4/ Not more than one output should be shorted at one time.

TABLE I. Electrical performance characteristics, device types 04 and 05.

Test	Symbol	Conditions $-55^{\circ}\text{C} \leq T_C \leq +125^{\circ}\text{C}$	Limits		Units
			Min	Max	
Low-level output voltage	V_{OL1}	$V_{CC} = 4.5 \text{ V}, I_{OL} = 80 \text{ mA}$	---	0.9	V
Low-level output voltage	V_{OL2}	$V_{CC} = 4.5 \text{ V}, I_{OL} = 20 \text{ mA}$	---	0.4	V
Input clamp voltage	V_{IC}	$V_{CC} = 4.5 \text{ V}, I_{IN} = -12 \text{ mA}$	---	-1.5	V
Maximum collector cut-off current	I_{CEX}	$V_{CC} = 4.5 \text{ V}, V_{OH} = \text{max } 1/$		250	μA
Low-level input current	I_{IL}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0.4 \text{ V } 2/$	-0.7	-1.6	mA
High-level input current	I_{IH1}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 2.4 \text{ V } 3/$	---	40	μA
High-level input current	I_{IH2}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 5.5 \text{ V } 3/$		100	μA
Supply current	I_{CC}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0 \text{ V}$	---	62	mA
Propagation delay time to a high logic level	t_{PLH}	$C_L = 50 \text{ pF minimum}$ $R_L = 390 \Omega \pm 5\%$	5	73	ns
Propagation delay time to a low logic level	t_{PHL}	(Figure 6)	5	73	ns

- 1/ Device type 04 maximum $V_{OH} = 30 \text{ V}$.
 Device type 05 maximum $V_{OH} = 15 \text{ V}$.
 2/ All unspecified inputs at 5.5 volts.
 3/ All unspecified inputs grounded.

TABLE I. Electrical performance characteristics, device types 06 and 07.

Test	Symbol	Conditions $-55^{\circ}\text{C} \leq T_C \leq +125^{\circ}\text{C}$	Limits		Units
			Min	Max	
Low-level output voltage <u>1/</u>	V_{OL1}	$V_{CC} = 4.5 \text{ V}, I_{OL} = 40 \text{ mA}$	---	0.4	V
Low-level output voltage <u>2/</u>	V_{OL2}	$V_{CC} = 4.5 \text{ V}, I_{OL} = 8 \text{ mA}$	---	0.4	V
Input clamp voltage	V_{IC}	$V_{CC} = 4.5 \text{ V}, I_{IN} = -12 \text{ mA}$	---	-1.5	V
High-level output voltage <u>2/</u>	V_{OH}	$V_{CC} = 4.5 \text{ V}, I_{OH} = -0.2 \text{ mA}$	2.4	---	V
Maximum collector cut-off current <u>3/</u>	I_{CEX}	$V_{CC} = 4.5 \text{ V}, V_{OH} = \text{max } \underline{3/}$	---	250	μA
Low-level input current <u>4/</u>	I_{IL1}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0.4 \text{ V } \underline{5/}$	-0.4	-1.6	mA
Low-level input current <u>2/</u>	I_{IL2}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0.4 \text{ V } \underline{5/}$	-1.7	-4.2	mA
High-level input current <u>4/</u>	I_{IH1}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 2.4 \text{ V } \underline{6/}$	---	40	μA
High-level input current <u>4/</u>	I_{IH2}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 5.5 \text{ V } \underline{6/}$		100	μA
Short-circuit output current <u>1/</u>	I_{OS}	$V_{CC} = 5.5 \text{ V } \underline{6/}$	---	-4	mA
Supply current	I_{CC}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 5.5 \text{ V}$	---	85	mA
Propagation delay time from any input except RBI to any output	t_{PLH}	$C_L = 50 \text{ pF minimum}$ $R_L = 120 \Omega \pm 5\% \underline{1/}$ $R_L = 560 \Omega \pm 5\% \underline{2/}$ (Figure 7)	8	144	ns
Propagation delay time from RBI to any output	t_{PLH}		8	144	ns
Propagation delay time from any input except RBI to any out	t_{PHL}		8	144	ns
Propagation delay time from RBI to any output	t_{PHL}		8	144	ns

1/ Outputs a through g only.

2/ BI/RB0 node only.

3/ Device type 06 maximum $V_{OH} = 30 \text{ V}$.Device type 07 maximum $V_{OH} = 15 \text{ V}$.

4/ Any input except BI/RB0 node.

5/ All unspecified inputs at 5.5 volts.

6/ All unspecified inputs grounded.

TABLE I. Electrical performance characteristics, device type 08.

Test	Symbol	Conditions $-55^{\circ}\text{C} \leq T_C \leq +125^{\circ}\text{C}$	Limits		Units
			Min	Max	
High-level output voltage <u>1/</u>	V_{OH1}	$V_{CC} = 4.5 \text{ V}, I_{OH} = -0.4 \text{ mA}$	2.4	---	V
High-level output voltage <u>2/</u>	V_{OH2}	$V_{CC} = 4.5 \text{ V}, I_{OH} = -0.2 \text{ mA}$	2.4	---	V
Low-level output voltage <u>1/</u>	V_{OL1}	$V_{CC} = 4.5 \text{ V}, I_{OH} = 6.4 \text{ mA}$	---	0.4	V
Low-level output voltage <u>2/</u>	V_{OL2}	$V_{CC} = 4.5 \text{ V}, I_{OH} = 8 \text{ mA}$	---	0.4	V
Input clamp voltage	V_{IC}	$V_{CC} = 4.5 \text{ V}, I_{IN} = -12 \text{ mA}$	---	-1.5	V
Low-level input current <u>3/</u>	I_{IL1}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0.4 \text{ V } \underline{4/}$	-0.4	-1.6	mA
Low-level input current <u>3/</u>	I_{IL2}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0.4 \text{ V } \underline{4/}$	-1.7	-4.2	mA
High-level input current <u>3/</u>	I_{IH1}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 2.4 \text{ V } \underline{5/}$	---	40	μA
High-level input current <u>3/</u>	I_{IH2}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 5.5 \text{ V } \underline{5/}$		100	μA
Short-circuit output current	I_{OS}	$V_{CC} = 5.5 \text{ V}$	---	-4	mA
Supply current	I_{CC}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0 \text{ V}$	---	76	mA
Propagation delay time from any input except RBI to any output	t_{PLH}	$C_L = 50 \text{ pF}$ minimum $R_L = 750\Omega \pm 5\% \underline{1/}$ $R_L = 560\Omega \pm 5\% \underline{2/}$ (Figure 8)	8	144	ns
Propagation delay time from RBI to any output	t_{PLH}		8	144	ns
Propagation delay time from any input except RBI to any output	t_{PHL}		8	144	ns
Propagation delay time from RBI to any output	t_{PHL}		8	144	ns

1/ Outputs a through g only.

2/ BI/RBO node only.

3/ Any input except BI/RBO node.

4/ All unspecified inputs at 5.5 volts.

5/ All unspecified inputs grounded.

TABLE I. Electrical performance characteristics, device type 09 ^{1/}

Test	Symbol	Conditions $-55^{\circ}\text{C} \leq T_C \leq +125^{\circ}\text{C}$	Limits		Units
			Min	Max	
Low-level output voltage	V_{OL}	$V_{CC} = 4.5 \text{ V}, I_{OL} = 10 \text{ mA}$	---	0.4	V
Input clamp voltage	V_{IC}	$V_{CC} = 4.5 \text{ V}, I_{IN} = -12 \text{ mA}$	---	-1.5	V
Maximum collector cut-off current	I_{CEX}	$V_{CC} = 4.5 \text{ V}, V_{OH} = 5.5 \text{ V}$	---	250	μA
Low-level input current	I_{IL}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0.4 \text{ V } \underline{2/}$	-0.4	-1.6	mA
High-level input current	I_{IH1}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 2.4 \text{ V } \underline{3/}$	---	40	μA
High-level input current	I_{IH2}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 5.5 \text{ V } \underline{3/}$		100	μA
Supply current	I_{CC}	$V_{CC} = 5.5 \text{ V}, V_{IN} = 0 \text{ V}$	---	47	mA
Propagation delay time from any input to any output	t_{PLH}	$C_L = 50 \text{ pF minimum}$ $R_L = 470\Omega \pm 5\% \underline{1/}$	8	144	ns
Propagation delay time from any input to any output	t_{PHL}	(Figure 9)	8	144	ns

^{1/} Qualification requirements are removed for device type 09.

^{2/} All unspecified inputs at 5.5 volts.

^{3/} All unspecified inputs grounded.

3.5 Electrical test requirements. The electrical test requirements for each device class shall be the subgroups specified in table II. The electrical tests for each subgroup are described in table III.

3.6 Marking. Marking shall be in accordance with MIL-M-38510 and 1.2 herein. At the option of the manufacturer, marking of the country of origin may be omitted from the body of the microcircuit, but shall be retained on the initial container. The "JAN" or "J" certification mark shall not be used for device types 02, 03, and 09.

TABLE II. Electrical test requirements.

MIL-STD-883 test requirements	Subgroups (see table III)	
	Class S devices 1/	Class B devices
Interim electrical parameters (method 5004)	1	1
Final electrical test parameters (method 5004)	1*,2,3,7 9,10,11	1*,2,3, 7,9
Group A test requirements (method 5005)	1,2,3,7 8,9,10,11	1,2,3,7,8 9,10,11
Group C and D end-point electrical parameters (method 5005)	1,2,3	1,2,3
Additional electrical subgroups for group C periodic inspections	N/A	N/A

1/ Class S product assurance is not applicable to device types 02, 03, and 09.

*PDA applies to subgroup 1 (see 4.2c)).

3.7 Microcircuit group assignment. The devices covered by this specification shall be in microcircuit group number 4 (see MIL-M-38510, appendix E).

3.8 Manufacturer eligibility. To be eligible to supply microcircuits to this specification, a manufacturer shall have a manufacturer certification in accordance with MIL-M-38510 for at least one line, not necessarily the line producing the device described herein. This shall apply only for device types 02, 03, and 09.

3.9 Certification. Certification in accordance with MIL-M-38510 is not required for device types 02, 03, and 09, but shall be retained for the remaining device types.

4. QUALITY ASSURANCE PROVISIONS

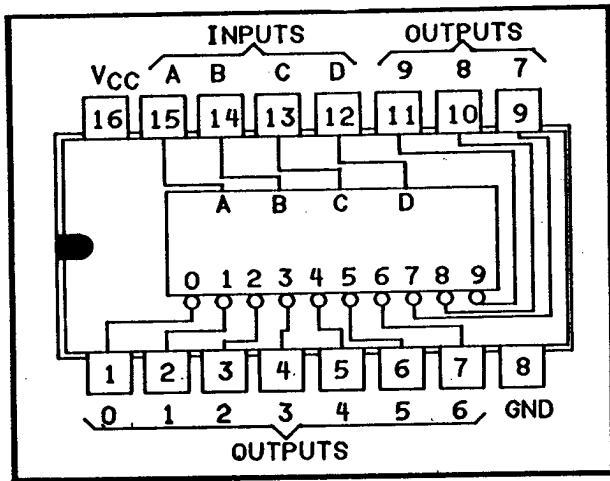
4.1 Sampling and inspection. Sampling and inspection procedures shall be in accordance with MIL-M-38510 and methods 5005 and 5007, as applicable, of MIL-STD-883, except as modified herein.

4.2 Screening. Screening shall be in accordance with method 5004 of MIL-STD-883, and shall be conducted on device types 01, 04, 05, 06, 07, and 08 prior to qualification and quality conformance inspection, and on device types 02, 03, and 09 prior to quality conformance inspection. The following additional criteria shall apply:

- a. Burn-in (method 1015 of MIL-STD-883).
 - (1) Test condition D or E, using the circuit shown on figure 4, or equivalent.
 - (2) $T_A = +125^{\circ}\text{C}$ minimum.
- b. Interim and final electrical test parameters shall be as specified in table II, except interim electrical parameters test prior to burn-in is optional at the discretion of the manufacturer.
- c. The percent defective allowable (PDA) shall be as specified in MIL-M-38510.

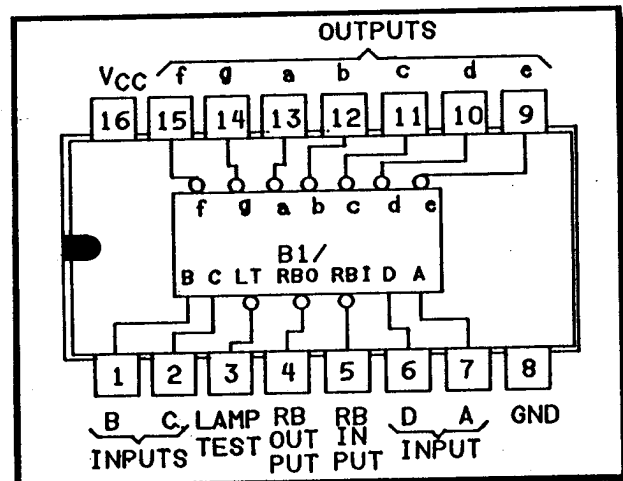
Device types 01, 02, 03, 04, and 05 1/

Cases E and F



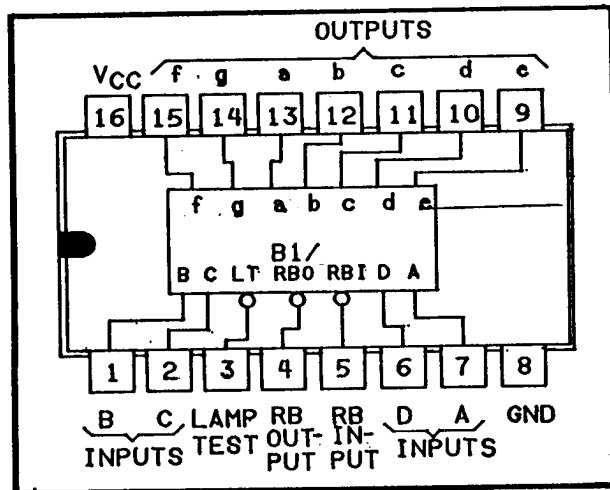
Device types 06 and 07

Cases E and F

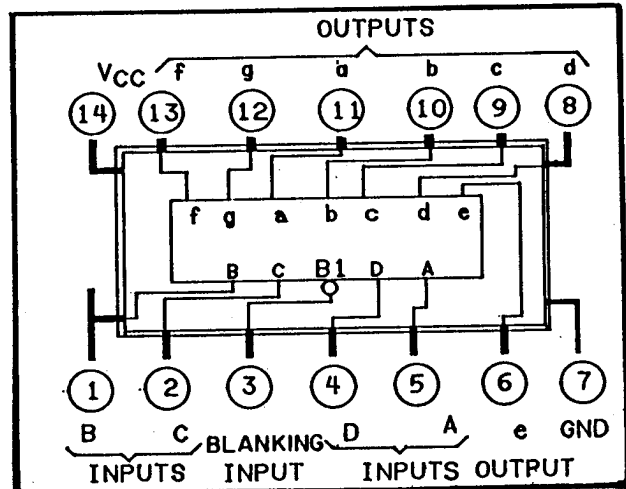


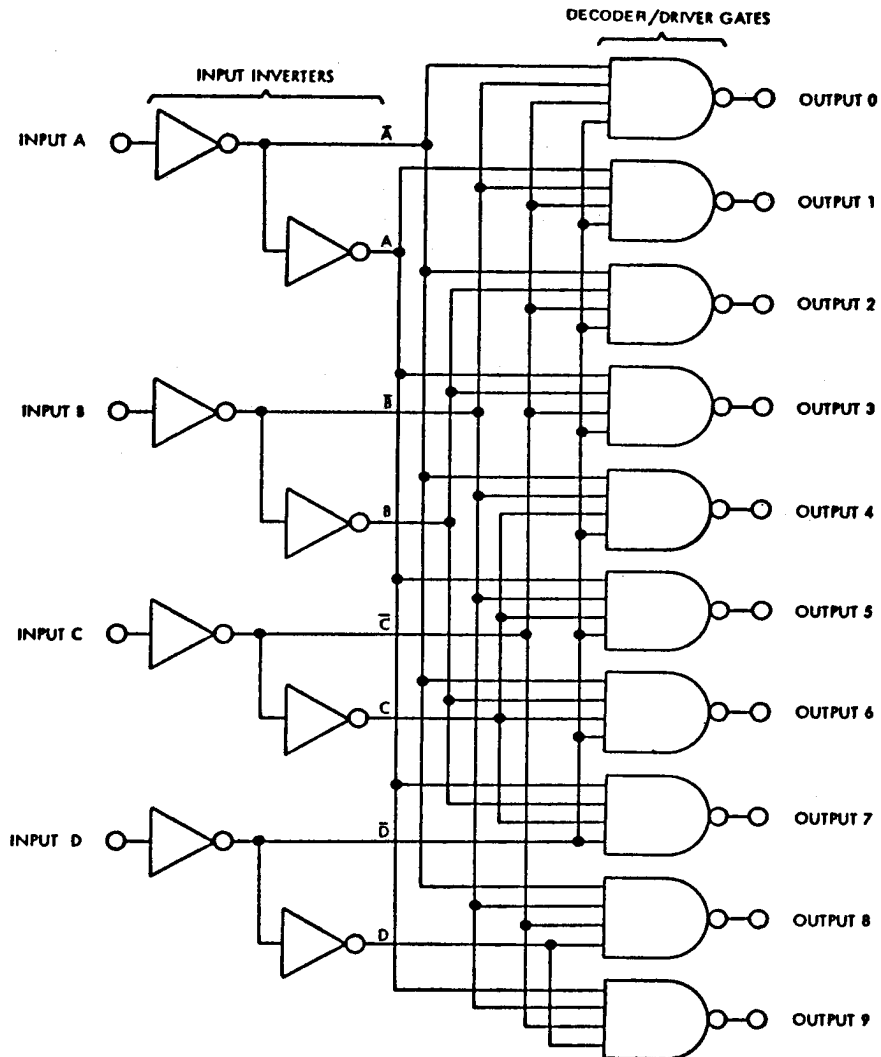
Device type 08

Cases E and F

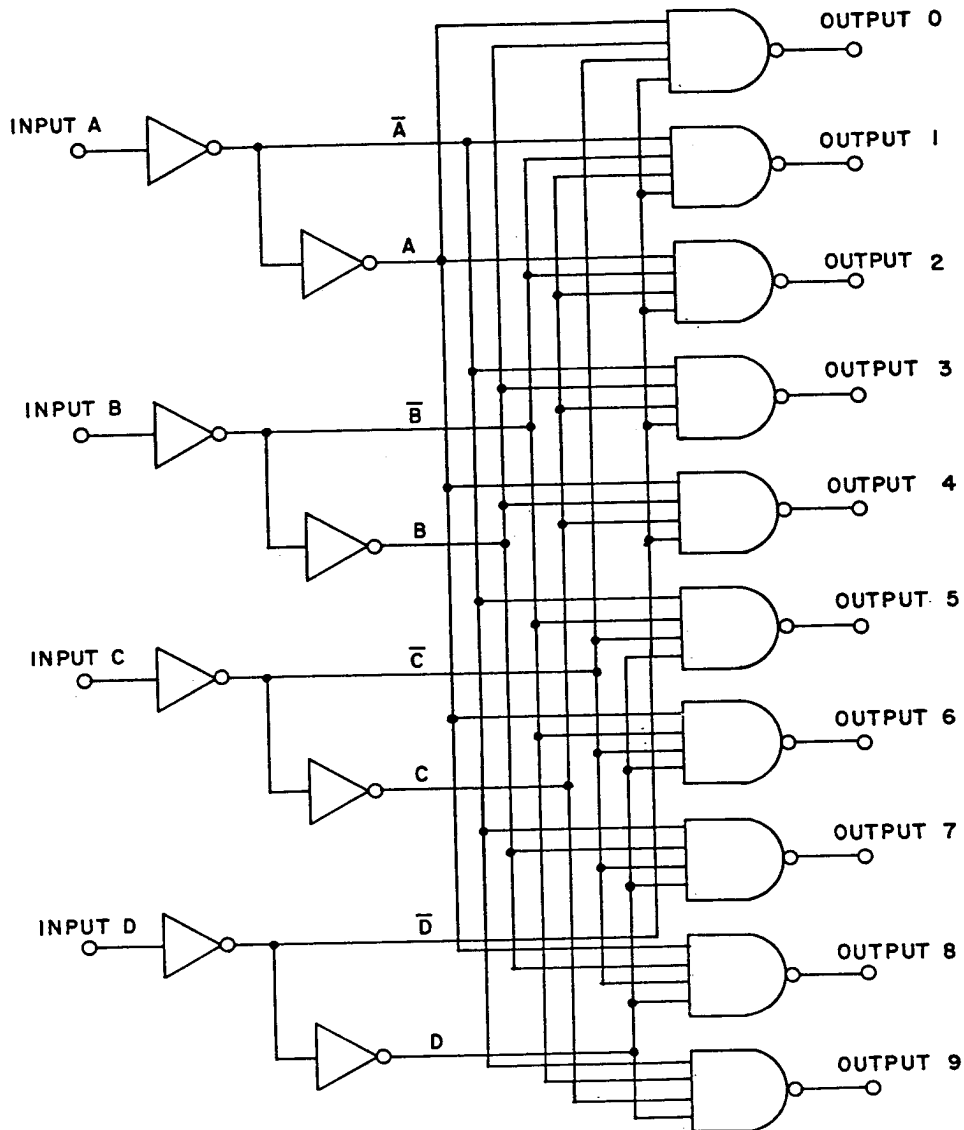
Device type 09 1/

Cases A, B, C or D

1/ Qualification requirements are removed for device types 02, 03, 09.FIGURE 1. Terminal connections.

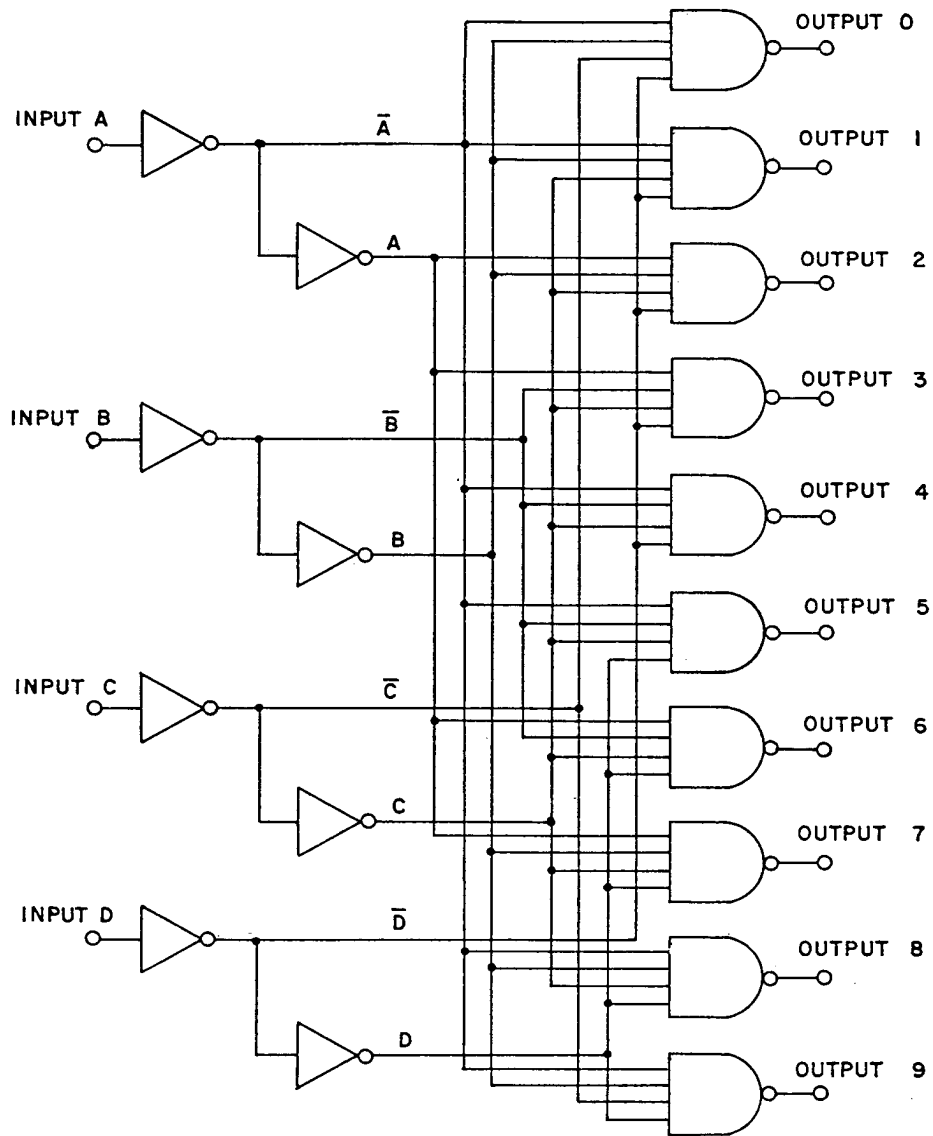
Device types 01, 04, and 05FIGURE 2. Logic diagrams.

Device type 02 1/



1/ Qualification requirements are removed for this device type.

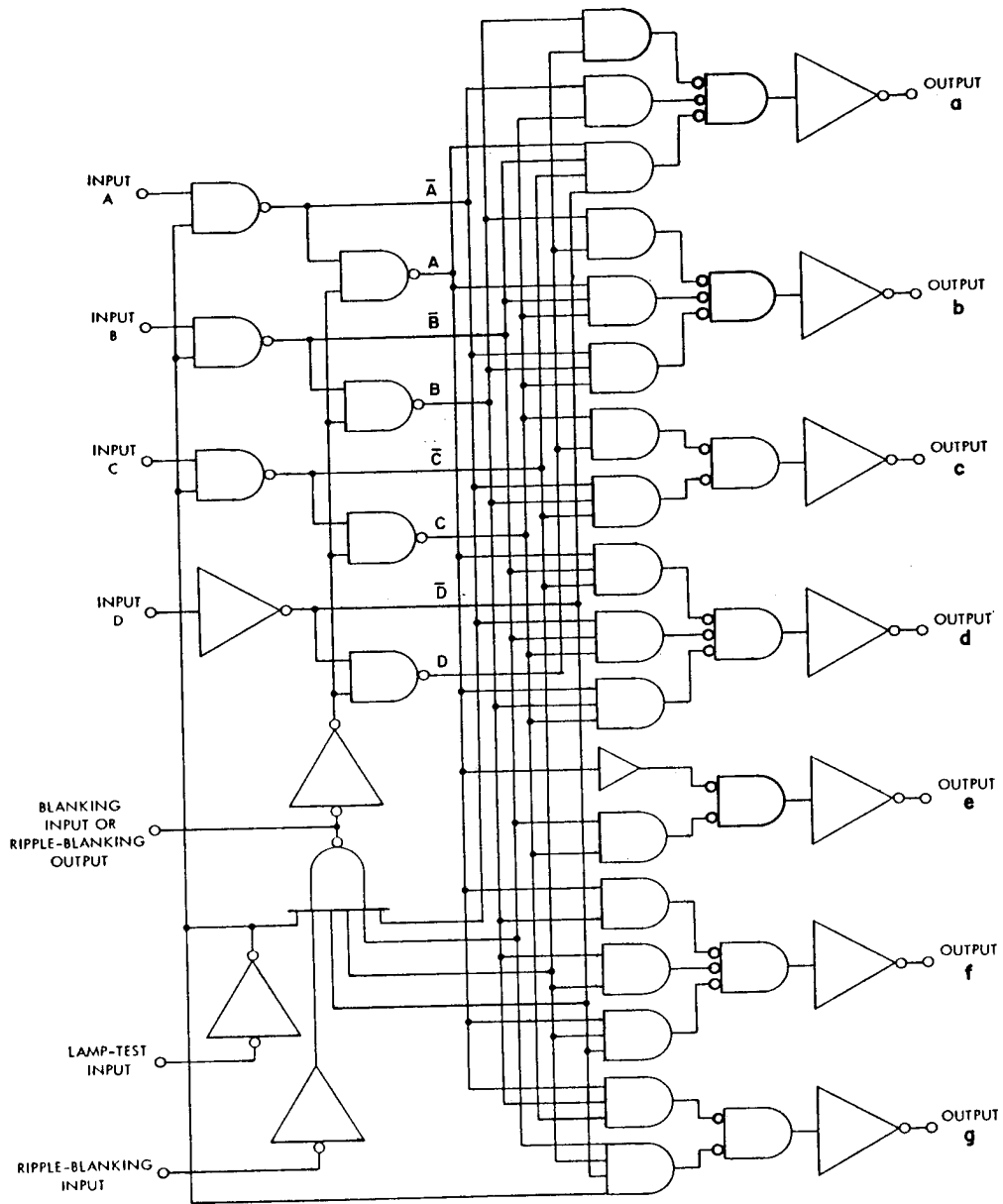
FIGURE 2. Logic diagrams. (Continued)

Device type 03 1/

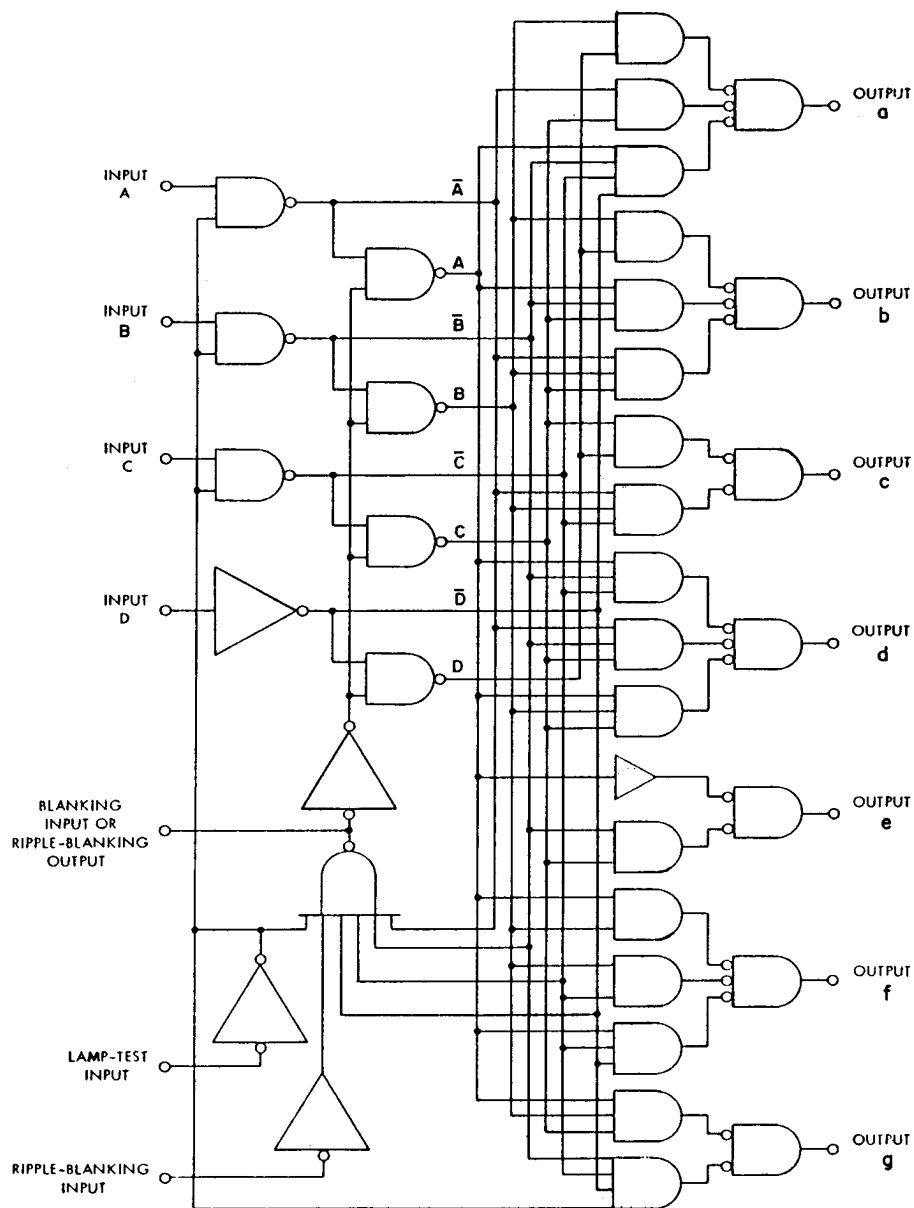
1/ Qualification requirements are removed for this device type.

FIGURE 2. Logic diagrams. (Continued)

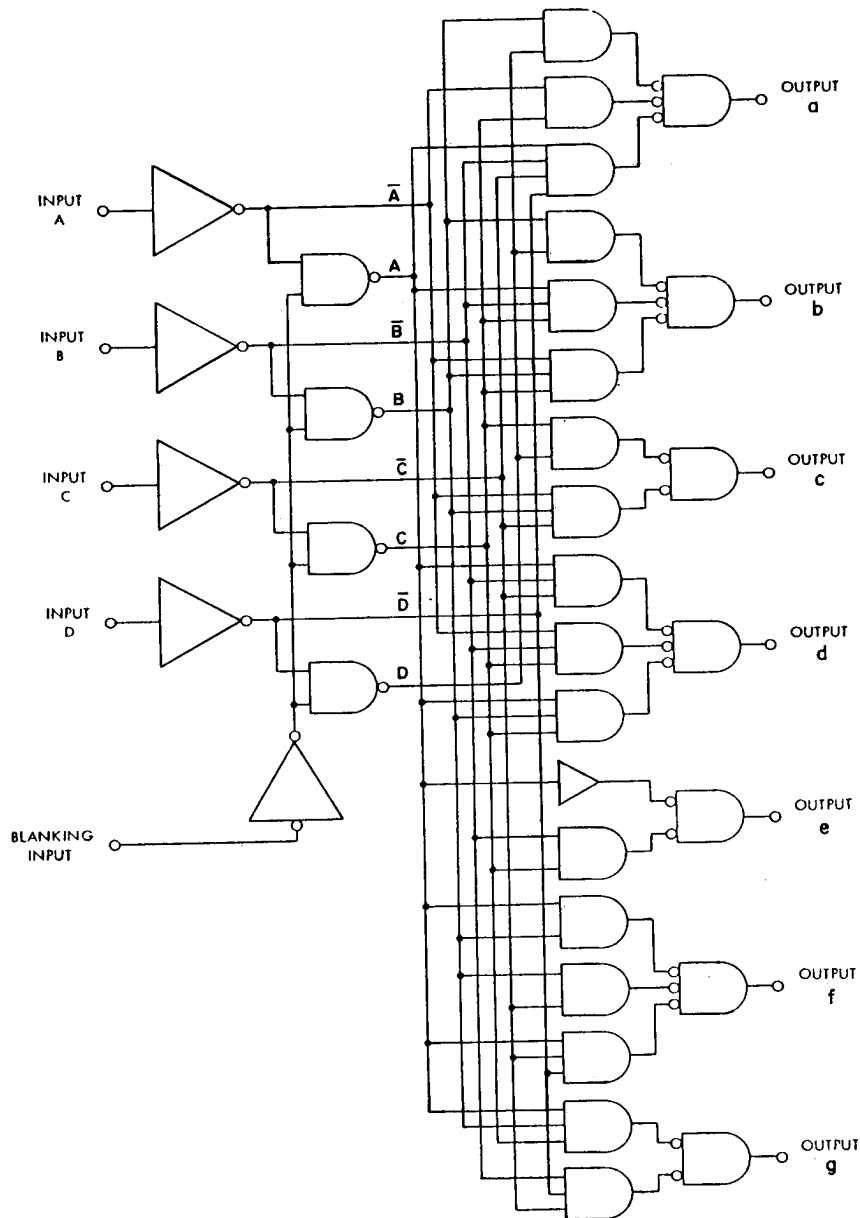
Device types 06 and 07

FIGURE 2. Logic diagrams. (Continued)

Device type 08

FIGURE 2. Logic diagrams. (Continued)

Device type 09 1/



1/ Qualification requirements are removed for this device type.

FIGURE 2. Logic diagrams. (Continued)

Device types 01, 04 and 05

[illegible]

FIGURE 3. Truth tables.

Device type 02 1/

INPUTS				OUTPUTS									
D	C	B	A	0	1	2	3	4	5	6	7	8	9
L	L	H	H	L	H	H	H	H	H	H	H	H	H
L	H	L	L	L	H	H	H	H	H	H	H	H	H
L	H	H	L	H	H	L	H	H	H	H	H	H	H
L	H	H	H	H	H	H	L	H	H	H	H	H	H
H	L	L	L	H	H	H	H	L	H	H	H	H	H
H	L	L	L	H	H	H	H	H	L	H	H	H	H
H	L	L	H	H	H	H	H	H	H	L	H	H	H
H	L	H	L	H	H	H	H	H	H	H	L	H	H
H	L	H	L	H	H	H	H	H	H	H	H	L	H
H	H	L	L	H	H	H	H	H	H	H	H	H	H
H	H	L	L	H	H	H	H	H	H	H	H	H	H
H	H	L	H	H	H	H	H	H	H	H	H	H	H
H	H	H	L	H	H	H	H	H	H	H	H	H	H
H	H	H	L	H	H	H	H	H	H	H	H	H	H
L	L	L	L	H	H	H	H	H	H	H	H	H	H
L	L	L	L	H	H	H	H	H	H	H	H	H	H

Device type 03 1/

INPUTS				OUTPUTS									
D	C	B	A	0	1	2	3	4	5	6	7	8	9
L	L	H	L	L	H	H	H	H	H	H	H	H	H
L	H	H	L	H	L	H	H	H	H	H	H	H	H
L	H	H	L	H	H	L	H	H	H	H	H	H	H
L	H	L	L	H	H	H	L	H	H	H	H	H	H
L	H	L	L	H	H	H	H	L	H	H	H	H	H
H	L	L	L	H	H	H	H	H	L	H	H	H	H
H	L	L	L	H	H	H	H	H	H	L	H	H	H
H	L	L	H	H	H	H	H	H	H	H	L	H	H
H	L	H	L	H	H	H	H	H	H	H	H	L	H
H	L	H	L	H	H	H	H	H	H	H	H	H	L
H	L	H	L	H	H	H	H	H	H	H	H	H	H
H	L	L	L	H	H	H	H	H	H	H	H	H	H
H	L	L	L	H	H	H	H	H	H	H	H	H	H
H	L	L	L	H	H	H	H	H	H	H	H	H	H
H	L	L	L	H	H	H	H	H	H	H	H	H	H
L	L	L	L	H	H	H	H	H	H	H	H	H	H
L	L	L	L	H	H	H	H	H	H	H	H	H	H

1/ Qualification requirements are removed for this device type.

FIGURE 3. Truth tables. (Continued)

Device type 06 and 07

DECIMAL OR FUNCTION	INPUTS							OUTPUTS							NOTE
	LT	RBI	D	C	B	A	BI/RBO	a	b	c	d	d	f	g	
0	H	H	L	L	L	L	H	L	L	L	L	L	L	H	1
1	H	X	L	L	L	H	H	H	L	L	H	H	H	H	1
2	H	X	L	L	H	L	H	L	L	H	L	L	H	L	
3	H	X	L	L	H	H	H	L	L	L	L	H	H	L	
4	H	X	L	H	L	L	H	H	L	L	H	H	L	L	
5	H	X	L	H	L	H	H	L	H	L	L	H	L	L	
6	H	X	L	H	H	L	H	H	H	L	L	L	L	L	
7	H	X	L	H	H	H	H	L	L	L	H	H	H	H	
8	H	X	H	L	L	L	H	L	L	L	L	L	L	L	
9	H	X	H	L	L	H	H	L	L	L	H	H	L	L	
10	H	X	H	L	H	L	H	H	H	H	L	L	H	L	
11	H	X	H	L	H	H	H	H	H	L	L	H	H	L	
12	H	X	H	H	L	L	H	H	L	H	H	H	L	L	
13	H	X	H	H	L	H	H	L	H	H	L	H	L	L	
14	H	X	H	H	H	L	H	H	H	H	L	L	L	L	
15	H	X	H	H	H	H	H	H	H	H	H	H	H	H	
BI	X	X	X	X	X	X	L	H	H	H	H	H	H	H	2
RBI	H	L	L	L	L	L	L	H	H	H	H	H	H	H	3
LT	L	X	X	X	X	X	H	L	L	L	L	L	L	L	4

NOTES:

1. BI/RBO is wire-OR logic serving as blanking input (BI) and/or ripple-blanking output (RBO). The blanking input must be open or held at a high logic level when output functions 0 through 15 are desired, and ripple-blanking input (RBI) must be open or at a high logic level during the decimal 0 output. X = input may be high or low.
2. When a low logic level is applied to the blanking input (forced condition) all segment outputs go to a low logic level regardless of the state of any other input condition.
3. When ripple-blanking input (RBI) is at a low logic level, lamp test input is at high logic level and A = B = C = D = low logic level, all segment outputs go to a low logic level and the ripple-blanking output goes to a low logic level (response condition).
4. When blanking input/ripple-blanking output is open or held at a high logic level, and a low logic level is applied to lamp-test input, all segment outputs go to a high logic level.

FIGURE 3. Truth tables (Continued)

Device type 08

DECIMAL OR FUNCTION	INPUTS							OUTPUTS							NOTE
	LT	RBI	D	C	B	A	BI/RBO	a	b	c	d	d	f	g	
0	H	H	L	L	L	L	H	H	H	H	H	H	H	L	1 1
1	H	X	L	L	L	L	H	L	H	H	L	H	L	L	
2	H	X	L	L	L	H	H	H	H	L	H	L	L	H	
3	H	X	L	L	L	H	H	H	H	L	L	L	L	H	
4	H	X	L	L	L	L	H	L	L	H	H	L	H	H	
5	H	X	L	L	H	L	H	L	L	H	H	L	H	H	
6	H	X	L	L	H	H	H	L	L	H	H	L	L	H	
7	H	X	L	L	H	H	H	H	H	H	L	L	L	H	
8	H	X	H	L	L	L	H	H	H	H	L	L	H	H	
9	H	X	H	L	L	L	H	H	H	H	L	L	H	H	
10	H	X	H	L	L	H	H	L	L	L	H	L	L	H	
11	H	X	H	L	H	L	H	L	L	H	L	L	L	H	
12	H	X	H	H	L	L	H	L	L	L	L	L	H	H	
13	H	X	H	H	H	L	H	L	L	L	L	L	H	H	
14	H	X	H	H	H	L	H	L	L	L	L	L	L	L	
15	H	X	H	H	H	H	H	L	L	L	L	L	L	L	2 3 4
BI	X	X	X	X	X	X	L	L	L	L	L	L	L	L	
RBI	H	L	L	L	L	L	L	L	L	L	L	L	L	L	
LT	L	X	X	X	X	X	H	H	H	H	H	H	H	H	

NOTES:

1. BI/RBO is wire-OR logic serving as blanking input (BI) and/or ripple-blanking output (RBO). The blanking input must be open or held at a high logic level when output functions 0 through 15 are desired, and ripple-blanking input (RBI) must be open or at a high logic level during the decimal 0 output. X = input may be high or low.
2. When a low logic level is applied to the blanking input (forced condition) all segment outputs go to a low logic level regardless of the state of any other input condition.
3. When ripple-blanking input (RBI) is at a low logic level, lamp test input is at high logic level and A = B = C = D = low logic level, all segment outputs go to a low logic level and the ripple-blanking output goes to a low logic level (response condition).
4. When blanking input/ripple-blanking output is open or held at a high logic level, and a low logic level is applied to lamp-test input, all segment outputs go to a high logic level.

FIGURE 3. Truth tables (Continued)

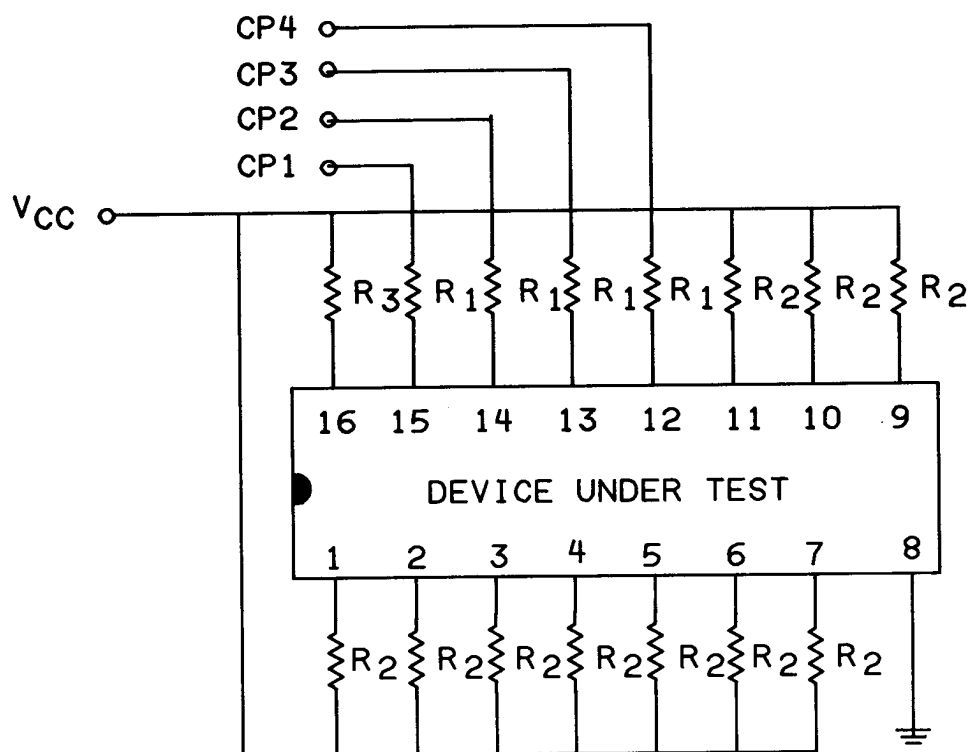
Device type 09

INPUTS						OUTPUTS							
DECIMAL OR FUNCTION	D	C	B	A	BI	a	b	c	d	e	f	g	NOTE
0	L	L	L	L	H	H	H	H	H	H	H	L	1
1	L	L	L	H	H	L	H	H	L	L	L	L	
2	L	L	H	L	H	L	H	L	H	L	L	H	
3	L	L	H	H	H	H	H	H	H	L	L	H	
4	L	H	L	L	H	L	H	H	L	L	H	H	
5	L	H	L	L	H	H	L	H	L	L	H	H	
6	L	H	H	L	H	L	L	H	H	L	H	H	
7	L	H	H	H	H	H	H	H	L	L	L	L	
8	H	L	L	L	H	H	H	H	H	L	H	H	
9	H	L	L	H	H	H	H	H	L	L	H	H	
10	H	L	H	L	H	L	L	L	H	L	L	H	
11	H	L	H	H	H	L	L	H	H	L	L	H	
12	H	H	L	L	H	L	H	L	L	L	H	H	
13	H	H	L	L	H	H	L	L	L	L	H	H	
14	H	H	H	L	H	L	L	L	H	H	H	H	
15	H	H	H	H	H	L	L	L	L	L	L	L	
BI	X	X	X	X	L	L	L	L	L	L	L	L	2

- NOTES: 1. The blanking input must be open or held at a high logic level when output functions 0 through 15 are desired.
2. When a low logic level is applied to the blanking input all segment outputs go to a low logic level regardless of the state of any other input condition. X = input may be high or low.
3. Qualification requirements are removed for this device type.

FIGURE 3. Truth tables. (Continued)

Device type 01, 02, and 03

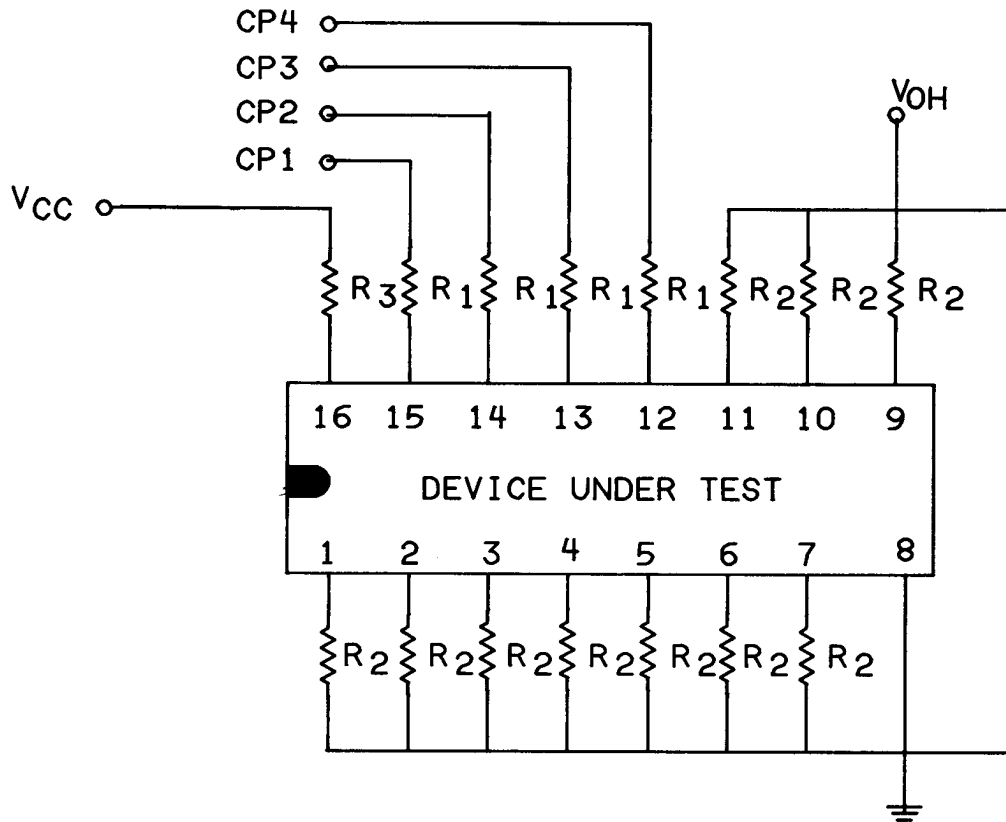


NOTES:

1. V_{CC} is such that the minimum voltage at the device terminals is 5 volts.
2. $R_1 = 27\Omega \pm 5\%$; $R_2 = 270\Omega \pm 5\%$; $R_3 = 4.7\Omega \pm 5\%$.
3. CP1 -- 100 kHz, with minimum $V_{IN} = 3$ V, and minimum duty cycle of 50%.
 CP2 -- 50 kHz, with minimum $V_{IN} = 3$ V, and minimum duty cycle of 50%.
 CP3 -- 25 kHz, with minimum $V_{IN} = 3$ V, and minimum duty cycle of 50%.
 CP4 -- 12.5 kHz, with minimum $V_{IN} = 3$ V, and minimum duty cycle of 50%.
4. Qualification requirements are removed for device types 02, 03.

FIGURE 4. Burn-in and life test circuit.

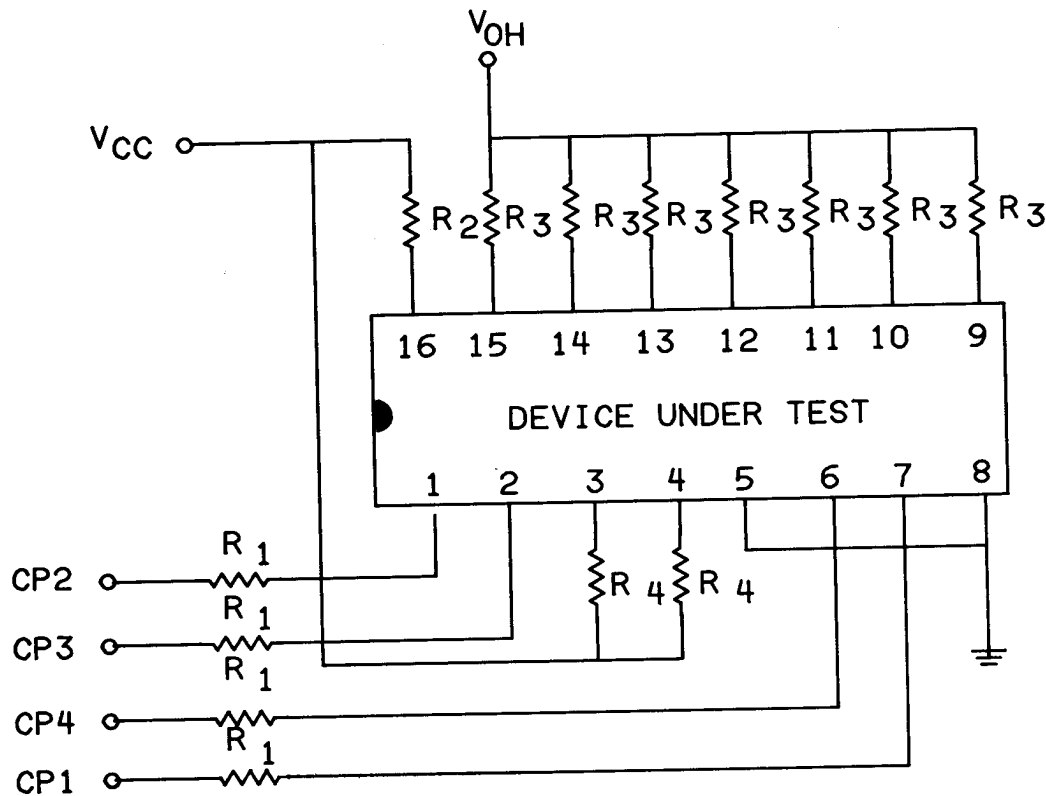
Device types 04 and 05



NOTES:

1. V_{CC} is such that the minimum voltage at the device terminals is 5 volts.
2. $R_1 = 27\Omega \pm 5\%$; $R_3 = 4.7\Omega \pm 5\%$.
3. $R_2 = 400\Omega \pm 5\%$ for device type 04.
 $R_2 = 200\Omega \pm 5\%$ for device type 05.
4. V_{OH} is such that the minimum voltage at the device terminals is 30 volts for device type 04 and 15 volts for device type 05.
5. CP_1 -- 100 kHz, with minimum $V_{IN} = 3$ V, and minimum duty cycle of 50%.
 CP_2 -- 50 kHz, with minimum $V_{IN} = 3$ V, and minimum duty cycle of 50%.
 CP_3 -- 25 kHz, with minimum $V_{IN} = 3$ V, and minimum duty cycle of 50%.
 CP_4 -- 12.5 kHz, with minimum $V_{IN} = 3$ V, and minimum duty cycle of 50%.

FIGURE 4. Burn-in and life test circuit - Continued.

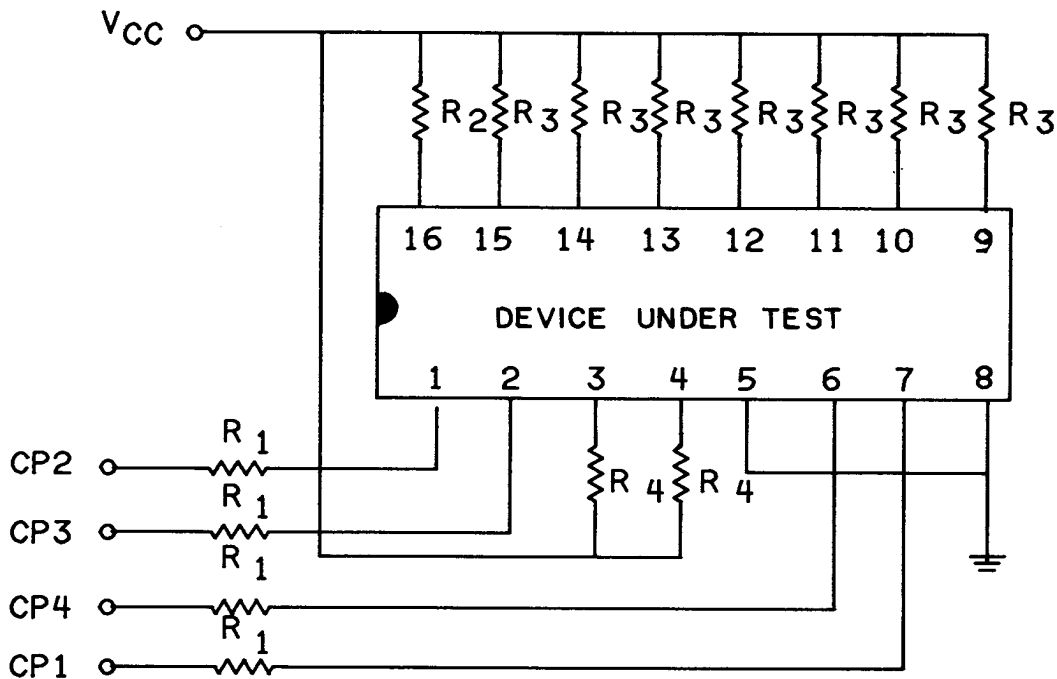
Device types 06 and 07

NOTES:

1. V_{CC} is such that the minimum voltage at the device terminals is 5 volts.
2. $R_1 = 27\Omega \pm 5\%$; $R_2 = 4.7\Omega \pm 5\%$; $R_4 = 560\Omega \pm 5\%$.
3. $R_3 = 830\Omega \pm 5\%$ for device type 06.
 $R_3 = 470\Omega \pm 5\%$ for device type 07.
4. V_{OH} is such that the minimum voltage at the device terminals is 30 volts for device type 06 and 15 volts for device type 07.
5. CP1 -- 100 kHz, with minimum $V_{IN} = 3$ V, and minimum duty cycle of 50%.
 CP2 -- 50 kHz, with minimum $V_{IN} = 3$ V, and minimum duty cycle of 50%.
 CP3 -- 25 kHz, with minimum $V_{IN} = 3$ V, and minimum duty cycle of 50%.
 CP4 -- 12.5 kHz, with minimum $V_{IN} = 3$ V, and minimum duty cycle of 50%.

FIGURE 4. Burn-in and life test circuit - Continued.

Device type 08

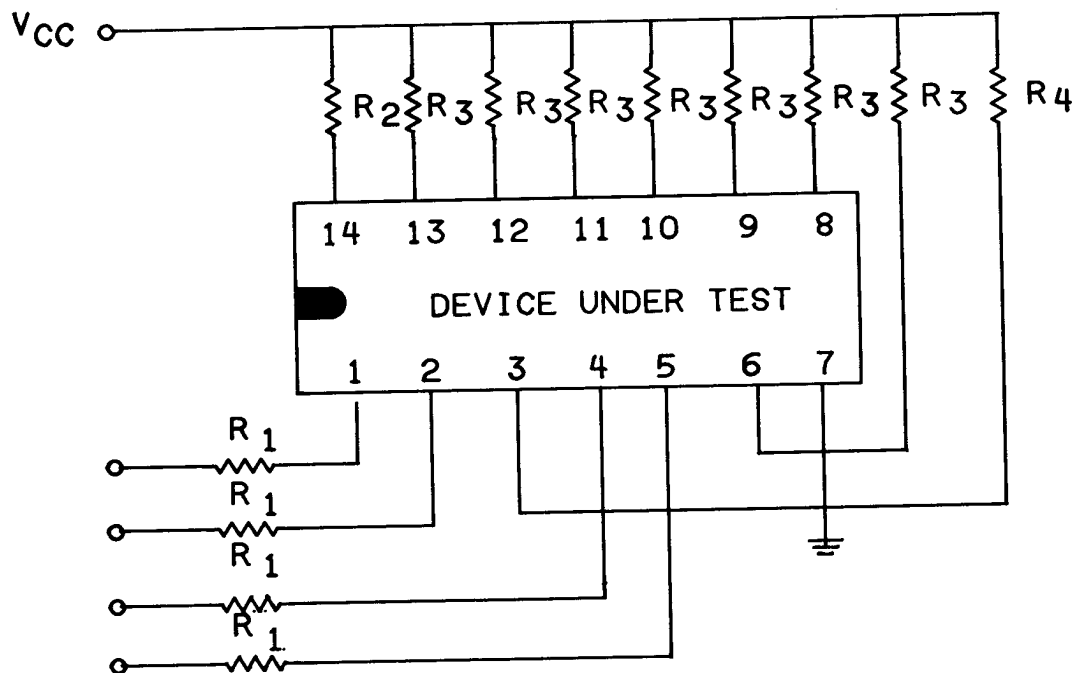


NOTES:

1. V_{CC} is such that the minimum voltage at the device terminals is 5 volts.
2. $R_1 = 27\Omega \pm 5\%$; $R_2 = 4.7\Omega \pm 5\%$; $R_3 = 820\Omega \pm 5\%$; $R_4 = 560\Omega \pm 5\%$.
3. CP1 -- 100 kHz, with minimum $V_{IN} = 3$ V, and minimum duty cycle of 50%.
 CP2 -- 50 kHz, with minimum $V_{IN} = 3$ V, and minimum duty cycle of 50%.
 CP3 -- 25 kHz, with minimum $V_{IN} = 3$ V, and minimum duty cycle of 50%.
 CP4 -- 12.5 kHz, with minimum $V_{IN} = 3$ V, and minimum duty cycle of 50%.

FIGURE 4. Burn-in and life test circuit - Continued.

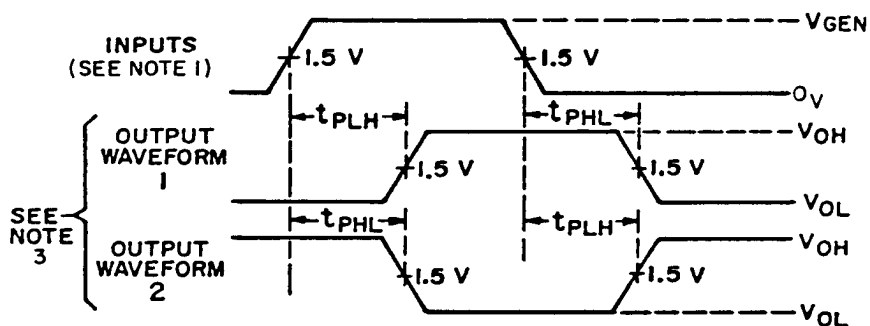
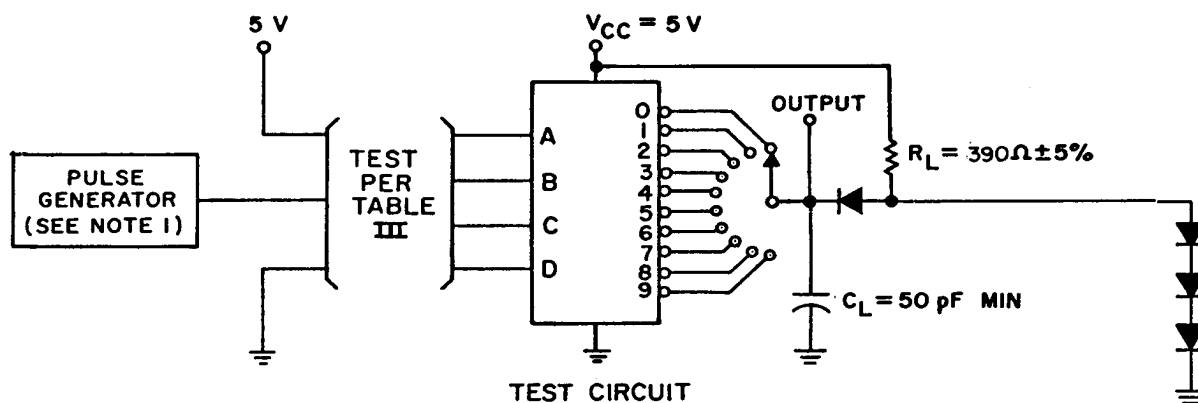
Device type 09



NOTES:

1. V_{CC} is such that the minimum voltage at the device terminals is 5 volts.
2. $R_1 = 27\Omega \pm 5\%$; $R_2 = 4.7\Omega \pm 5\%$; $R_3 = 470\Omega \pm 5\%$; $R_4 = 1\text{ k}\Omega \pm 5\%$.
3. CP1 -- 100 kHz, with minimum $V_{IN} = 3\text{ V}$, and minimum duty cycle of 50%.
 CP2 -- 50 kHz, with minimum $V_{IN} = 3\text{ V}$, and minimum duty cycle of 50%.
 CP3 -- 25 kHz, with minimum $V_{IN} = 3\text{ V}$, and minimum duty cycle of 50%.
 CP4 -- 12.5 kHz, with minimum $V_{IN} = 3\text{ V}$, and minimum duty cycle of 50%.
4. Qualification requirements are removed for this device type.

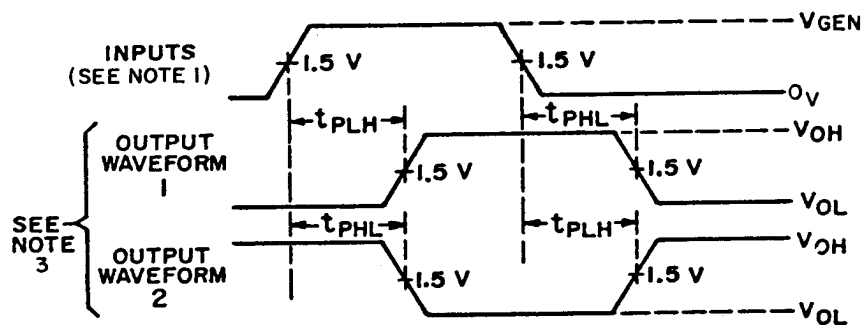
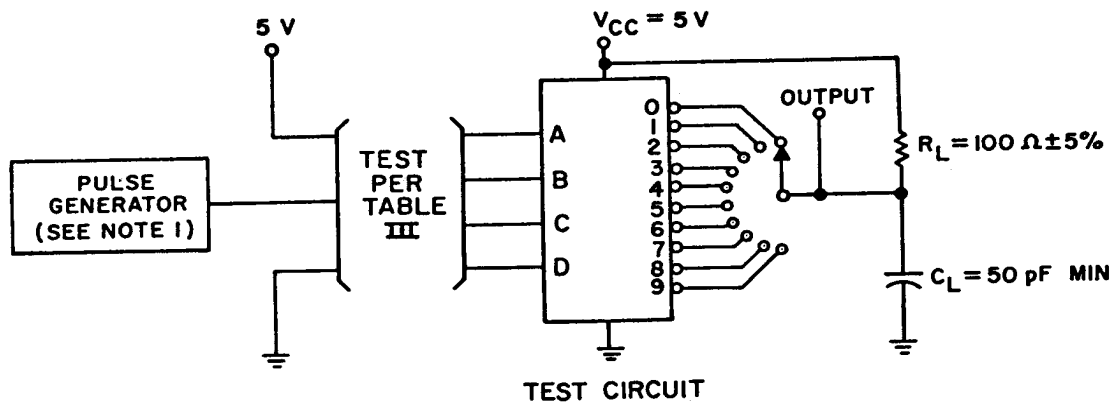
FIGURE 4. Burn-in and life test circuit - Continued.



NOTES:

1. The pulse generator has the following characteristics: $V_{gen} = 3.0$ V minimum, t_{PLH} (0.7 V to 2.7 V) and t_{PHL} (2.7 V to 0.7 V) ≤ 10 ns, PRR = 1 MHz, and minimum duty cycle = 50%.
2. C_L includes probe and jig capacitance.
3. Input - output waveform combination in accordance with the truth tables (see figure 3).
4. All diodes are 1N3064 or equivalent.
5. Qualification requirements are removed for device types 02, 03.

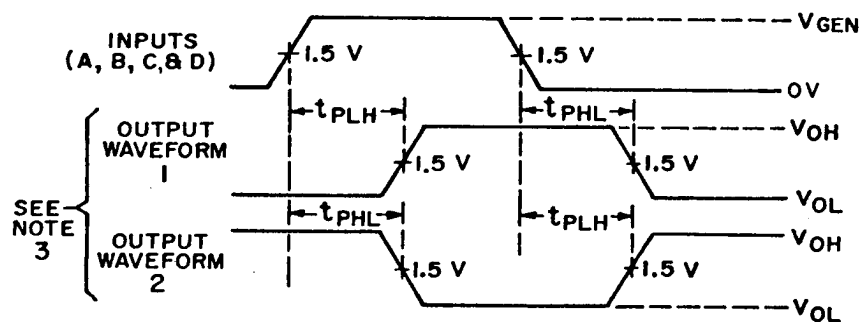
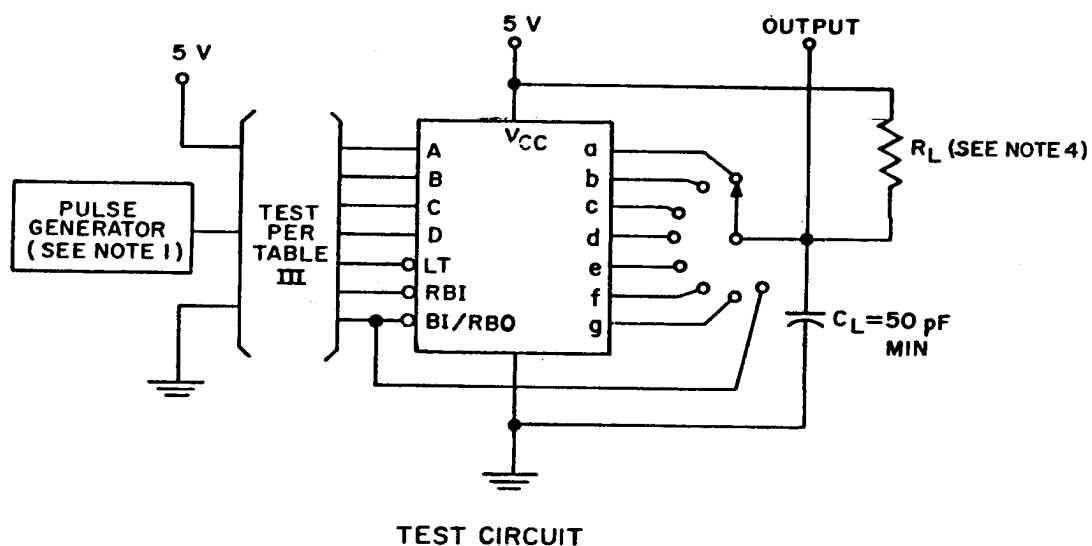
FIGURE 5. Switching times for device types 01, 02, and 03.



NOTES:

1. The pulse generator has the following characteristics: $V_{gen} = 3.0$ V minimum, t_{PLH} (0.7 V to 2.7 V) and t_{PHL} (2.7 V to 0.7 V) ≤ 10 ns, PRR = 1 MHz, and minimum duty cycle = 50%.
2. C_L includes probe and jig capacitance.
3. Input - output waveform combination in accordance with the truth tables (see figure 3).

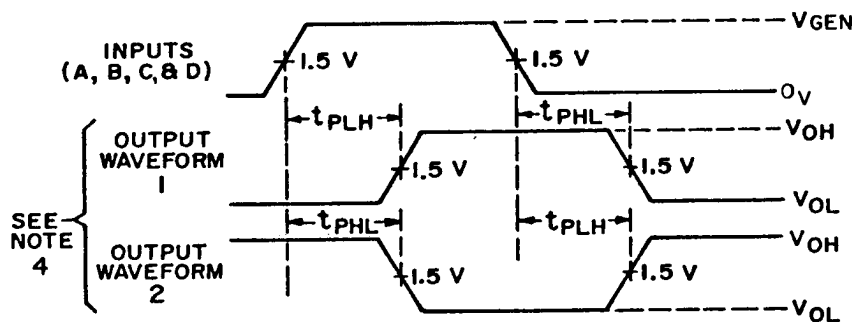
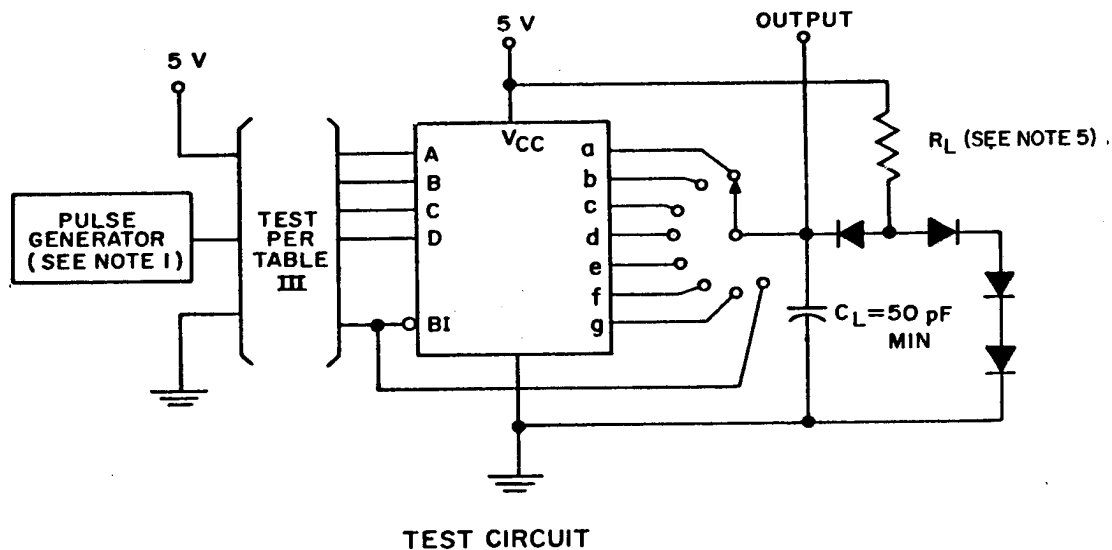
FIGURE 6. Switching times for device types 04 and 05.



NOTES:

1. The pulse generator has the following characteristics: $V_{gen} = 3.0$ V minimum, t_{TLH} (0.7 V to 2.7 V) and t_{THL} (2.7 V to 0.7 V) ≤ 10 ns, PRR = 1 MHz, and minimum duty cycle = 50%.
2. C_L includes probe and jig capacitance.
3. Input - output waveform combination in accordance with the truth tables (see figure 3).
4. $R_L = 120\Omega \pm 5\%$ for outputs a thru g; $R_L = 560\Omega \pm 5\%$ for output BI/RBO.

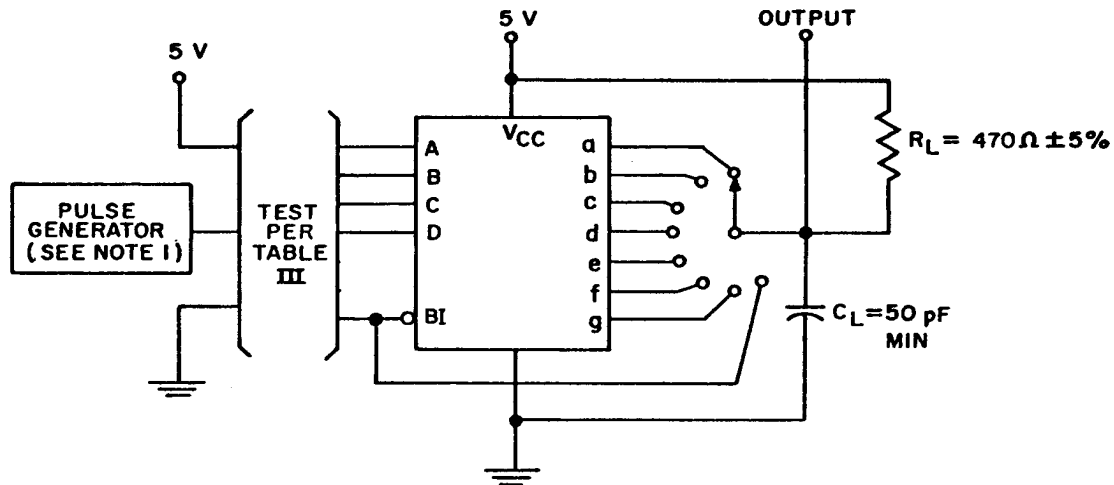
FIGURE 7. Switching times for device types 06 and 07.



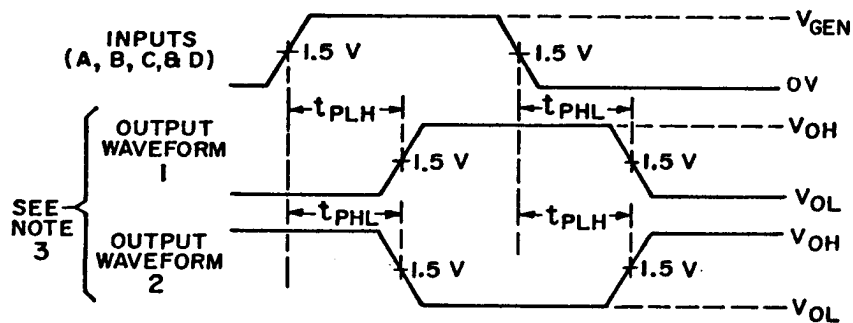
NOTES:

1. The pulse generator has the following characteristics: $V_{gen} = 3.0 \text{ V}$ minimum, t_{TLH} (0.7 V to 2.7 V) and t_{THL} (2.7 V to 0.7 V) $\leq 10 \text{ ns}$, $PRR = 1 \text{ MHz}$, and minimum duty cycle = 50%.
2. C_L includes probe and jig capacitance.
3. All diodes are 1N3064 or equivalent.
4. Input - output waveform combination in accordance with the truth tables (see figure 3).
5. $R_L = 750\Omega \pm 5\%$ for outputs a thru g; $R_L = 560\Omega \pm 5\%$ for output BI/RB0.

FIGURE 8. Switching times for device type 08.



TEST CIRCUIT



NOTES:

1. The pulse generator has the following characteristics: $V_{gen} = 3.0 \text{ V}$ minimum, t_{TLH} (0.7 V to 2.7 V) and t_{THL} (2.7 V to 0.7 V) $\leq 10 \text{ ns}$, $PRR = 1 \text{ MHz}$, and minimum duty cycle = 50%.
2. C_L includes probe and jig capacitance.
3. Input - output waveform combination in accordance with the truth tables (see figure 3).
4. Qualification requirements are removed for this device type.

FIGURE 9. Switching times for device type 09.

TABLE III. Group A inspection for device type 01.
Terminal conditions (pins not designated may be high ≥ 2.4 V or low ≤ 0.8 V or open).

Subgroup	Symbol	MIL-STD-883 method	Cases E, F Test no.	Terminal conditions																Measured terminal	Test limits			
				1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16		Min	Max	Unit	
1 $T_C = 25^\circ C$	V_{OH}	3006 " " " " " " " " "	1 2 3 4 5 6 7 8 9 10	-0.8 mA -0.8 mA -0.8 mA -0.8 mA -0.8 mA -0.8 mA -0.8 mA -0.8 mA -0.8 mA -0.8 mA																				
	V_{OL}	3007 " " " " " " " " "	11 12 13 14 15 16 17 18 19 20	16 mA 16 mA 16 mA 16 mA 16 mA 16 mA 16 mA 16 mA 16 mA 16 mA																				
	V_{IC}		21 22 23 24																					
	I_{IH1}	3010 " " "	25 26 27 28																					
	I_{IH2}	3010 " " "	29 30 31 32																					
	I_{IL}	3009 " " "	33 34 35 36																					
	I_{OS}	3011 " " " " " " " " "	37 38 39 40 41 42 43 44 45 46	GND GND GND GND GND GND GND GND GND GND																				
	I_{CC}	3005	47																					
2				Same tests, terminal conditions and limits as for subgroup 1, except $T_C = 125^\circ C$ and V_{IC} tests are omitted.																				
3				Same tests, terminal conditions and limits as for subgroup 1, except $T_C = -55^\circ C$ and V_{IC} tests are omitted.																				

See note at end of device type 01.

TABLE III. Group A inspection for device type 01 - Continued.
Terminal conditions (pins not designated may be high ≥ 2.4 V or low ≤ 0.8 V or open).

Subgroup	Symbol	MIL-STD-883 method	Cases E, F Test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Test limits		Unit			
				0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15		16	Min		Max		
7 T _C = 25°C	Truth table test		48	L	H	H	H	H	H	H	GND	H	H	H	H	GND	GND	GND	GND	5.0 V						
			49	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	5.0 V						
			50	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	5.0 V						
			51	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	5.0 V						
			52	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	5.0 V						
			53	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	5.0 V						
			54	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	5.0 V						
			55	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	5.0 V						
			56	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	5.0 V						
			57	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	5.0 V						
			58	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	5.0 V						
			59	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	5.0 V						
			60	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	5.0 V						
8 T _C = 25°C	Truth table test		64 thru 79	Same tests as subgroup 7, except T _C = 125°C.																			See note			
			80 thru 95	Same tests as subgroup 7, except T _C = -55°C.																				ns		
			96 & 97	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT			5	32
			98 & 99	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT			OUT	37
9 T _C = 25°C	Truth table test	3003 (Fig. 6)	100 & 101	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	37			
			102 & 103	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	37		
			104 & 105	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	32		
			106 & 107	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	37		
			108 & 109	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	32		
			110 & 111	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	32		
			112 & 113	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	32		
			114 & 115	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	32		
			116 & 117	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	32		
			118 & 119	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	37		
			120 & 121	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	37		
			122 & 123	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	32		
			10 T _C = 125°C			138 & 139	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39
140 & 141	OUT	OUT				OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	46			
142 & 143	OUT	OUT				OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
144 & 145	OUT	OUT				OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
146 & 147	OUT	OUT				OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	46			
148 & 149	OUT	OUT				OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
150 & 151	OUT	OUT				OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
152 & 153	OUT	OUT				OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
154 & 155	OUT	OUT				OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
156 & 157	OUT	OUT				OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
158 & 159	OUT	OUT				OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
160 & 161	OUT	OUT				OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
162 & 163	OUT	OUT				OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
11			164 & 165	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
			166 & 167	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
			168 & 169	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
			170 & 171	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
			172 & 173	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
			174 & 175	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
			176 & 177	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
			178 & 179	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
			180 & 181	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
			182 & 183	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
			184 & 185	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
			186 & 187	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			
			188 & 189	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	39			

NOTE:

Output voltages shall be either:

- (a) H = 2.4 volts minimum and L = 0.4 volts maximum when using a high speed checker double comparator, or
- (b) H \geq 1.5 volts and L \leq 1.5 volts when using a high speed checker single comparator.

TABLE III. Group A inspection for device type 02. 1/
Terminal conditions (pins not designated may be high ≥ 2.4 V or low ≤ 0.8 V or open).

Subgroup	Symbol	MIL-STD-883 method	Case E Test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal		Test limits		Unit
				0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	V _{CC}	Measured terminal	Min	Max	
1 T _C = 25°C	V _{OH}	3006	1	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	V
		"	2	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	"
		"	3	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	"
		"	4	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	"
		"	5	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	"
		"	6	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	"
		"	7	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	"
		"	8	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	"
		"	9	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	"
		"	10	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	-0.8 mA	"
	V _{OL}	3007	11	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	"
		"	12	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	"
		"	13	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	"
		"	14	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	"
		"	15	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	"
		"	16	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	"
		"	17	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	"
		"	18	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	"
		"	19	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	"
		"	20	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	16 mA	"
	V _{IC}		21																					"
			22																					"
			23																					"
			24																					"
	V _{IH1}	3010	25																					"
		"	26																					"
		"	27																					"
		"	28																					"
	I _{IH2}	3010	29																					"
		"	30																					"
		"	31																					"
		"	32																					"
	I _{IL}	3009	33																					"
		"	34																					"
		"	35																					"
		"	36																					"
	I _{OS}	3011	37	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	"
		"	38	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	"
		"	39	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	"
		"	40	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	"
		"	41	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	"
		"	42	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	"
		"	43	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	"
		"	44	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	"
		"	45	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	"
		"	46	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	GND	"
	I _{CC}	3005	47																					"
																								"

2 Same tests, terminal conditions and limits as for subgroup 1, except T_C = 125°C and V_{IC} tests are omitted.

3 Same tests, terminal conditions and limits as for subgroup 1, except T_C = -55°C and V_{IC} tests are omitted.

See notes at end of device type 02.

TABLE III. Group A Inspection for device type 02 - Continued. 1/
Terminal conditions (pins not designated may be high ≥ 2.4 V or low ≤ 0.8 V or open).

Subgroup	Symbol	MIL-STD-883 method	Cases E test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Test limits		Unit		
				Measured terminal	Min	Max																		
7 T _C = 25°C	Truth table test		48	L	H	H	H	H	H	H	GND	H	H	H	H	GND	5.0 V	5.0 V	5.0 V					
			49	H	L	H	"	"	"	"	"	"	"	"	"	"	"	GND	GND	5.0 V				
			50	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	"				
			51	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	"				
			52	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	"				
			53	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	GND	GND	5.0 V			
			54	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	GND	GND	5.0 V			
			55	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	5.0 V	"			
			56	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	5.0 V	"			
			57	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	5.0 V	"			
			58	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	5.0 V	"			
			59	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	5.0 V	"			
			60	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	5.0 V	"			
			61	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	GND	GND	GND			
62	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	GND	GND	5.0 V						
63	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	GND						
8	Truth table test		Same tests as subgroup 7, except T _C = 125°C.																					
			Same tests as subgroup 7, except T _C = -55°C.																					
9 T _C = 25°C	tp _{HL} tp _{LL}	3003 (Fig. 6)	96 & 97	OUT	OUT	OUT	OUT	OUT	OUT	OUT	GND	OUT	OUT	OUT	OUT	GND	5.0 V	IN	5.0 V	IA to 1	5	32	ns	
			98 & 99	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	IA to 7	"	"	"
			100 & 101	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	IA to 9	"	"	"
			102 & 103	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	IA to 0	"	37	"
			104 & 105	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	IN	5.0 V	IB to 2	"	32	"
			106 & 107	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	IB to 6	"	32	"
			108 & 109	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	IB to 9	"	32	"
			110 & 111	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	IB to 3	"	37	"
			112 & 113	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	IN	5.0 V	IB to 8	"	37	"
			114 & 115	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	IC to 0	"	32	"
			116 & 117	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	IC to 5	"	32	"
			118 & 119	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	IC to 8	"	32	"
			120 & 121	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	IC to 1	"	37	"
			122 & 123	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	IC to 2	"	"	"
124 & 125	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	IN	5.0 V	IC to 9	"	"	"			
9 T _C = 25°C	Truth table test	3003 (Fig. 6)	126 & 127	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	5.0 V	ID to 3	"	32	"	
			128 & 129	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	5.0 V	ID to 4	"	32	"
			130 & 131	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	5.0 V	ID to 5	"	37	"
			132 & 133	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	5.0 V	ID to 6	"	"	"
			134 & 135	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	5.0 V	ID to 7	"	"	"
			136 & 137	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	5.0 V	ID to 8	"	"	"
			138 & 139	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	5.0 V	ID to 9	"	"	"
			140 & 141	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	5.0 V	ID to 10	"	"	"
			142 & 143	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	5.0 V	ID to 11	"	"	"
			144 & 145	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	OUT	5.0 V	"	"	5.0 V	ID to 12	"	"	"

See notes at end of device type 02.

TABLE III. Group A inspection for device type Q2 - Continued. 1/
Terminal conditions (pins not designated may be high ≥ 2.4 V or low ≤ 0.8 V or open).

Subgroup	Symbol	MIL-STD-883 method	Cases E test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal		Test limits		Unit		
				0	1	2	3	4	5	6	7	8	GND	7	8	9	10	11	D	C	B	A	VCC		Min	Max
10 T _C = 125°C	t _{pHL} t _{pLH}	3003 (Fig. 6)	144 & 145		OUT							OUT				GND	5.0 V	GND	IN	15.0 V	A to 1	5	39	ns		
			146 & 147											OUT	15.0 V	5.0 V	5.0 V	5.0 V			A to 7		39			
			148 & 149																GND			A to 9		39		
			150 & 151	OUT															5.0 V			A to 0		46		
			152 & 153							OUT									GND			A to 6		46		
			154 & 155			OUT													GND	15.0 V	15.0 V	B to 2		39		
			156 & 157								OUT								GND	15.0 V	15.0 V	B to 6		39		
			158 & 159												OUT	15.0 V	5.0 V	5.0 V		GND		B to 9		39		
			160 & 161				OUT												5.0 V			B to 3		46		
			162 & 163											OUT					GND			B to 7		46		
			164 & 165												OUT				5.0 V			B to 8		46		
			166 & 167													OUT			5.0 V	5.0 V	5.0 V	C to 0		39		
			168 & 169								OUT								GND			C to 5		39		
			170 & 171	OUT															5.0 V	GND		C to 8		39		
			172 & 173		OUT														GND			C to 1		46		
			174 & 175																GND	5.0 V		C to 2				
			176 & 177																5.0 V	5.0 V		C to 4				
			178 & 179				OUT												GND			C to 9		34		
			180 & 181																5.0 V	GND		D to 3		34		
			182 & 183			OUT													15.0 V	5.0 V	5.0 V	D to 4		46		
			184 & 185				OUT													GND			D to 5			
			186 & 187									OUT									5.0 V		D to 6			
			188 & 189												OUT					5.0 V	GND		D to 7			
190 & 191													OUT				5.0 V	5.0 V		D to 8						
11	Same tests, terminal conditions and limits as for subgroup 10 except T _C = -55°C.																									

1/ Qualification requirements are removed for this device type.

2/ Output voltages shall be either:

- (a) $H = 2.4$ volts minimum and $L = 0.4$ volts maximum when using a high speed checker double comparator,
or
(b) $H \geq 1.5$ volts and $L \leq 1.5$ volts when using a high speed checker single comparator.

TABLE III. Group A inspection for device type 03. 1/
Terminal conditions (pins not designated may be high ≥ 2.4 V or low ≤ 0.8 V or open).

[illegible]

See notes at end of device type 03.

TABLE III. Group A inspection for device type 03 - Continued. 1/
Terminal conditions (pins not designated may be high ≥ 2.4 V or low ≤ 0.8 V or open).

Subgroup	Symbol	MIL-STD-883 method	Cases E and F Test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Test limits		Unit																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																							
				0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15		16	Min		Max																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																						
7 T _C = 25°C	Truth table test		48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63	L	H	H	H	H	H	H	GND	H	H	H	GND	GND	5.0 V	5.0 V	GND	5.0 V	See note 2/																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																									
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9 T _C = 25°C	Truth table test		Same tests as subgroup 7, except T _C = -55°C.																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																											
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			t _{PHL} t _{PLH}	3003 (Fig. 6)	96 & 97 98 & 99 100 & 101 102 & 103 104 & 105 106 & 107 108 & 109 110 & 111 112 & 113 114 & 115 116 & 117 118 & 119 120 & 121 122 & 123 124 & 125 126 & 127 128 & 129 130 & 131 132 & 133 134 & 135 136 & 137 138 & 139 140 & 141 142 & 143 144 & 145	OUT		OUT					GND		OUT		5.0 V	GND	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN	5.0 V	IN</

See notes at end of device type 03.

TABLE III. Group A inspection for device type 03 - Continued. 1/
Terminal conditions (pins not designated may be high ≥ 2.4 V or low ≤ 0.8 V or open).

Subgroup	Symbol	MIL-STD-883 method	Cases E.P. test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Test limits				
				0	1	2	3	4	5	6	GND	7	8	9	D	C	B	A	V _{CC}	Measured terminal	Min	Max	Unit	
10	t _{PHL} T _C = 125°C	3003 (Fig. 6)	1146 & 147	OUT							GND		OUT		GND	GND	5.0 V	IN	15.0 V	A to 0	5	39	ns	
			1148 & 149			OUT							15.0 V	GND		5.0 V	IN		IN		A to 8	"	39	
			1150 & 151											GND	"	5.0 V	IN		IN		A to 2	"	46	
			1152 & 153				OUT							GND	"	5.0 V	IN		5.0 V		B to 3	"	39	
			1154 & 155											GND	"	"	GND		GND		B to 4	"	"	
			1156 & 157											15.0 V	"	"	"	GND		B to 5	"	"		
			1158 & 159											5.0 V	"	"	5.0 V		B to 6	"	"			
			1160 & 161											GND	GND	"	GND		B to 0	"	46			
			1162 & 163			OUT								GND	5.0 V	"	GND		B to 1	"	"			
			1164 & 165											15.0 V	"	"	5.0 V		B to 2	"	"			
			1166 & 167											GND	5.0 V	"	GND		B to 8	"	"			
			1168 & 169											5.0 V	5.0 V	"	IN	5.0 V	GND		B to 2	"	39	
			1170 & 171		OUT								OUT	5.0 V	GND	"	5.0 V	GND		C to 1	"	46		
			1172 & 173											GND	"	"	GND	5.0 V		C to 3	"	"		
			1174 & 175											GND	"	"	"	GND		C to 4	"	"		
			1176 & 177											5.0 V	"	"	"	5.0 V		C to 5	"	"		
			1178 & 179											5.0 V	"	"	5.0 V		C to 6	"	"			
			1180 & 181												OUT		IN	5.0 V	5.0 V		D to 1	"	39	
			1182 & 183														"	"	5.0 V		D to 2	"	"	
			1184 & 185			OUT											"	"	15.0 V		D to 4	"	39	
			1186 & 187														"	"	GND	5.0 V		D to 5	"	46
			1188 & 189														"	"	GND	GND		D to 6	"	"
			1190 & 191														"	"	"	5.0 V		D to 7	"	"
			1192 & 193														"	"	GND	5.0 V		D to 8	"	"
			1194 & 195													OUT	OUT	"	5.0 V	5.0 V		D to 9	"	"
11	Same tests, terminal conditions and limits as for subgroup 10 except T _C = -55°C.																							

1/ Qualification requirement are removed for this device type.

2/ Output voltage shall be either:

- (a) H = 2.4 volts minimum and L = 0.4 volts maximum when using a high speed checker double comparator, or
(b) H \geq 1.5 volts and L \leq 1.5 volts when using a high speed checker single comparator.

TABLE III. Group A inspection for device types 04 and 05. Terminal conditions (pins not designated may be high ≥ 2.4 V or low ≤ 0.8 V or open).

Subgroup	Symbol	MIL-STD-883 method	Cases E.F.		Test limits																			
			Test no.		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Min	Max	Unit
1 T _C = 25°C	V _{OL}	3007	1	80 mA																				
		"	2	80 mA																				
		"	3	80 mA																				
		"	4	80 mA																				
		"	5	80 mA																				
		"	6	80 mA																				
		"	7	80 mA																				
		"	8	80 mA																				
		"	9	80 mA																				
		"	10	80 mA																				
	V _{OL2}	3007	11	20 mA																				
		"	12	20 mA																				
		"	13	20 mA																				
		"	14	20 mA																				
		"	15	20 mA																				
		"	16	20 mA																				
		"	17	20 mA																				
		"	18	20 mA																				
		"	19	20 mA																				
		"	20	20 mA																				
	I _{CEX} (See note 1)		21	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	μA
			22	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	"
			23	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	"
			24	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	"
			25	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	"
			26	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	"
			27	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	"
			28	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	"
			29	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	"
			30	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	Y	"
	V _{IC}		31																					V
			32																				"	
			33																				"	
			34																				"	
	I _{IL}		35																				mA	
			36																				"	
			37																				"	
			38																				"	
	I _{IH1}		39																				μA	
			40																				"	
			41																				"	
			42																				"	
	I _{IH2}		43																				μA	
			44																				"	
			45																				"	
			46																				"	
	I _{CC}		47																				mA	
		3005																						

2 Same tests. terminal conditions and limits as for subgroup 1, except $T_c = 125^\circ\text{C}$ and VIC tests are omitted.

3 | Same tests, terminal conditions and limits as for subgroup 1, except $T_C = -55^\circ\text{C}$ and VIC tests are omitted.

See notes at end of device types 04 and 05.

TABLE III. Group A inspection for device types 04 and 05 - Continued.
Terminal conditions (pins not designated may be high ≥ 2.4 V or low ≤ 0.8 V or open).

Subgroup	Symbol	MIL-STD-883 method	Cases E.F. Test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Test limits				
				0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Min	Max	Unit
7 T _C = 25°C	Truth table test		48	L	H	H	H	H	H	H	GND	H	H	H	GND	GND	GND	GND	5.0 V					
			49	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	5.0 V				
			50	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V				
			51	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V				
			52	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V				
			53	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V				
			54	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V				
			55	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V				
			56	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V				
			57	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V				
			58	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V				
			59	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V				
			60	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V				
			61	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V				
62	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V							
63	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V							
8 T _C = 25°C	Truth table test		64 thru 79	Same tests as subgroup 7, except T _C = 125°C.																				
			80 thru 95	Same tests as subgroup 7, except T _C = -55°C.																				
9 T _C = 25°C	Truth table test	3003 (Fig. 7)	96 & 97	OUT	OUT																			
			98 & 99		OUT																			
			100 & 101			OUT																		
			102 & 103				OUT																	
			104 & 105					OUT																
			106 & 107						OUT															
			108 & 109							OUT														
			110 & 111								OUT													
			112 & 113									OUT												
			114 & 115										OUT											
			116 & 117											OUT										
			118 & 119												OUT									
			120 & 121													OUT								
			122 & 123														OUT							
			124 & 125															OUT						
			126 & 127																OUT					
			128 & 129																	OUT				
			130 & 131																		OUT			
			132 & 133																			OUT		
			134 & 135																				OUT	
			136 & 137																					OUT
			138 & 139																					

See notes at end of device types 04 and 05.

TABLE III. Group A inspection for device types 04 and 05 - Continued.
Terminal conditions (pins not designated may be high ≥ 2.4 V or low ≤ 0.8 V or open).

Subgroup	Symbol	MIL-STD-883 method	Cases E _F Test no.	Terminal conditions																Test limits		Unit		
				1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Min		Max	
10	t_{PHL} $T_C = 125^\circ\text{C}$ t_{PLH}	3003 (Fig. 7)	140 & 141	OUT																				
			142 & 143		OUT																			
			144 & 145																					
			146 & 147																					
			148 & 149																					
			150 & 151																					
			152 & 153																					
			154 & 155																					
			156 & 157																					
			158 & 159																					
			160 & 161																					
			162 & 163																					
			164 & 165																					
			166 & 167																					
11	Same tests, terminal conditions and limits as for subgroup 10, except $T_C = -55^\circ\text{C}$.		168 & 169																					
			170 & 171																					
			172 & 173																					
			174 & 175																					
			176 & 177																					
			178 & 179																					
			180 & 181																					
			182 & 183																					

* NOTES:

1/ Y = 30 volts for device type 04 and 15 volts for device type 05.

2/ Output voltages shall be either:

- (a) H = 2.4 volts minimum and L = 0.4 volts minimum when using a high speed checker double comparator, or
(b) H \geq 1.5 volts and L \leq 1.5 volts when using a high speed checker single comparator.

TABLE III. Group A inspection for device types 06 and 07.
Terminal conditions (pins not designated may be high ≥ 2.4 V or low ≤ 0.8 V or open).

Subgroup	Symbol	MIL-STD-883C method	Cases E.F. test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Test Limits			
				B	C	LT	R80	R81	D	A	GND	e	d	c	b	a	g	f	VCC	Measured terminal	Unit	Mfn	Max
1 T _C = 25°C	VOL1 (See note 1)	3007	1	X	X	0.8 V		X	X	X	X	GND	40 mA	40 mA	40 mA	40 mA	40 mA	40 mA	4.5 V	e	0.4 V		
			2	X	X			X	X	X	X									d			
			3	X	X			X	X	X	X									c			
			4	X	X			X	X	X	X									b			
			5	X	X			X	X	X	X									a			
			6	X	X			X	X	X	X									g			
			7	X	X			X	X	X	X									f			
	VOL2	3007	8	0.8 V	0.8 V	2.0 V	8 mA	0.8 V	0.8 V	0.8 V										R80	0.4 V		
	I _{CEX} (See note 2)		9	X	X			X	X	X	X	Y	Y	Y	Y	Y	Y	Y		e	250 μA		
			10	X	X			X	X	X	X									d			
			11	X	X			X	X	X	X									c			
			12	X	X			X	X	X	X									b			
			13	X	X			X	X	X	X									a			
2	V _{OH}	3006	16	0.8 V	0.8 V	2.0 V	-2 mA	2.0 V	0.8 V	0.8 V										R80	2.4 V		
	V _{TC}		17	-12 mA	-12 mA		-12 mA	-12 mA	-12 mA	-12 mA										B	-1.5 V		
			18	-12 mA	-12 mA		-12 mA	-12 mA	-12 mA	-12 mA										C			
			19	-12 mA	-12 mA		-12 mA	-12 mA	-12 mA	-12 mA										R80			
			20	-12 mA	-12 mA		-12 mA	-12 mA	-12 mA	-12 mA										R81			
			21	-12 mA	-12 mA		-12 mA	-12 mA	-12 mA	-12 mA										D			
	I _{IL1}	3009	23(See note 4)	10.4 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V									5.5 V	B	-1.7		
			23 CKT C	10.4 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V										B	-1.3		
			24(See note 4)	10.4 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V										C	-1.3		
			25(See note 4)	10.4 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V										LT	-1.3		
			26(See note 4)	10.4 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V										R81	-1.3		
3	I _{IL2}	3009	29				10.4 V	5.5 V	5.5 V	5.5 V										R80	-1.7		
	I _{IH1}	3010	30	2.4 V	GND	GND	GND	GND	GND	GND										B	40 μA		
			31	GND	2.4 V	GND	GND	GND	GND	GND										C			
			32	GND	GND	2.4 V	GND	GND	GND	GND										LT			
			33	GND	GND	GND	2.4 V	GND	GND	GND										R81			
			34	GND	GND	GND	GND	GND	GND	GND										D			
	I _{IH2}	3010	36	5.5 V	GND	GND	GND	GND	GND	GND										A			
			37	GND	5.5 V	GND	GND	GND	GND	GND										B	100 μA		
			38	GND	GND	5.5 V	GND	GND	GND	GND										C			
			39	GND	GND	GND	5.5 V	GND	GND	GND										LT			
			40	GND	GND	GND	GND	5.5 V	GND	GND										R81			
4	I _{OS}	3011	42				GND	GND	GND	GND										A			
	I _{CC}	3005	43	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V										R80	-4 mA		
			43	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V	5.5 V										VCC	85 mA		
Same tests, terminal conditions and limits as for subgroup 1, except T _C = 125°C and V _{TC} tests are omitted.																							
Same tests, terminal conditions and limits as for subgroup 1, except T _C = -55°C and V _{TC} tests are omitted.																							

See notes at the end of device types 06 and 07.

TABLE III. Group A Inspection for device types 06 and 07 - Continued.
Terminal conditions (pins not designated may be high ≥ 2.0 V or low ≤ 0.8 V or open).

Subgroup	Symbol	MIL-STD-883C method	Cases E _F test no.	Test Limits																						
				1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Unit					
7	Truth table test		44	GND	GND	5.0 V		5.0 V	GND	GND		L	L	L	L	L	L	L	L	5.0 V	SEE NOTE 3					
				GND	GND			5.0 V	X					L	L	L	L	L	L	L						
				5.0 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		"	"			
				5.0 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		"	"			
				GND	5.0 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		"	"			
				GND	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		"	"			
				5.0 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		"	"			
				5.0 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		"	"			
				GND	GND	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		"	"			
				GND	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		"	"			
				5.0 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		"	"			
				5.0 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		"	"			
				GND	5.0 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		"	"			
				GND	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		"	"			
				5.0 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		"	"			
				5.0 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		"	"			
				5.0 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		"	"			
				8	Truth table test		63 - 81	Same tests as subgroup 7, except T _C = 125°C.																		
								82 - 100	Same tests as subgroup 7, except T _C = -55°C.																	
				9	t _{PHL} t _{PLH}	3003 (Fig. 8)	101 & 102	GND	GND	5.0 V		5.0 V	GND	GND		L	L	L	L	L		L	L	L	5.0 V	IA to a
103 & 104	GND	GND	5.0 V					5.0 V	GND	GND		L	L	L	L	L	L	L	L	L	5.0 V	IA to a				
105 & 106	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
107 & 108	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to b			
109 & 110	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to c			
111 & 112	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to d			
113 & 114	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to d			
115 & 116	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to e			
117 & 118	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to e			
119 & 120	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to f			
121 & 122	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to f			
123 & 124	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to g			
125 & 126	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to g			
127 & 128	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
129 & 130	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
131 & 132	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
133 & 134	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
135 & 136	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
137 & 138	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
139 & 140	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
141 & 142	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
143 & 144	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
145 & 146	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
147 & 148	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
149 & 150	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
151 & 152	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
153 & 154	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
155 & 156	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
157 & 158	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
159 & 160	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
161 & 162	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
163 & 164	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
165 & 166	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
167 & 168	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
169 & 170	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
171 & 172	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
173 & 174	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
175 & 176	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
177 & 178	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
179 & 180	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
181 & 182	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
183 & 184	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
185 & 186	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			
187 & 188	GND	"	"				"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	5.0 V	IA to a			

See notes at end of device types 06 and 07.

TABLE III. Group A inspection for device types 06 and 07 - Continued
Terminal conditions (pins not designated may be high ≥ 2.4 V or low ≤ 0.8 V or open).

Subgroup	Symbol	MIL-STD-883 method	Cases E,F test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Test limits		Unit
				B	C	LT	R80	R81	D	A	GND	e	d	c	b	a	g	f	V _{CC}		Min	Max	
10	t _{PHL} T _C = 125°C t _{PLH}	3003 (Fig. 8)	189 & 190	GND	GND	5.0 V	5.0 V	5.0 V	GND	IN	GND					OUT			5.0 V	A to a	8	144	ns
			191 & 192	GND	5.0 V	"	"	"	5.0 V	"	"					OUT			"	A to a	"	"	"
			193 & 194	5.0 V	"	"	"	"	GND	"	"					OUT			"	A to a	"	"	"
			195 & 196	GND	"	"	"	"	"	"	"					OUT			"	A to b	"	"	"
			197 & 198	5.0 V	"	"	"	"	"	"	"					OUT			"	A to b	"	"	"
			199 & 200	5.0 V	GND	"	"	"	"	"	"					OUT			"	A to c	"	"	"
			201 & 202	GND	GND	"	"	"	"	"	"					OUT			"	A to d	"	"	"
			203 & 204	GND	5.0 V	"	"	"	"	"	"					OUT			"	A to d	"	"	"
			205 & 206	5.0 V	5.0 V	"	"	"	"	"	"					OUT			"	A to e	"	"	"
			207 & 208	GND	GND	"	"	"	"	"	"					OUT			"	A to e	"	"	"
			209 & 210	5.0 V	GND	"	"	"	"	"	"					OUT			"	A to f	"	"	"
			211 & 212	GND	GND	"	"	"	"	"	"					OUT			"	A to f	"	"	"
			213 & 214	5.0 V	5.0 V	"	"	"	5.0 V	"	"					OUT			"	A to g	"	"	"
			215 & 216	5.0 V	5.0 V	"	"	"	5.0 V	"	"					OUT			"	A to R80	"	"	"
			217 & 218	GND	GND	"	"	"	GND	"	"					OUT			"	B to a	"	"	"
			219 & 220	IN	GND	"	"	"	5.0 V	GND	"					OUT			"	B to a	"	"	"
			221 & 222	"	GND	"	"	"	5.0 V	GND	"					OUT			"	B to b	"	"	"
			223 & 224	"	5.0 V	"	"	"	GND	"	"					OUT			"	B to b	"	"	"
			225 & 226	"	GND	"	"	"	5.0 V	GND	"					OUT			"	B to b	"	"	"
			227 & 228	"	5.0 V	"	"	"	5.0 V	GND	"					OUT			"	B to b	"	"	"
			229 & 230	"	GND	"	"	"	5.0 V	GND	"					OUT			"	B to b	"	"	"
			231 & 232	"	GND	"	"	"	GND	"	"					OUT			"	B to c	"	"	"
			233 & 234	"	GND	"	"	"	GND	"	"					OUT			"	B to c	"	"	"
			235 & 236	"	5.0 V	"	"	"	5.0 V	"	"					OUT			"	B to d	"	"	"
			237 & 238	"	5.0 V	"	"	"	5.0 V	"	"					OUT			"	B to d	"	"	"
			239 & 240	"	5.0 V	"	"	"	"	GND	"					OUT			"	B to e	"	"	"
			241 & 242	"	GND	"	"	"	"	GND	"					OUT			"	B to f	"	"	"
			243 & 244	"	GND	"	"	"	5.0 V	"	"					OUT			"	B to f	"	"	"
			245 & 246	"	GND	"	"	"	5.0 V	"	"					OUT			"	B to f	"	"	"
			247 & 248	GND	IN	"	"	"	5.0 V	"	"					OUT			"	B to g	"	"	"
			249 & 250	5.0 V	"	"	"	"	5.0 V	"	"					OUT			"	C to a	"	"	"
			251 & 252	GND	"	"	"	"	GND	"	"					OUT			"	C to a	"	"	"
253 & 254	GND	"	"	"	"	GND	"	"					OUT			"	C to c	"	"	"			
255 & 256	GND	"	"	"	"	GND	"	"					OUT			"	C to c	"	"	"			
257 & 258	5.0 V	"	"	"	"	5.0 V	"	"					OUT			"	C to d	"	"	"			
259 & 260	GND	"	"	"	"	GND	"	"					OUT			"	C to e	"	"	"			
261 & 262	5.0 V	5.0 V	"	"	"	5.0 V	"	"					OUT			"	C to f	"	"	"			
263 & 264	GND	5.0 V	"	"	"	IN	"	"					OUT			"	D to a	"	"	"			
265 & 266	"	GND	"	"	"	IN	"	"					OUT			"	D to c	"	"	"			
267 & 268	"	"	"	"	"	GND	"	"					OUT			"	D to g	"	"	"			
269 & 270	"	"	IN	OUT	"	GND	"	"					OUT			"	LT to a	"	"	"			
271 & 272	"	"	5.0 V	IN	5.0 V	"	"	"					OUT			"	LT to R80	"	"	"			
273 & 274	"	"	"	IN	IN	"	"	"					OUT			"	R80 to a	"	"	"			
275 & 276	"	"	"	OUT	OUT	"	"	"					OUT			"	R81 to a	"	"	"			
11	Same tests terminal conditions and limits as for subgroup 10, except T _C = -55°C.																						

1/ X = Input may be high level or low level.

2/ Y = 30 volts for device type 05 and 15 volts for device type 07.

3/ Output voltages shall be either:

(a) H = 2.4 volts minimum and L = 0.4 volts minimum when using high speed checker double comparator, or

(b) H \geq 1.5 volts and L \leq 1.5 volts when using a high speed checker single comparator.

4/ CKT except C.

TABLE III. Group A inspection for device type 08.

Terminal conditions (pins not designated may be high ≥ 2.4 V or low ≤ 0.8 V or open).

Subgroup	Symbol	MIL-STD-883 method	Cases E, F Test no.	Test limits																			
				1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Min	Max	Unit
1 T _C = 25°C	V _{OH1} (See note 1)	3006	1	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	V
			2	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
			3	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
			4	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
			5	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
			6	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
			7	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"
V _{OH2}	3006	8	10.8 V	0.8 V	2.0 V	-12 mA	10.8 V	10.8 V	10.8 V	10.8 V	"	"	"	"	"	"	"	"	"	"	"		
V _{OL1}	3007	9	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
			10	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
			11	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
			12	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
			13	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
			14	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
			15	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
V _{OL2}	3007	16	10.8 V	10.8 V	2.0 V	8 mA	10.8 V	10.8 V	10.8 V	10.8 V	"	"	"	"	"	"	"	"	"	"	"		
V _{IC}			17	-12 mA	-12 mA	-12 mA	-12 mA	-12 mA	-12 mA	-12 mA	"	"	"	"	"	"	"	"	"	"	"		
			18	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
			19	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
			20	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
			21	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
			22	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
I _{IHL1}	3009	23(See note 3)	10.4 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	"	"	"	"	"	"	"	"	"	"	"		
		23 CKT B	10.4 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	"	"	"	"	"	"	"	"	"	"	"		
		24(See note 3)	5.5 V	10.4 V	10.4 V	10.4 V	10.4 V	10.4 V	10.4 V	10.4 V	"	"	"	"	"	"	"	"	"	"	"		
		24 CKT B	5.5 V	10.4 V	10.4 V	10.4 V	10.4 V	10.4 V	10.4 V	10.4 V	"	"	"	"	"	"	"	"	"	"	"		
		25(See note 3)	10.4 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	"	"	"	"	"	"	"	"	"	"	"		
		25 CKT B	10.4 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	"	"	"	"	"	"	"	"	"	"	"		
		26(See note 3)	10.4 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	"	"	"	"	"	"	"	"	"	"	"		
		26 CKT B	10.4 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	"	"	"	"	"	"	"	"	"	"	"		
		27(See note 3)	10.4 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	"	"	"	"	"	"	"	"	"	"	"		
		27 CKT B	10.4 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	"	"	"	"	"	"	"	"	"	"	"		
		28(See note 3)	10.4 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	"	"	"	"	"	"	"	"	"	"	"		
		28 CKT B	10.4 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	15.5 V	"	"	"	"	"	"	"	"	"	"	"		
I _{IHL2}	3009	29	"	"	"	"	10.4 V	"	5.5 V	5.5 V	"	"	"	"	"	"	"	"	"	"	"		
I _{IHL1}	3010	30	2.4 V	GND	GND	GND	GND	GND	GND	GND	"	"	"	"	"	"	"	"	"	"	"		
		31	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
		32	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
		33	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
		34	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
		35	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
I _{IHL2}	3010	36	5.5 V	GND	GND	GND	GND	GND	GND	GND	"	"	"	"	"	"	"	"	"	"	"		
		37	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
		38	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
		39	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
		40	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
		41	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
I _{ILOS}	3011	42	X	X	X	X	X	X	X	X	"	"	"	"	"	"	"	"	"	"	"		
		43	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
		44	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
		45	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
		46	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
		47	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
		48	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
		49	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
I _{ICC}	3005	50	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		

See notes at the end of device type 08.

TABLE III. Group A Inspection for device type 08 - Continued.
Terminal conditions (pins not designated may be high ≥ 2.4 V or low ≤ 0.8 V or open).

Subgroup	Symbol	MIL-STD-883 test method	Cases E.F. test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Measured terminal	Test limits	
				B	C	LT	RBO	RBI	D	A	GND	e	d	c	b	a	g	f	V _{CC}		Min	Max
2	Same tests, terminal conditions and limits as for subgroup 1, except T _C = 125°C and V _{IC} tests are omitted.																					
3	Same tests, terminal conditions and limits as for subgroup 1, except T _C = -55°C and V _{IC} tests are omitted.																					
7 T _C = 25°C	Truth table test		51	GND	GND	5.0 V	5.0 V	GND	GND	GND	GND	H	H	H	H	H	L	L	H	5.0 V	SEE NOTE 2	
			52	GND	"	"	"	"	5.0 V	"	"	L	L	H	H	H	L	L	"	"		
			53	5.0 V	"	"	"	"	GND	"	"	"	"	"	"	"	"	"	"	"		
			54	5.0 V	"	"	"	"	5.0 V	"	"	"	"	"	"	"	"	"	"	"		
			55	GND	5.0 V	"	"	"	GND	"	"	"	"	"	"	"	"	"	"	"		
			56	GND	"	"	"	"	5.0 V	"	"	"	"	"	"	"	"	"	"	"		
			57	5.0 V	"	"	"	"	GND	"	"	"	"	"	"	"	"	"	"	"		
			58	5.0 V	"	"	"	"	5.0 V	"	"	"	"	"	"	"	"	"	"	"		
			59	GND	GND	"	"	"	5.0 V	"	"	"	"	"	"	"	"	"	"	"		
			60	GND	"	"	"	"	5.0 V	"	"	"	"	"	"	"	"	"	"	"		
			61	5.0 V	"	"	"	"	5.0 V	"	"	"	"	"	"	"	"	"	"	"		
			62	5.0 V	"	"	"	"	5.0 V	"	"	"	"	"	"	"	"	"	"	"		
			63	GND	5.0 V	"	"	"	GND	"	"	"	"	"	"	"	"	"	"	"		
			64	GND	"	"	"	"	GND	"	"	"	"	"	"	"	"	"	"	"		
			65	5.0 V	"	"	"	"	5.0 V	"	"	"	"	"	"	"	"	"	"	"		
			66	5.0 V	"	"	"	"	GND	"	"	"	"	"	"	"	"	"	"	"		
			67	X	X	X	GND	"	"	5.0 V	"	"	"	"	"	"	"	"	"	"		
			68	GND	GND	5.0 V	"	"	"	GND	"	"	"	"	"	"	"	"	"	"		
			69	X	X	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"		
8	Truth table test		Same tests as subgroup 7, except T _C = 125°C.																			
			Same tests as subgroup 7, except T _C = -55°C.																			
9 T _C = 25°C	t _{PHL} t _{PLH}	3003 (Fig. 9)	110 & 111	GND	"	"	"	"	5.0 V	"	"	"	"	"	"	"	"	"	"	5.0 V	A to a	ns
			112 & 113	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	A to d	"	
			114 & 115	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	A to e	"	
			116 & 117	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	A to f	"	
			118 & 119	5.0 V	5.0 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"	A to a	"	
			120 & 121	GND	5.0 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"	A to b	"	
			122 & 123	5.0 V	5.0 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"	A to d	"	
			124 & 125	GND	GND	"	"	"	"	"	"	"	"	"	"	"	"	"	"	A to RBO	"	
			126 & 127	IN	"	"	"	"	5.0 V	"	"	"	"	"	"	"	"	"	"	B to c	"	
			128 & 129	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	B to f	"	
			130 & 131	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	B to g	"	
			132 & 133	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	"	B to a	"	
			134 & 135	"	5.0 V	"	"	"	"	"	5.0 V	"	"	"	"	"	"	"	"	B to f	"	
			136 & 137	"	5.0 V	"	"	"	"	"	5.0 V	"	"	"	"	"	"	"	"	B to g	"	
			138 & 139	"	5.0 V	"	"	"	"	"	GND	"	"	"	"	"	"	"	"	B to e	"	
			140 & 141	"	IN	"	"	"	"	"	"	"	"	"	"	"	"	"	"	C to d	"	
			142 & 143	"	IN	"	"	"	"	"	"	"	"	"	"	"	"	"	"	C to a	"	
			144 & 145	"	5.0 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"	D to b	"	
			146 & 147	"	GND	"	"	"	"	"	"	"	"	"	"	"	"	"	"	D to c	"	
			148 & 149	"	5.0 V	"	"	"	"	"	"	"	"	"	"	"	"	"	"	D to a	"	
			150 & 151	"	GND	"	"	"	"	"	"	"	"	"	"	"	"	"	"	LT to c	"	
			152 & 153	"	GND	X	X	"	"	"	"	"	"	"	"	"	"	"	"	LT to a	"	
			154 & 155	"	GND	GND	5.0 V	"	"	"	"	"	"	"	"	"	"	"	"	RBO to a	"	

See notes at end of device type 08.

TABLE III. Group A inspection for device type D8 - Continued.
Terminal conditions (pins not designated may be high ≥ 2.4 V or low ≤ 0.8 V or open).

Subgroup	Symbol	MIL-STD-883 method	Cases E.F. Test no.	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	Test limits		Unit		
				B	C	LT	RBO	RBI	D	A	GND	e	d	c	b	a	g	f	VCC	Measured terminal	Min		Max	
10	t _{PHL} T _C = 125°C t _{PLH}	3003 (Fig. 9)	156 & 157	GND	GND	5.0 V	5.0 V	5.0 V	GND	IN	GND		OUT			OUT			5.0 V	A to a	8	144	ns	
			158 & 159	"	"	"	"	"	"	"	"	"							"	A to d	"	"	"	
			160 & 161	"	"	"	"	"	"	"	"	"								"	A to e	"	"	"
			162 & 163	"	"	"	"	"	"	"	"	"								"	A to f	"	"	"
			164 & 165	5.0 V	5.0 V	"	"	"	"	"	"	"								"	A to a	"	"	"
			166 & 167	GND	5.0 V	"	"	"	"	"	"	"								"	A to b	"	"	"
			168 & 169	5.0 V	5.0 V	"	"	"	"	"	"	"								"	A to d	"	"	"
			170 & 171	GND	GND	"	"	"	"	GND	"	"								"	A to RBO	"	"	"
			172 & 173	IN	"	"	"	"	"	5.0 V	"	"								"	B to c	"	"	"
			174 & 175	"	"	"	"	"	"	"	"	"								"	B to f	"	"	"
			176 & 177	"	"	"	"	"	"	"	"	"								"	B to g	"	"	"
			178 & 179	"	"	"	"	"	"	5.0 V	"	"								"	B to a	"	"	"
			180 & 181	"	5.0 V	"	"	"	GND	5.0 V	"	"								"	B to f	"	"	"
			182 & 183	"	5.0 V	"	"	"	"	5.0 V	"	"								"	B to g	"	"	"
			184 & 185	"	5.0 V	"	"	"	"	"	"	"								"	B to e	"	"	"
			186 & 187	"	IN	"	"	"	"	"	"	"								"	C to d	"	"	"
			188 & 189	5.0 V	IN	"	"	"	"	"	"	"								"	C to b	"	"	"
			190 & 191	"	GND	"	"	"	IN	"	"	IN								"	D to a	"	"	"
			191 & 193	"	GND	"	"	"	"	"	"	IN	"							"	D to b	"	"	"
			194 & 195	"	5.0 V	"	"	"	"	"	"	IN	"							"	D to c	"	"	"
			196 & 197	"	GND	"	"	"	GND	"	"	"	"							"	LT to a	"	"	"
			198 & 199	"	GND	X	X	"	X	X	"	"	"								RBO to a	"	"	"
			200 & 201	"	GND	GND	15.0 V	"	IN	GND	"	"	"								"	RBI to a	"	"
11	Same tests terminal conditions and limits as for subgroup 10, except T _C = -55°C.																							

1/ X = input may be high level or low level (for I_{OS}, input may be GND or 5.5 V).

2/ Output voltages shall be either:

- (a) H = 2.4 volts minimum and L = 0.4 volts maximum when using a high speed checker double comparator, or
(b) H \geq 1.5 volts and L \leq 1.5 volts when using a high speed checker single comparator.

3/ CKT except B.

TABLE III. Group A inspection for device type 09. 1/
Terminal conditions (pins not designated maybe high ≥ 2.4 V or low ≤ 0.8 V or open).

[illegible]

See notes at end of device type 09.

TABLE III. Group A inspection for device type 09 - Continued. 1/
conditions (pins not designated maybe high ≥ 2.4 V or low ≤ 0.8 V or open).

[illegible]

2. Classification requirement are removed for this device type.

\bar{X} = Input may be high or low.

Output voltages shall be either:

- (a) $H = 2.4$ volts minimum and $L = 0.4$ volts maximum when using a high speed checker double comparator, or
- (b) $H = 1.5$ volts and $L \leq 1.5$ volts when using a high speed checker single comparator.

4.3 Qualification inspection. Qualification inspection shall be in accordance with MIL-M-38510. Inspections to be performed shall be those specified in method 5005 of MIL-STD-883 and herein for groups A, B, C, and D inspections (see 4.4.1 through 4.4.4). Qualification inspection is not required for device types 02, 03, and 09.

4.4 Quality conformance inspection. Quality conformance inspection shall be in accordance with MIL-M-38510 and as specified herein. Inspections to be performed shall be those specified in method 5005 of MIL-STD-883 and herein for groups A, B, C, and D inspections (see 4.4.1 through 4.4.4). Generic test data (see 6.6) may be used to satisfy the requirements for groups C and D inspections. Quality conformance inspection shall be completed on the specific devices covered by this specification before they are shipped.

4.4.1 Group A inspection. Group A inspection shall be in accordance with table I of method 5005 of MIL-STD-883 and as follows:

- a. Electrical tests requirements shall be as specified in table II herein.
- b. Subgroups 4, 5, and 6, of table I of method 5005 of MIL-STD-883 shall be omitted.

4.4.2 Group B inspection. Group B inspection shall be in accordance with table II of method 5005 of MIL-STD-883. Electrical test requirements for device types 01, and 03 shall be as specified in table II herein.

4.4.3 Group C inspection. Group C inspection shall be in accordance with table III of method 5005 of MIL-STD-883 and as follows:

- a. End-point electrical parameters shall be as specified in table II herein.
- b. Steady-state life test (method 1005 of MIL-STD-883) conditions:
 - (1) Test condition D or E, using the circuit shown on figure 4, or equivalent.
 - (2) $T_A = +125^\circ\text{C}$ minimum.
 - (3) Test duration: 1,000 hours, except as permitted by method 1005 of MIL-STD-883.

4.4.4 Group D inspection. Group D inspection shall be in accordance with table IV of method 5005 of MIL-STD-883. End-point electrical tests shall be as specified in table II herein.

4.5 Methods of inspection. Methods of inspection shall be specified as follows:

4.5.1 Voltage and current. All voltages given are referenced to the microcircuit ground terminal. Currents given are conventional and positive when flowing into the referenced terminal.

5. PACKAGING

5.1 Packaging requirements. The requirements for packaging shall be in accordance with MIL-M-38510.

6. NOTES

6.1 Intended use. Microcircuits conforming to this specification are intended for original equipment design applications and logistic support of existing equipment. Device types 02, 03, and 09 are intended for use for logistics support of existing equipment.

6.2 Ordering data. The acquisition document should specify the following:

- a. Complete part number (see 1.2).
- b. Requirements for delivery of one copy of the quality conformance inspection data pertinent to the device inspection lot to be supplied with each shipment by the device manufacturer, if applicable.

- c. Requirements for certificate of compliance, if applicable.
- d. Requirements for notification of change of product or process to the contracting activity for device types 02, 03, and 09. In addition to that, notification to the qualifying activity for device types 01, 04, 05, 06, 07, and 08, if applicable.
- e. For device types 01, 04, 05, 06, 07, and 08 requirements for failure analysis (including required test condition of method 5003 of MIL-STD-883), corrective action and reporting of results, if applicable.
- f. Requirements for product assurance options, device types 01, 04, 05, 06, 07, and 08. Requirements for packaging and packing, device types 02, 03, and 09.
- g. Requirements for special carriers, lead lengths, or lead forming, if applicable. These requirements shall not affect the part number. Unless otherwise specified, these requirements shall not apply to direct purchase by or direct shipment to the Government.
- h. Requirements for "JAN" marking. This shall apply to device types 01, 04, 05, 06, 07, and 08 only.

6.3 Abbreviations, symbols, and definitions. The abbreviations, symbols, and definitions used herein are defined in MIL-M-38510, MIL-STD-1331, and as follows:

GND	- - - - -	Ground zero voltage potential.
I _{IN}	- - - - -	Current flowing into an input terminal.
V _{IN}	- - - - -	Voltage level at an input terminal.

6.4 Logistic support. Lead materials and finishes (see 3.3) are interchangeable. Unless otherwise specified, microcircuits acquired for Government logistic support will be acquired to device class B (see 1.2.2), lead material and finish C (see 3.3). Longer length leads and lead forming shall not affect the part number.

6.5 Generic test data. Generic test data may be used to satisfy the requirements of 4.4.3. Group C generic test data shall be on date codes no more than 1 year old and on a die in the same microcircuit group (see appendix E of MIL-M-38510) with the same material, design, and process and from the same plant as the die represented. Group D generic data shall be on date codes no more than 1 year old and on the same package type (see terms, definitions, and symbols of MIL-M-38510) and from the same plant as the package represented. The vendor is required to retain the generic data for a period of not less than 36 months from the date of shipment.

6.6 Substitutability. The cross-reference information below is presented for the convenience of users. Microcircuits covered by this specification will functionally replace the listed generic-industry type. Generic-industry microcircuit types may not have equivalent operational performance characteristics across military temperature ranges or reliability factors equivalent to MIL-M-38510 device types and may have slight physical variations in relation to case size. The presence of this information shall not be deemed as permitting substitution of generic-industry types for MIL-M-38510 types or as a waiver of any of the provisions of MIL-M-38510.

<u>Military device type</u>	<u>Generic-industry type</u>
01	5442
02 5/	5443
03 5/	5444
04	5445
05	54145
06	5446
07	5447
08	5448
09 5/	5449

5/ Qualification requests are removed for this device type.

6.7 Manufacturers' designators. Manufacturers' circuits included in this specification are designated as shown in table IV herein.

TABLE IV. Manufacturers' designations.

Device type	Circuit				
	Texas Instru- ment Incorporated	Signetics Corporation	National Semi- Conductor Corporation	Moto-ola Incorporated	Fairchild
01	D	C	E	B	A
04	C	A	E	B	D
05	C	A	E	B	D
06	C	B	D		
07	C	B	D		
08	B	C	E	A	

6.8 Ordering guidance. Since the qualification and certification requirements have been removed from the specification for device types 02, 03, and 09, orders may be specified immediately.

6.9 Changes from previous issue. Asterisks are not used in this revision to identify changes with respect to the previous issue, due to the extensiveness of the changes.

Custodians:

Army - ER
Navy - EC
Air Force - 17

Review activities:

Army - AR, MI
Navy - OS, SH, TD
Air Force - 11, 19, 85, 99
DLA - ES

User activities:

Army - SM
Navy - AS, CG, MC

Preparing activity:
Air Force - 17

Agent:
DLA - ES

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