



SBOS333B – JULY 2005 – REVISED OCTOBER 2005

## Precision, Gain of 0.2 Level Translation DIFFERENCE AMPLIFIER

### FEATURES

- GAIN OF 0.2 TO INTERFACE  $\pm 10\text{V}$  SIGNALS TO SINGLE-SUPPLY ADCs
- GAIN ACCURACY:  $\pm 0.024\%$  (max)
- WIDE BANDWIDTH: 1.5MHz
- HIGH SLEW RATE:  $15\text{V}/\mu\text{s}$
- LOW OFFSET VOLTAGE:  $\pm 100\mu\text{V}$
- LOW OFFSET DRIFT:  $\pm 1.5\mu\text{V}/^\circ\text{C}$
- SINGLE-SUPPLY OPERATION DOWN TO 1.8V

### APPLICATIONS

- INDUSTRIAL PROCESS CONTROLS
- INSTRUMENTATION
- DIFFERENTIAL TO SINGLE-ENDED CONVERSION
- AUDIO LINE RECEIVERS

### DESCRIPTION

The INA159 is a high slew rate,  $G = 1/5$  difference amplifier consisting of a precision op amp with a precision resistor network. The gain of  $1/5$  makes the INA159 useful to couple  $\pm 10\text{V}$  signals to single-supply analog-to-digital converters (ADCs), particularly those operating on a single  $+5\text{V}$  supply. The on-chip resistors are laser-trimmed for accurate gain and high common-mode rejection. Excellent temperature coefficient of resistance (TCR) tracking of the resistors maintains gain accuracy and common-mode rejection over temperature. The input common-mode voltage range extends beyond the positive and negative supply rails. It operates on a total of  $+1.8\text{V}$  to  $+5.5\text{V}$  single or split supplies. The INA159 reference input uses two resistors for easy mid-supply or reference biasing.

The difference amplifier is the foundation of many commonly-used circuits. The INA159 provides this circuit function without using an expensive external precision resistor network. The INA159 is available in an MSOP-8 surface-mount package and is specified for operation over the extended industrial temperature range,  $-40^\circ\text{C}$  to  $+125^\circ\text{C}$ .

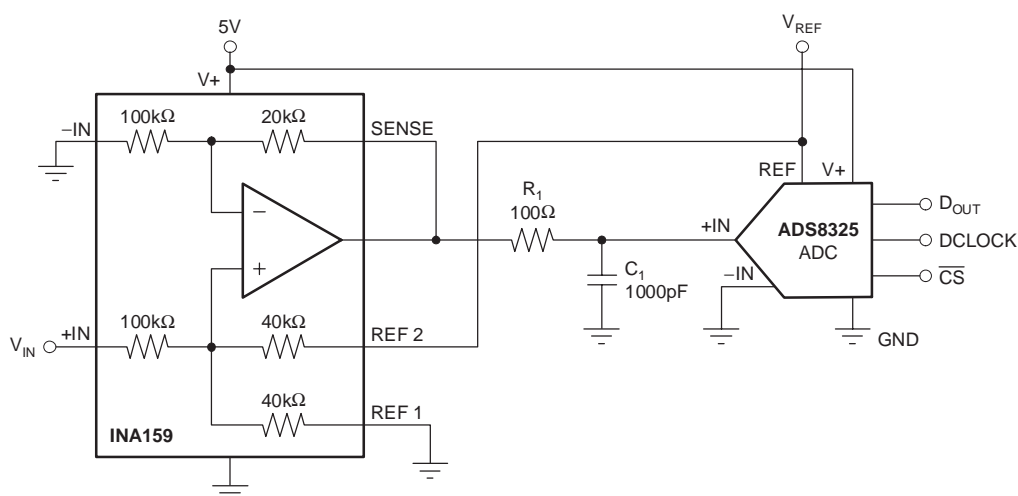


Figure 1. Typical Application



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

All trademarks are the property of their respective owners.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

**ABSOLUTE MAXIMUM RATINGS<sup>(1)</sup>**

Supply Voltage .....	+5.5V
Signal Input Terminals (–IN and +IN), Voltage .....	±30V
Reference (REF 1 and REF2) and Sense Pins	
Current .....	±10mA
Voltage .....	(V–) – 0.5V to (V+) + 0.5V
Output Short Circuit .....	Continuous
Operating Temperature .....	–40°C to +150°C
Storage Temperature .....	–65°C to +150°C
Junction Temperature .....	+150°C
ESD Rating	
Human Body Model .....	4000V
Charged Device Model .....	1000V

(1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those specified is not supported.



This integrated circuit can be damaged by ESD. Texas Instruments recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

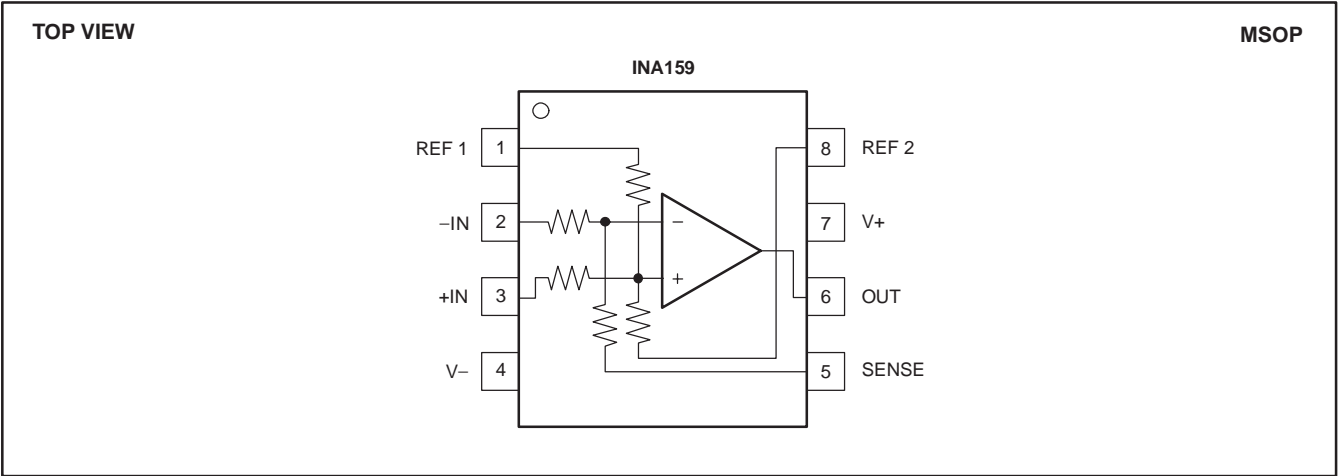
ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

**ORDERING INFORMATION<sup>(1)</sup>**

PRODUCT	PACKAGE-LEAD	PACKAGE DESIGNATOR	PACKAGE MARKING
INA159	MSOP-8	DGK	CJB

(1) For the most current package and ordering information, see the Package Option Addendum at the end of this document, or see the TI web site at [www.ti.com](http://www.ti.com).

**PIN CONFIGURATIONS**



**ELECTRICAL CHARACTERISTICS:  $V_S = +5V$** 

**Boldface** limits apply over the specified temperature range,  $T_A = -40^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$ .

At  $T_A = +25^{\circ}\text{C}$ ,  $R_L = 10\text{k}\Omega$  connected to  $V_S/2$ , REF pin 1 connected to ground, and REF pin 2 connected to  $V_{REF} = 5V$ , unless otherwise noted.

PARAMETER	CONDITIONS	INA159			UNIT
		MIN	TYP	MAX	
<b>OFFSET VOLTAGE<sup>(1)</sup></b> Initial (1) $V_{OS}$ <b>vs Temperature</b> vs Power Supply PSRR Reference Divider Accuracy <sup>(2)</sup> <b>over Temperature</b>	RTO $V_S = \pm 2.5V$ , Reference and Input Pins Grounded  $V_S = \pm 0.9V$ to $\pm 2.75V$		$\pm 100$ <b><math>\pm 1.5</math></b> $\pm 20$ $\pm 0.002$ <b><math>\pm 0.002</math></b>	$\pm 500$  $\pm 100$ $\pm 0.024$	$\mu V$ $\mu V/^{\circ}\text{C}$ $\mu V/V$ % %
<b>INPUT IMPEDANCE<sup>(3)</sup></b> Differential Common-Mode			240 60		$\text{k}\Omega$ $\text{k}\Omega$
<b>INPUT VOLTAGE RANGE</b> Common-Mode Voltage Range $V_{CM}$ Positive Negative Common-Mode Rejection Ratio CMRR <b>over Temperature</b>	RTI    $V_{CM} = -10V$ to $+10V$ , $R_S = 0\Omega$	80	17.5 -12.5 96 <b>94</b>		V V dB <b>dB</b>
<b>OUTPUT VOLTAGE NOISE<sup>(4)</sup></b> $f = 0.1\text{Hz}$ to $10\text{Hz}$ $f = 10\text{kHz}$	RTO		10 30		$\mu V_{PP}$ $\text{nV}/\sqrt{\text{Hz}}$
<b>GAIN</b> Initial Error <b>vs Temperature</b> Nonlinearity	$V_{REF2} = 4.096V$ , $R_L$ Connected to GND, ( $V_{IN+}$ ) - ( $V_{IN-}$ ) = $-10V$ to $+10V$ , $V_{CM} = 0V$  G		0.2 $\pm 0.005$ <b><math>\pm 1</math></b> $\pm 0.0002$	$\pm 0.024$	V/V % <b>ppm/^{\circ}\text{C}</b> % of FS
<b>OUTPUT</b> Voltage, Positive Voltage, Negative Current Limit, Continuous to Common Capacitive Load Open-Loop Output Impedance $R_O$	$V_{REF2} = 4.096V$ , $R_L$ Connected to GND $V_{REF2} = 4.096V$ , $R_L$ Connected to GND   $f = 1\text{MHz}$ , $I_O = 0$	(V+) - 0.1 (V-) + 0.048  See Typical Characteristic	(V+) - 0.02 (V-) + 0.01 $\pm 60$ 110		V V mA pF $\Omega$
<b>FREQUENCY RESPONSE</b> Small-Signal Bandwidth Slew Rate SR Settling Time, 0.01% $t_S$ Overload Recovery Time	-3dB   4V Output Step, $C_L = 100\text{pF}$ 50% Overdrive		1.5 15 1 250		MHz V/ $\mu\text{s}$ $\mu\text{s}$ ns
<b>POWER SUPPLY</b> Specified Voltage Range $V_S$ Operating Voltage Range Quiescent Current $I_Q$	   $I_Q = 0\text{mA}$ , $V_S = \pm 2.5V$ , Reference and Input Pins Grounded	+1.8	+5  1.1	+5.5  1.5	V V mA
<b>TEMPERATURE RANGE</b> Specified Range Operating Range Storage Range Thermal Resistance $\theta_{JA}$ MSOP-8	   Surface-Mount	-40 -40 -65		+125 +150 +150	$^{\circ}\text{C}$ $^{\circ}\text{C}$ $^{\circ}\text{C}$ $^{\circ}\text{C}/\text{W}$

(1) Includes effects of amplifier input bias and offset currents.

(2) Reference divider accuracy specifies the match between the reference divider resistors using the configuration in Figure 2.

(3) Internal resistors are ratio matched but have  $\pm 20\%$  absolute value.

(4) Includes effects of amplifier input current noise and thermal noise contribution of resistor network.

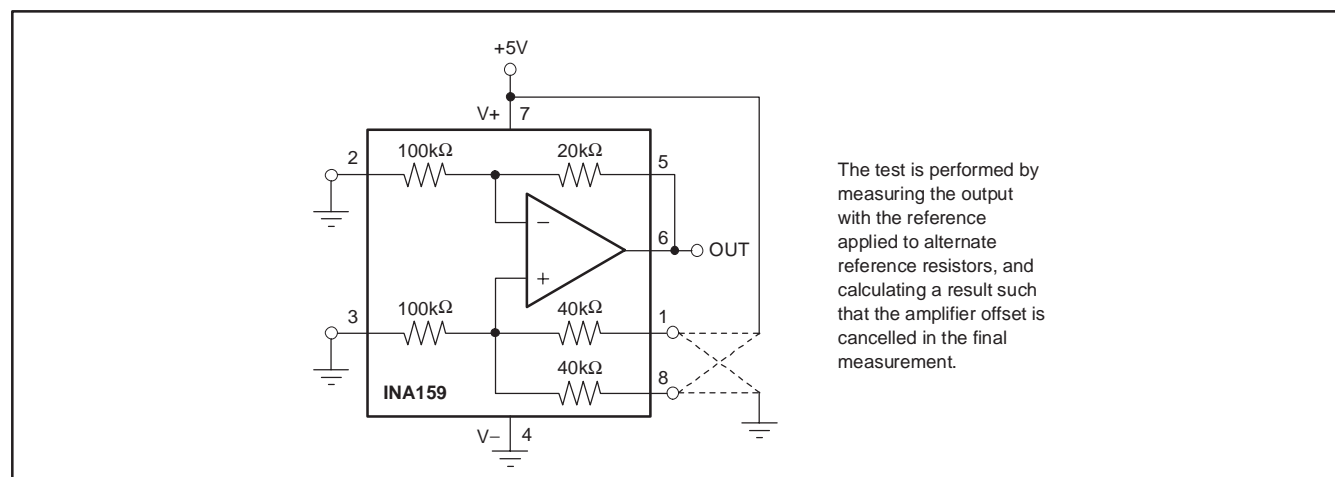
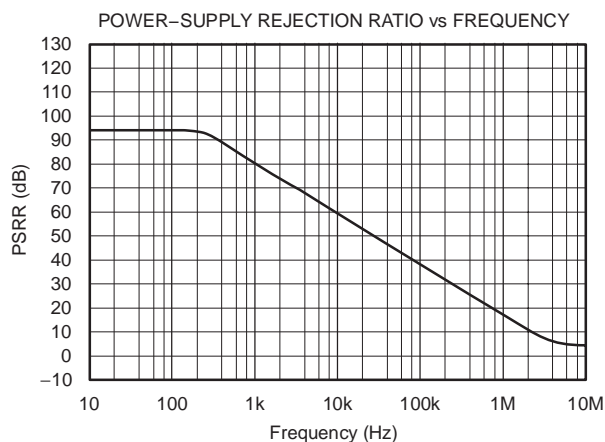
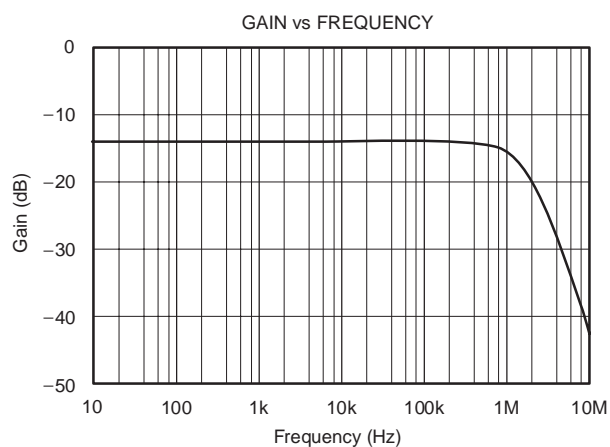
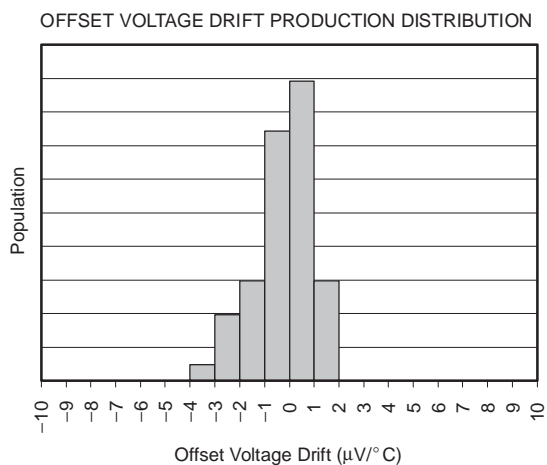
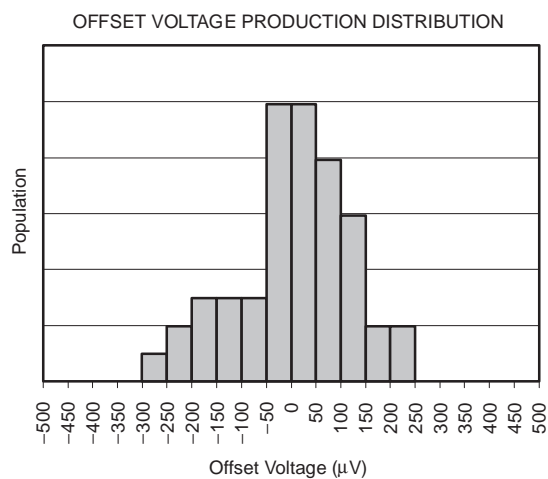


Figure 2. Test Circuit for Reference Divider Accuracy

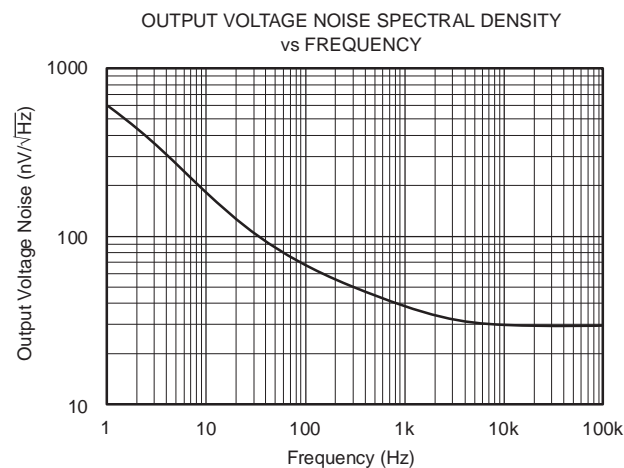
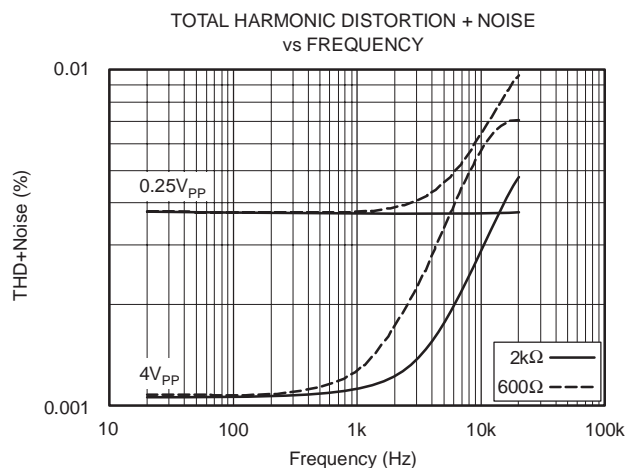
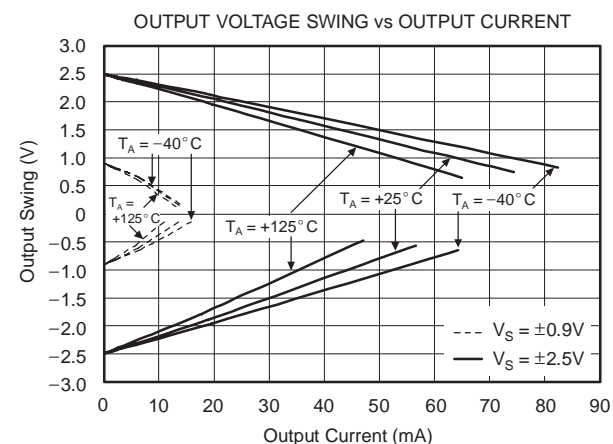
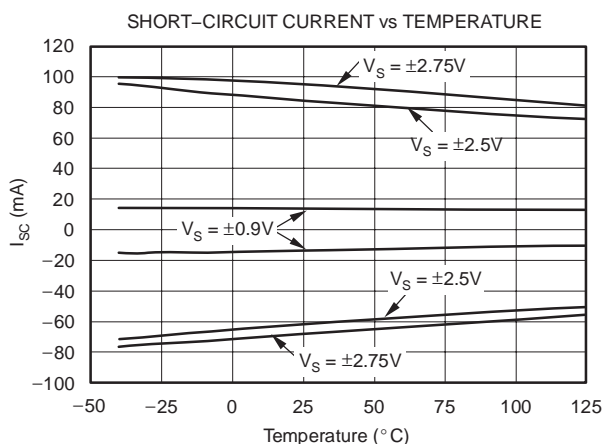
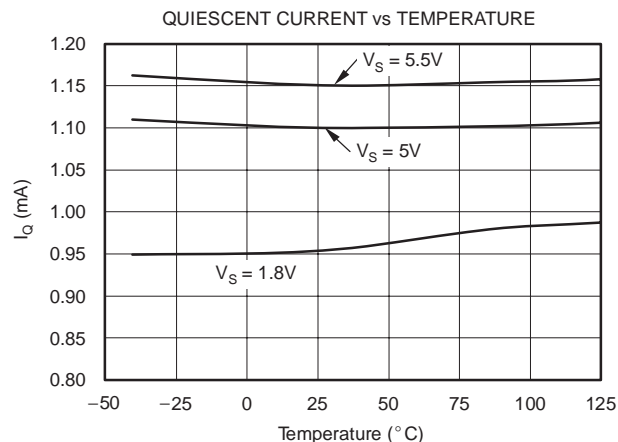
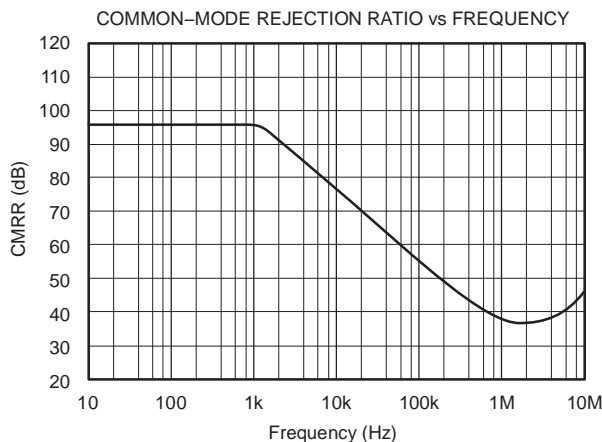
## TYPICAL CHARACTERISTICS

At  $T_A = +25^\circ\text{C}$ ,  $R_L = 10\text{k}\Omega$  connected to  $V_S/2$ , REF pin 1 connected to ground, and REF pin 2 connected to  $V_{REF} = 5\text{V}$ , unless otherwise noted.



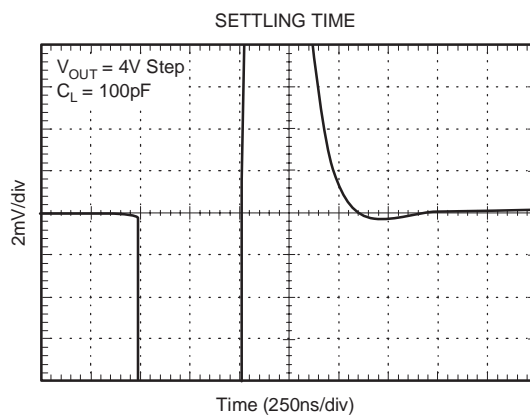
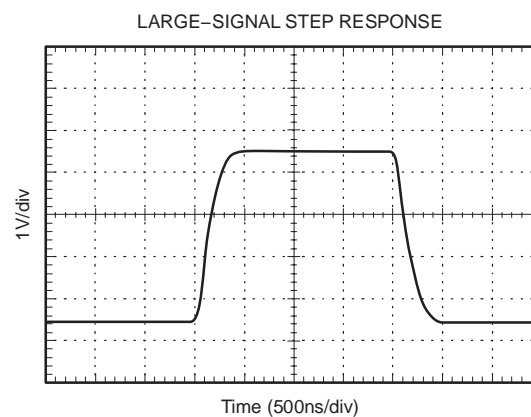
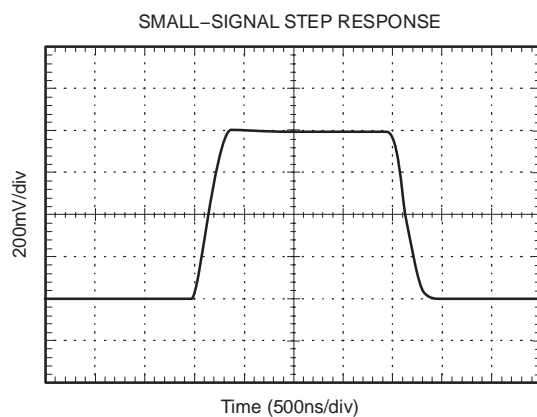
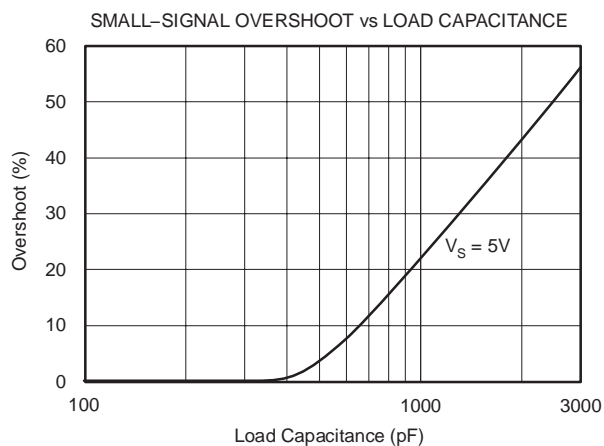
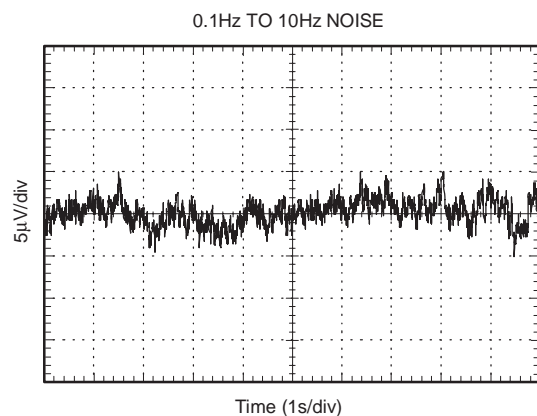
## TYPICAL CHARACTERISTICS (continued)

At  $T_A = +25^\circ\text{C}$ ,  $R_L = 10\text{k}\Omega$  connected to  $V_S/2$ , REF pin 1 connected to ground, and REF pin 2 connected to  $V_{\text{REF}} = 5\text{V}$ , unless otherwise noted.



## TYPICAL CHARACTERISTICS (continued)

At  $T_A = +25^\circ\text{C}$ ,  $R_L = 10\text{k}\Omega$  connected to  $V_S/2$ , REF pin 1 connected to ground, and REF pin 2 connected to  $V_{REF} = 5\text{V}$ , unless otherwise noted.



## APPLICATION INFORMATION

The internal op amp of the INA159 has a rail-to-rail common-mode voltage capability at its inputs. A rail-to-rail op amp allows the use of  $\pm 10\text{V}$  inputs into a circuit biased to 1/2 of a 5V reference (2.5V quiescent output). The inputs to the op amp will swing from approximately 400mV to 3.75V in this application.

The unique input topology of the INA159 eliminates the input offset transition region typical of most rail-to-rail complementary stage operational amplifiers. This allows the INA159 to provide superior glitch- and transition-free performance over the entire common-mode range.

Good layout practice includes the use of a 0.1 $\mu\text{F}$  bypass capacitor placed closely across the supply pins.

### COMMON-MODE RANGE

The common-mode range of the INA159 is a function of supply voltage and reference. Where both pins, REF1 and REF2, are connected together:

$$V_{\text{CM}+} = (V+) + 5[(V+) - V_{\text{REF}}] \quad (1)$$

$$V_{\text{CM}-} = (V-) - 5[V_{\text{REF}} - (V-)] \quad (2)$$

Where one REF pin is connected to the reference, and the other pin grounded (1/2 reference connection):

$$V_{\text{CM}+} = (V+) + 5[(V+) - (0.5V_{\text{REF}})] \quad (3)$$

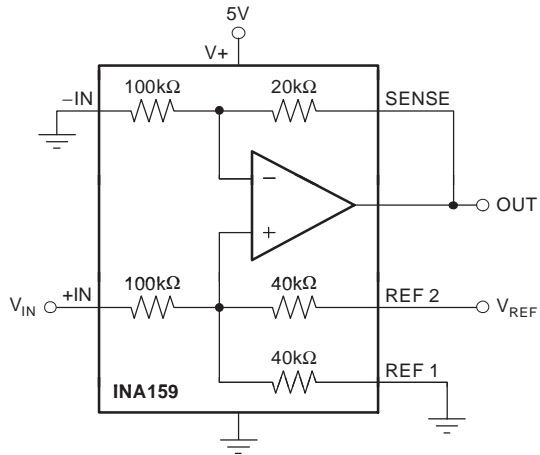
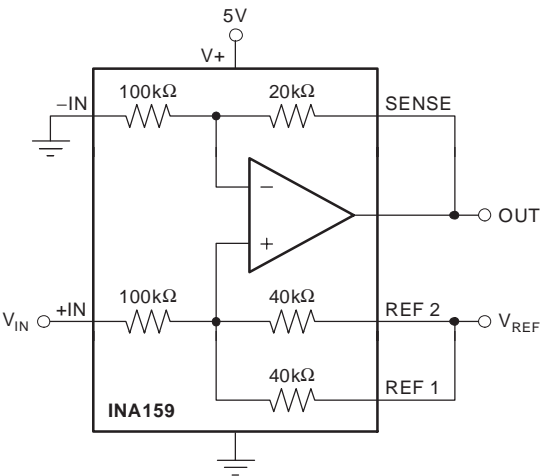
$$V_{\text{CM}-} = (V-) - 5[(0.5V_{\text{REF}}) - (V-)] \quad (4)$$

Some typical values are shown in Table 1.

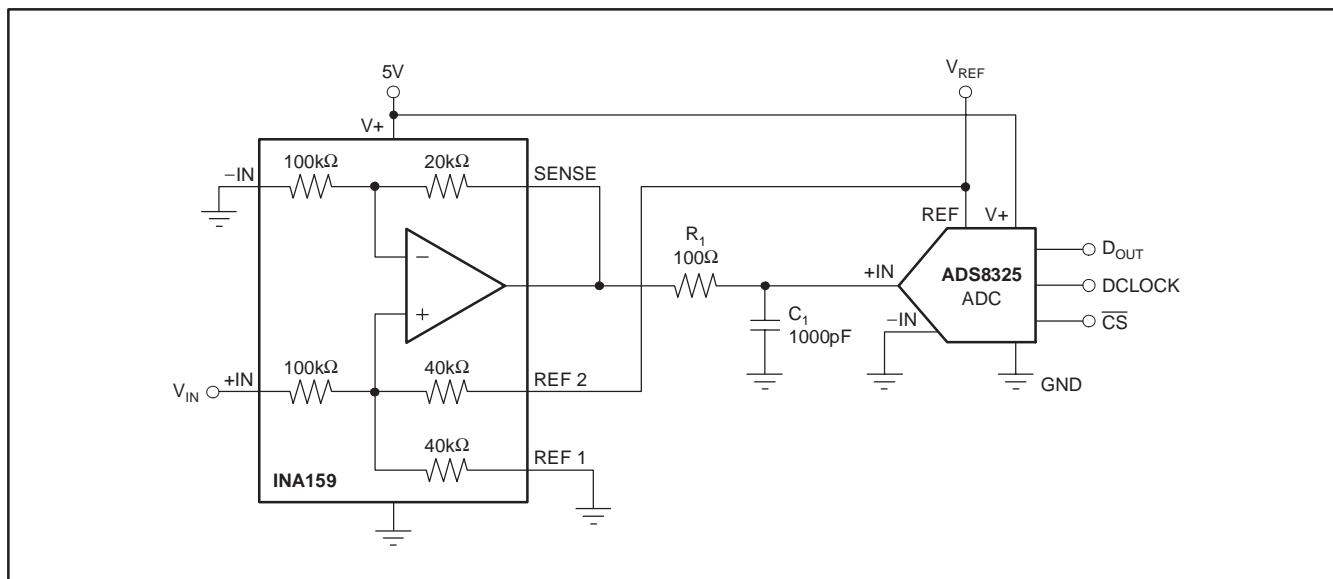
**Table 1. Common-Mode Range For Various Supply and Reference Voltages**

REF 1 and REF 2 Connected Together				
V+	V-	V <sub>REF</sub>	V <sub>CM+</sub>	V <sub>CM-</sub>
5	0	3	15	-15
5	0	2.5	17.5	-12.5
5	0	1.25	23.75	-6.25
1/2 Reference Connection				
V+	V-	V <sub>REF</sub>	V <sub>CM+</sub>	V <sub>CM-</sub>
5	0	5	17.5	-12.5
5	0	4.096	19.76	-10.24
5	0	2.5	23.75	-6.25
3.3	0	3.3	11.55	-8.25
3.3	0	2.5	13.55	-6.25
3.3	0	1.25	16.675	-3.125

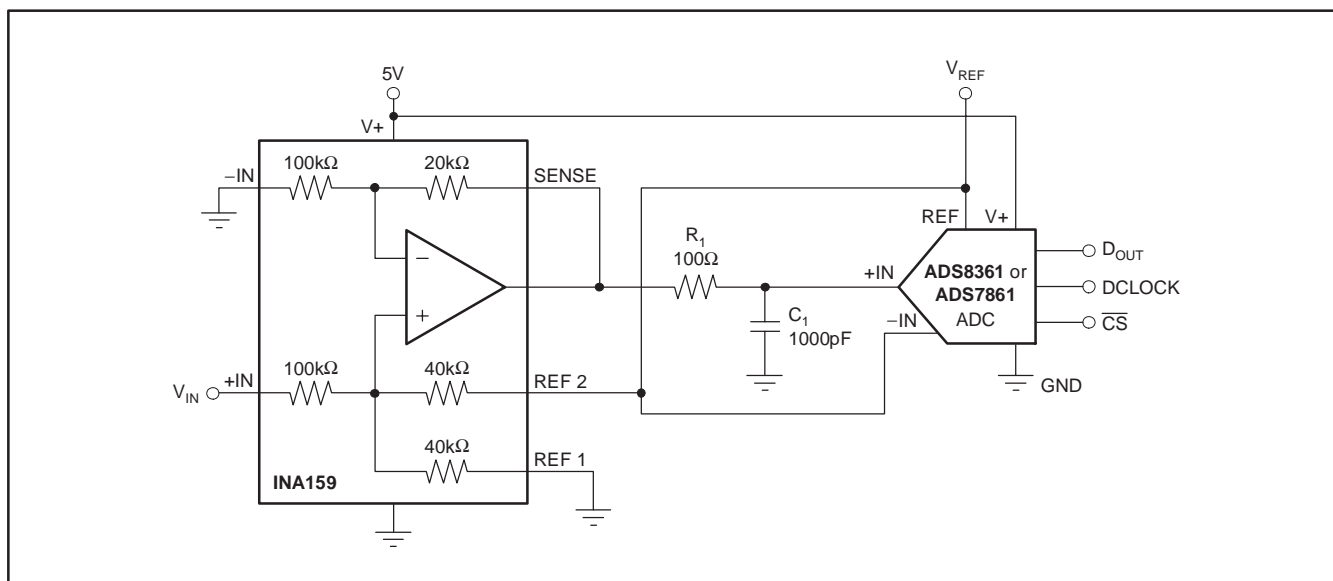
**Table 2. Input and Output Relationships for Various Reference and Connection Combinations**

$V_{REF}$ (V)	REF CONNECTION	$V_{OUT}$ for $V_{IN} = 0$ (V)	LINEAR $V_{IN}$ RANGE (V)	USEFUL $V_{OUT}$ SWING (V)
5		2.5	+10 0 -10	4.5 ( $\pm 2V$ swing) 0.5
4.096		2.048	+10 0 -10	4.048 ( $\pm 2V$ swing) 0.048
3.3		1.65	+10 0 -7.885	3.65 (-1.577V, +2V swing) 0.048
2.5		1.25	+10 (also +5) 0 -6 (also -5)	3.25 (-1.2V, +2V swing) 0.048
1.8		0.9	+10 0 -4.26	2.9 (-0.852V, +2V swing) 0.048
2.5		2.5	+10 0 -10	4.5 ( $\pm 2V$ swing) 0.5
1.8		1.8	+10 0 -8.76	3.8 (-1.752V, +2V swing) 0.048
1.2		1.2	+10 0 -5.76	3.2 (-1.15V, +2V swing) 0.048

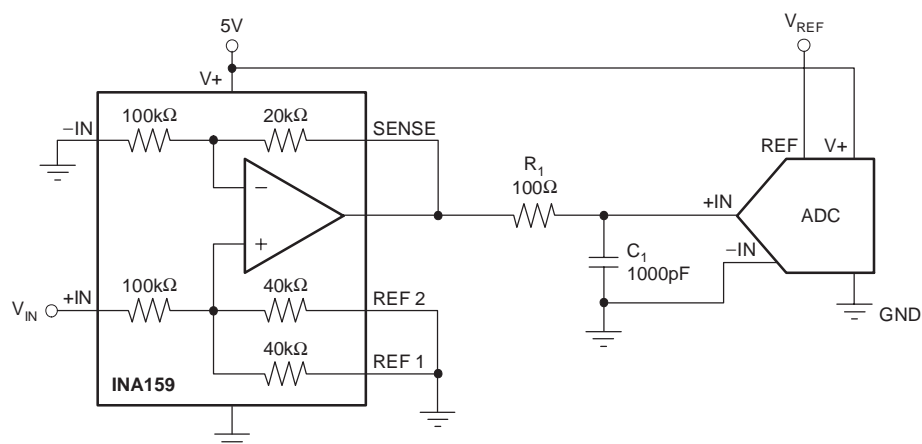




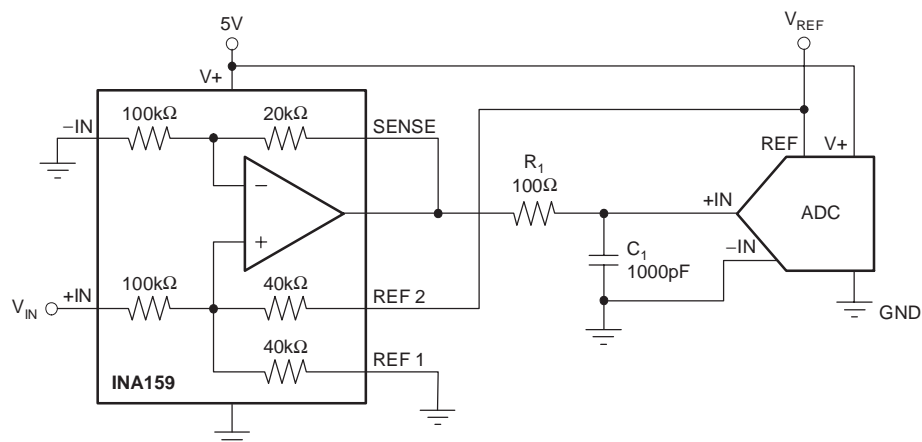
**Figure 3. Typical Application Circuit Interfacing to Medium-Speed, Single-Supply ADCs**



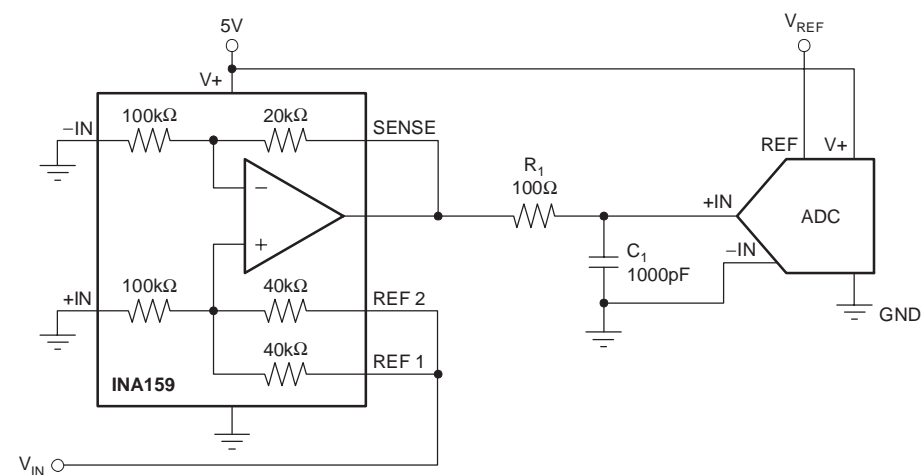
**Figure 4. Typical Application Circuit Interfacing to Medium-Speed, Single-Supply ADCs with Pseudo-Differential Inputs (such as the ADS7861 and ADS8361)**



a) Unipolar, Noninverting,  $G = 0.2$

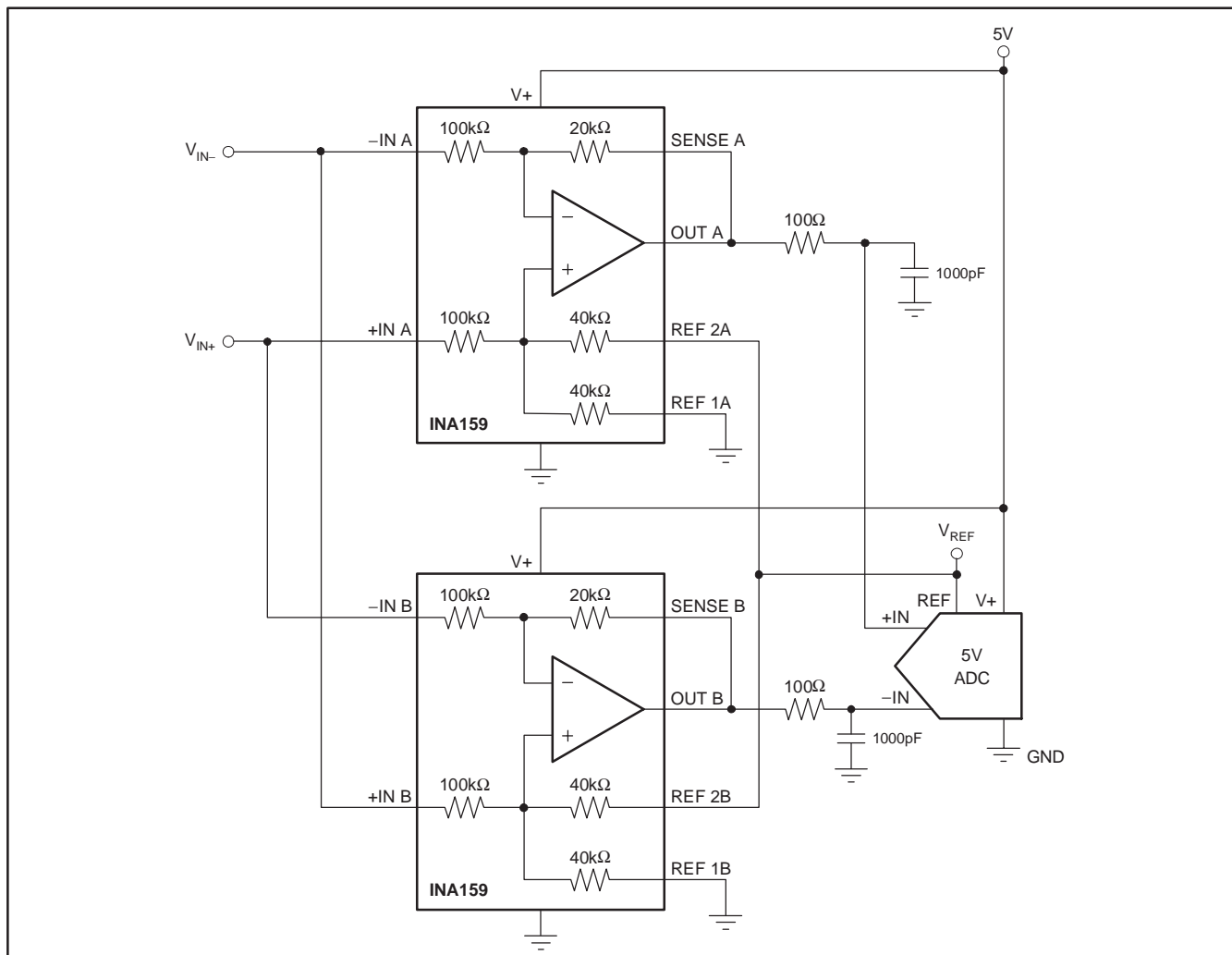


b) Bipolar, Noninverting,  $G = 0.2$



c) Unipolar, Unity Gain

Figure 5. Basic INA159 Configurations



**Figure 6. Differential ADC Drive**

## PACKAGING INFORMATION

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/Ball Finish	MSL Peak Temp <sup>(3)</sup>
INA159AIDGKR	ACTIVE	MSOP	DGK	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
INA159AIDGKT	ACTIVE	MSOP	DGK	8	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM
INA159AIDGKTG4	ACTIVE	MSOP	DGK	8	250	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBsolete:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS) or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

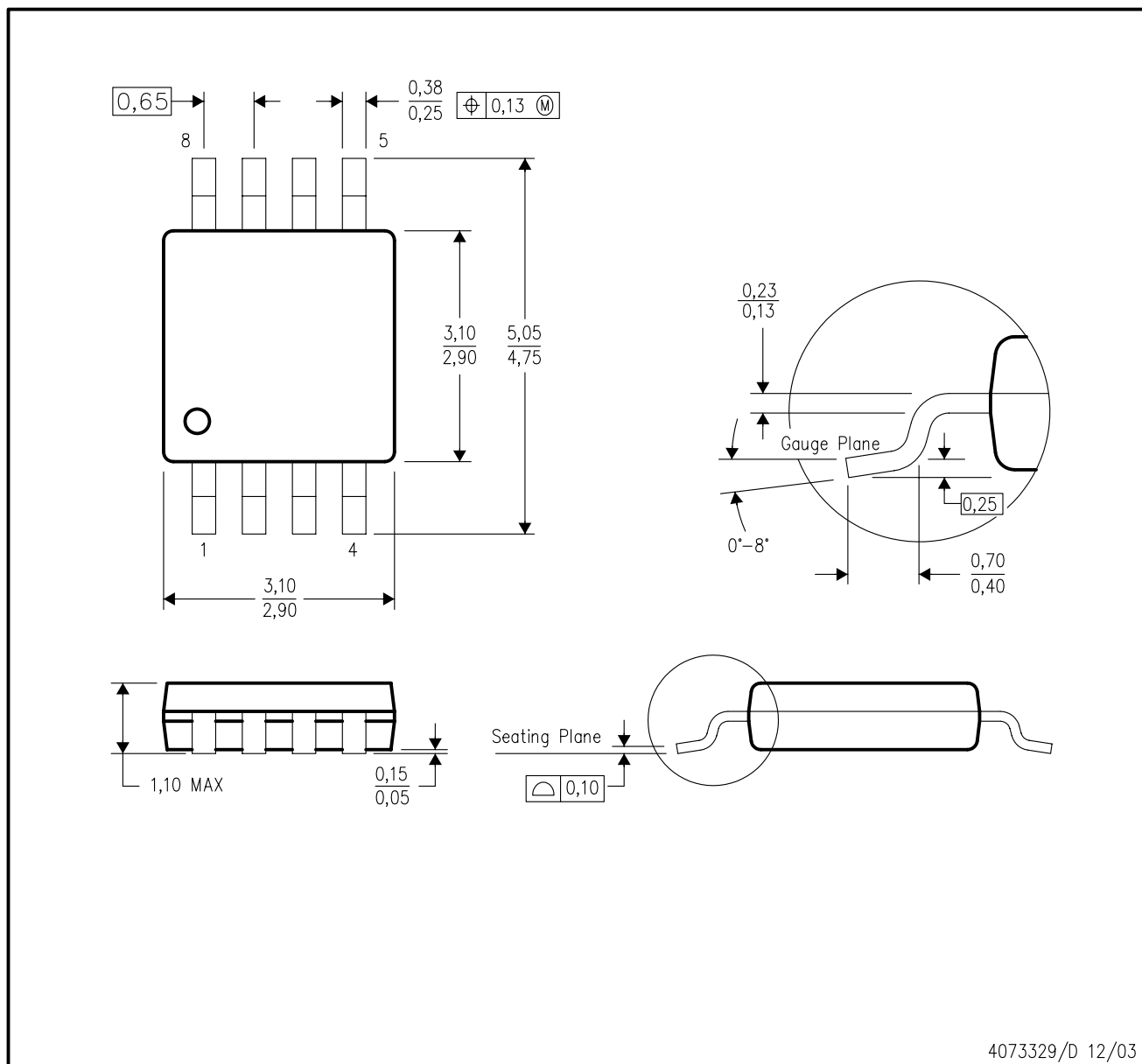
<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

## DGK (S-PDSO-G8)

## PLASTIC SMALL-OUTLINE PACKAGE



- NOTES:
- A. All linear dimensions are in millimeters.
  - B. This drawing is subject to change without notice.
  - C. Body dimensions do not include mold flash or protrusion.
  - D. Falls within JEDEC MO-187 variation AA.

## IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

<b>Products</b>		<b>Applications</b>	
Amplifiers	<a href="http://amplifier.ti.com">amplifier.ti.com</a>	Audio	<a href="http://www.ti.com/audio">www.ti.com/audio</a>
Data Converters	<a href="http://dataconverter.ti.com">dataconverter.ti.com</a>	Automotive	<a href="http://www.ti.com/automotive">www.ti.com/automotive</a>
DSP	<a href="http://dsp.ti.com">dsp.ti.com</a>	Broadband	<a href="http://www.ti.com/broadband">www.ti.com/broadband</a>
Interface	<a href="http://interface.ti.com">interface.ti.com</a>	Digital Control	<a href="http://www.ti.com/digitalcontrol">www.ti.com/digitalcontrol</a>
Logic	<a href="http://logic.ti.com">logic.ti.com</a>	Military	<a href="http://www.ti.com/military">www.ti.com/military</a>
Power Mgmt	<a href="http://power.ti.com">power.ti.com</a>	Optical Networking	<a href="http://www.ti.com/opticalnetwork">www.ti.com/opticalnetwork</a>
Microcontrollers	<a href="http://microcontroller.ti.com">microcontroller.ti.com</a>	Security	<a href="http://www.ti.com/security">www.ti.com/security</a>
		Telephony	<a href="http://www.ti.com/telephony">www.ti.com/telephony</a>
		Video & Imaging	<a href="http://www.ti.com/video">www.ti.com/video</a>
		Wireless	<a href="http://www.ti.com/wireless">www.ti.com/wireless</a>

Mailing Address: Texas Instruments  
Post Office Box 655303 Dallas, Texas 75265

Copyright © 2005, Texas Instruments Incorporated